

# MOS FIELD EFFECT TRANSISTOR 2SK3359

# SWITCHING N-CHANNEL POWER MOS FET INDUSTRIAL USE

#### **DESCRIPTION**

The 2SK3359 is N-Channel MOS Field Effect Transistor designed for high current switching applications.

#### **FEATURES**

- · Low on-state resistance
  - $R_{DS(on)1} = 20 \text{ m}\Omega$  MAX. (Vgs = 10 V, ID = 35 A)
- ★ RDS(on)2 =  $28 \text{ m}\Omega$  MAX. (VGS = 4.5 V, ID = 30 A)
- ★ Low Ciss: Ciss = 4900 pF TYP.
  - Built-in gate protection diode

#### **ORDERING INFORMATION**

PART NUMBER	PACKAGE
2SK3359	TO-220AB
2SK3359-S	TO-262
2SK3359-Z	TO-220SMD

(TO-220AB)

#### ABSOLUTE MAXIMUM RATINGS (TA = 25 °C)

Drain to Source Voltage (Vgs = 0 V)	VDSS	100	V
Gate to Source Voltage (Vps = 0 V)	VGSS(AC)	±20	V
Gate to Source Voltage (Vps = 0 V)	VGSS(DC)	+20, -10	V
Drain Current (DC)	ID(DC)	±70	Α
Drain Current (Pulse) Note1	ID(pulse)	±280	Α
Total Power Dissipation (Tc = 25°C)	Рт	100	W
Total Power Dissipation (T <sub>A</sub> = 25°C)	Рт	1.5	W
Channel Temperature	Tch	150	°C
Storage Temperature	Tstg	-55 to +150	°C
Single Avalanche Current Note2	las	50	Α
Single Avalanche Energy Note2	Eas	250	mJ

**Notes 1.** PW  $\leq$  10  $\mu$ s, Duty cycle  $\leq$  1 %

2. Starting Tch = 25 °C, Rg = 25  $\Omega$ , Vgs = 20 V  $\rightarrow$  0 V



(TO-262)



(TO-220SMD)



#### THERMAL RESISTANCE

Channel to Case	Rth(ch-C)	1.25	°C/W
Channel to Ambient	Rth(ch-A)	83.3	°C/W



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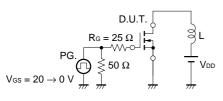
Not all devices/types available in every country. Please check with local NEC representative for availability and additional information.

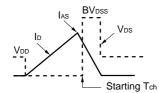


## 章语CTRICATOCHARACTERISTICS (TA = 25 °C)

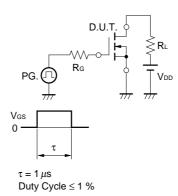
	CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
	Drain to Source On-state Resistance	RDS(on)1	Vgs = 10 V, Ip = 35 A		14	20	mΩ
*		RDS(on)2	$V_{GS} = 4.5  V,  I_{D} = 30  A$		19	28	mΩ
*	Gate to Source Cut-off Voltage	V <sub>GS(off)</sub>	$V_{DS} = 10 \text{ V}, \text{ ID} = 250 \mu\text{A}$	1.5	2.0	2.5	V
	Forward Transfer Admittance	yfs	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 35 A	23	47		S
	Drain Leakage Current	Ioss	$V_{DS} = 100  V$ , $V_{GS} = 0  V$			10	μΑ
	Gate to Source Leakage Current	Igss	$V_{GS} = \pm 20  V,  V_{DS} = 0  V$			±10	μΑ
*	Input Capacitance	Ciss	V <sub>DS</sub> = 10 V		4900		pF
	Output Capacitance	Coss	V <sub>G</sub> S = 0 V		990		рF
	Reverse Transfer Capacitance	Crss	f = 1 MHz		580		рF
	Turn-on Delay Time	td(on)	ID = 35 A		58		ns
	Rise Time	tr	$V_{GS(on)} = 10 V$		400		ns
	Turn-off Delay Time	td(off)	$V_{DD} = 50 V$		340		ns
	Fall Time	tf	$R_G = 10 \Omega$		340		ns
	Total Gate Charge	Q <sub>G</sub>	I <sub>D</sub> = 70 A		130		nC
*	Gate to Source Charge	Qgs	$V_{DD} = 80 V$		14		nC
*	Gate to Drain Charge	Q <sub>GD</sub>	$V_{GS(on)} = 10 \text{ V}$		50		nC
	Body Diode Forward Voltage	VF(S-D)	IF = 70 A, VGS = 0 V		1.0		V
	Reverse Recovery Time	trr	IF = 70 A, VGS = 0 V		170		ns
	Reverse Recovery Charge	Qrr	$di/dt = 100 A/\mu s$		920		nC

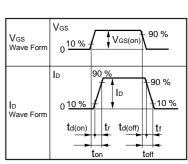
#### **TEST CIRCUIT 1 AVALANCHE CAPABILITY**





#### **TEST CIRCUIT 2 SWITCHING TIME**





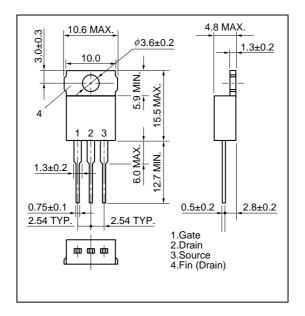
#### **TEST CIRCUIT 3 GATE CHARGE**

$$\begin{array}{c|c} D.U.T. \\ \hline \\ IG = 2 \text{ mA} \\ \hline \\ PG. \\ \hline \\ \end{array}$$

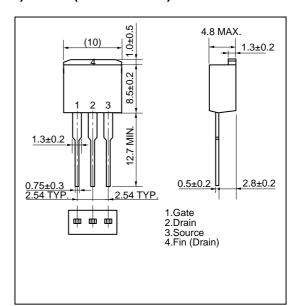


### 章AOKAGO BRAWINGS (Unit:mm)

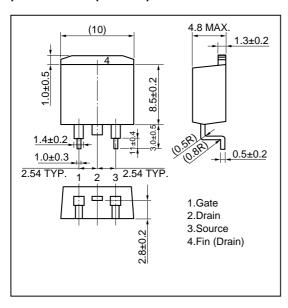
#### 1)TO-220AB (MP-25)



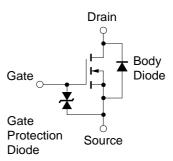
#### 2)TO-262 (MP-25 Fin Cut)



#### 3)TO-220SMD (MP-25Z)



#### **EQUIVALENT CIRCUIT**



**Remark** The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

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