# **MORNSUN**

# A XD-1W & B XD-1W Series 1W,FIXED INPUT,1000V ISOLATED & UNREGULATED **DUAL/SINGLE OUTPUT DC-DC CONVERTER**



multi-country patent protection RoHS

FEA	TU	RE	S

- High Efficiency up to 80%
- DIP Package
- 1KVDC Isolation
- Temperature Range: -40°C ~ +85°C
- No Heat sink Required
- No External Component Required
- Internal SMD Construction
- Industry standard pinout
- RoHS Compliance
- Compatible with "DCP01" Series

## **APPLICATIONS**

The A\_XD-1W & B\_XD-1W Series are specially designed for applications where a group of polar power supplies are isolated from the input power supply in a distributed power supply system on a circuit board. These products apply to:

- 1) Where the voltage of the input power
- supply is fixed (voltage variation  $\leq \pm 10\%$ ); 2) Where isolation is necessary between input and output (isolation voltage ≤1000VDC);
- 3) Where the regulation of the output voltage and the output ripple noise are not demanding.

Such as: purely digital circuits, ordinary low frequency analog circuits, and IGBT power device driving circuits.

# MODEL SELECTION

.dzsc.com

<u>A0505XD-1W</u>	
	Rated Power
	— Package Style
	Output Voltage
	— Input Voltage

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PRODUCT	1				and the second			
Part	Input		Output			Efficiency	2.00	
Number	Voltage (VDC)		Voltage	Current (mA)		(%, Typ.)	Certificate	
	Nominal	Range	(VDC)	Max.	Min.			
B0505XD-1W	6417		5	200	20	70	UL CE	
B0509XD-1W		/	9	111	12	78	UL CE	
B0512XD-1W			12	83	9	78	UL CE	
B0515XD-1W	5	4.5-5.5	15	67	7	80	UL CE	
A0505XD-1W	J	4.5-5.5	±5	±100	±10	72	UL	
A0509XD-1W			±9	±56	±6	77	UL	
A0512XD-1W			±12	±42	±5	79	UL	
A0515XD-1W			±15	±33	±4	80	UL	
B1205XD-1W		10.8-13.2	5	200	20	71	UL CE	
B1209XD-1W	50		9	111	12	76	UL CE	
B1212XD-1W			12	83	9	78	UL CE	
B1215XD-1W	12		15	67	7	79	UL CE	
A1205XD-1W	12		±5	±100	±10	72	UL	
A1209XD-1W			±9	±56	±6	78	UL	
A1212XD-1W			±12	±42	±5	79	UL	
A1215XD-1W			±15	±33	±4	78	UL	
B2405XD-1W		-	5	200	20	73	UL CE	
B2409XD-1W		21.6-26.4	9	111 _	12	78	UL CE	
B2412XD-1W			12	83	9	78	UL CE	
B2415XD-1W	24		15	67	7	79	UL CE	
A2405XD-1W	24		±5	±100	±10	73	UL	
A2409XD-1W	50		±9	±56	±6	79	UL	
A2412XD-1W			±12	±42	±5	80	UL	
A2415XD-1W	1		±15	±33	±4	80	UL	

# COMMON SPECIFICATIONS

COMMON SFLC						
Item	Test Conditions	Min.	Тур.	Max.	Units	
Storage humidity range		5	- C	95	%	
Operating temperature		-40	1.000	85		
Storage temperature	2913. E- W.	-55		125	°C	
Temp. rise at full load	6		15	25		
Lead temperature	1.5mm from case for 10 seconds			300		
Short circuit protection*				1	S	
Cooling		Free air convection				
Case material		Plastic(UL94-V0)				
MTBF		3500			K hours	
Weight			2.4		g	
*Supply voltage must be discontinued at the end of short circuit duration.						

ISOLATION SPECIFICATIONS							
查	查福···A1212XD-1W" 供应商 ions			Тур.	Max.	Units	
	Isolation voltage	Tested for 1 minute and 1mA max	1000			VDC	
	Isolation resistance	Test at 500VDC	1000			MΩ	

OUTPUT SPECIFICATIONS						
Test conditions	Min	Тур	Max	Units		
	0.1		1	W		
For Vin change of 2			±1.2	%		
10% to 100% load (5V output)			12.8	15	%	
10% to 100% load (9V output)			8.3	15		
10% to 100% load (12V output)			6.8	15		
10% to 100% load (15V output)			6.3	15		
		See tole	erance e	nvelope	graph	
100% full load				0.03	%/°C	
20MHz Bandwidth	A_XD-1W		50	75	mVp-p	
	B_XD-1W		75	100		
Full load, nominal input			100		KHz	
	Test conditions For Vin change of 1 10% to 100% load 10% to 100% load 10% to 100% load 10% to 100% load 20MHz Bandwidth Full load, nomina	Test conditions      For Vin change of 1%      10% to 100% load (5∨ output)      10% to 100% load (12V output)      10% to 100% load (12V output)      10% to 100% load (15V output)      100% full load      20MHz    A_XD-1W      Bandwidth    B_XD-1W	Test conditions      Min        0.1      0.1        For Vin change of 1%      0.1        10% to 100% load (5V output)      10        10% to 100% load (2V output)      10        10% to 100% load (12V output)      10        10% to 100% load (15V output)      10        10% to 100% load (15V output)      10        10% to 100% load (15V output)      10        100% full load      100        20MHz      A_XD-1W        Bandwidth      B_XD-1W        Full load, nominal input      10	Test conditions    Min    Typ      0.1    0.1      For Vin change of 1 <sup>+/-</sup> 12.8      10% to 100% load (>/- vutput)    12.8      10% to 100% load (12/- vutput)    6.8      10% to 100% load (12/- vutput)    6.3      10% to 100% load (15/- vutput)    6.3      10% to 100% load (15/- vutput)    6.3      10% to 100% load (15/- vutput)    6.3      100% full load    50      20MHz    A_XD-1W    50      Bandwidth    75      Full load, nomint    100	Test conditions    Min    Typ    Max      0.1    0.1    1      For Vin change of 1···    I    ±1.2      10% to 100% load (5·· output)    I    12.8    15      10% to 100% load (2·· output)    I    8.3    15      10% to 100% load (12·· output)    I    6.8    15      10% to 100% load (12·· output)    I    6.3    15      10% to 100% load (15·· output)    I    6.3    15      10% to 100% load (5·· output)    I    I    0.03      100% full load    I    I    0.03      20MHz    A_XD-1W    I    50    75      Bandwidth    B_XD-1W    I    I    I	

\*Test ripple and noise by "parallel cable" method. See detailed operation instructions at Testing of Power Converter section, application notes.

Note: Dual output models unbalanced load: ±5%

## **APPLICATION NOTE**

#### Requirement on output load

To ensure this module can operate efficiently and reliably, During operation, the minimum output load is **not less than 10%** of the full load, and that this product should **never be operated under no load!** If the actual output power is very small, please connect a resistor with proper resistance at the output end in parallel to increase the load, or use our company's products with a lower rated output power (A/B\_XD-W25 Series).

#### **Recommended testing and application circuit**

If you want to further decrease the input/output ripple, an "LC" filtering network may be connected to the input and output ends of the DC/DC converter, see (Figure 1).

It should also be noted that the inductance and the frequency of the "LC" filtering network should be staggered with the DC/DC frequency to avoid mutual interference. However, the capacitance of the output filter capacitor must be proper. If the capacitance is too big, a startup problem might arise. For every channel of output, provided the safe and reliable operation is ensured, the greatest capacitance of its filter capacitor sees (Table 1).

#### **Output Voltage Regulation and Over-voltage Protection Circuit**

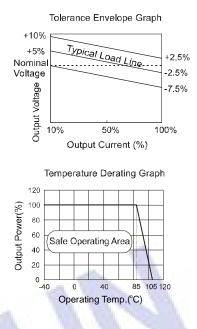
The simplest device for output voltage regulation, over-voltage and over-current protection is a linear voltage regulator with overheat protection that is connected to the input or output end in series (Figure 2).

#### **Overload Protection**

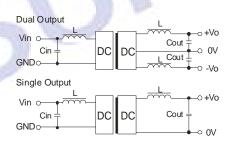
Under normal operating conditions, the output circuit of these products has no protection against overload. The simplest method is to connect a self-recovery fuse in series at the input end or add a circuit breaker to the circuit.

#### No parallel connection or plug and play

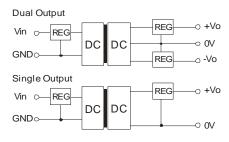
## **TYPICAL CHARACTERISTICS**



### **RECOMMENDED CIRCUIT**





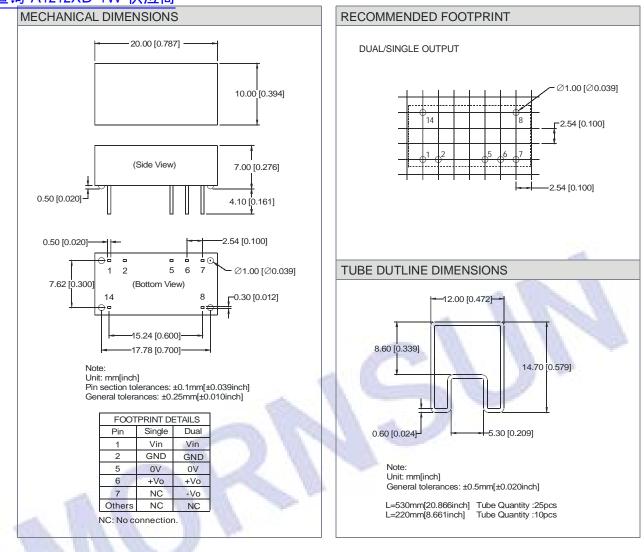




EXTERNAL CAPACITOR TABLE (Table 1) Vin Cin Sinale Cout Dual Cout (VDC) (uF) Vout (uF) Vout (uF) (VDC) (VDC) 5 4.7 4.7 10 5 ±5 2.2 2.2 12 9 4.7 +9±12 24 12 2.2 1 1 15 1 ±15 1 -

It's not recommended to connect any external capacitor in the application field with less than 0.5 watt output.

# **OUTLINE DIMENSIONS & PIN CONNECTIONS** 查询"A1212XD-1W"供应商



#### Note:

- 1. Operation under minimum load will not damage the converter; However, they may not meet all specification listed.
- All specifications measured at Ta=25°C, humidity<75%, nominal input voltage and rated output load unless otherwise specified.</li>
  In this datasheet, all the test methods of indications are based on corporate standards.
- 4. Only typical models listed, other models may be different, please contact our technical person for more details.