## SK6908

#### LINEAR INTEGRATED CIRCUIT

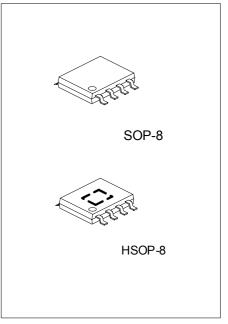
# SINGLE-PHASE DC-FAN **MOTOR DRIVER**

#### **DESCRIPTION**

SK6908 is a single-phase driver for dc-fan motors. It provides high efficiency, low noise output and supports the functions of motor lock protection, auto restart and rotation detection.

#### **FEATURES**

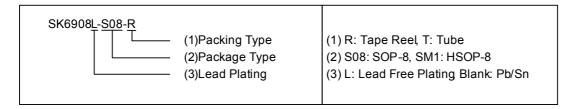
- \*Wide supply voltage range of 2.5V to 20V
- \*Output current I<sub>O(MAX)</sub>=600mA
- \*Operate with Hall element
- \*Lock protection
- \*Auto-restart when the motor lock is undone
- \*FG(frequency generator) output
- \*Package type SOP-8 and HSOP-8 expose pan



\*Pb-free plating product number: SK6908L

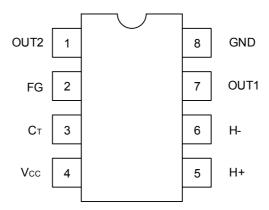
#### **■ ORDERING INFORMATION**

Order Number		Dookogo	Dooking	
Normal	Lead Free Plating	Package	Packing	
SK6908-S08-R	SK6908L-S08-R	SOP-8	Tape Reel	
SK6908-S08-T	SK6908L-S08-T	SOP-8	Tube	
SK6908-SM1-R	SK6908L-SM1-R	HSOP-8	Tape Reel	
SK6908-SM1-T	SK6908L-SM1-T	HSOP-8	Tube	

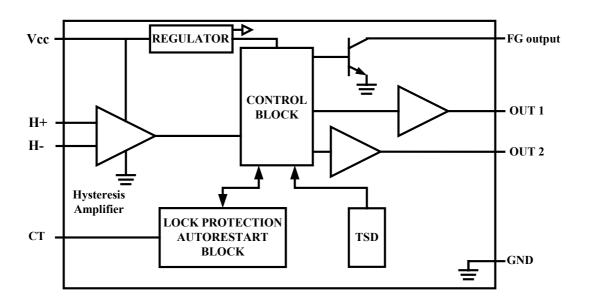




#### **■ PIN CONFIGURATION**



#### **■ BLOCK DIAGRAM**





#### ■ **ABSOLUTE MAXIMUM RATINGS** (Ta = $25^{\circ}$ C)

PARAMETER			RATINGS	UNIT
Supply voltage		Vcc	20	V
Supply current		Icc	20	mA
Circuit current		Ιο	600	mA
FG output current		$I_{FG}$	20	mA
Device dissination (Danata at To-25°C radius 5 FraNV°C)	SOP-8	P <sub>D</sub>	700	mW
Power dissipation (Derate at Ta=25°C reduce 5.5mW/°C)	HSOP-8	$P_D$	1000	mW
Output Voltage		$V_{OUT}$	20	V
Hall input common mode voltage range		$V_{HIC}$	1.0 ~ Vcc-0.5	V
Operating ambient temperature		T <sub>OPR</sub>	-20 ~ +100(Note1)	$^{\circ}\!\mathbb{C}$
Storage temperature		$T_{STG}$	-55 ~ +150	$^{\circ}\!\mathbb{C}$

Note 1.For operation in ambient temperatures above  $25^{\circ}$ C ,the driver device must be derated based on a  $150^{\circ}$ C maximum temperature

#### ■ ELECTRICAL CHARACTERISTICS (Ta=25°C, Vcc=5V)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT
Current drain	I <sub>CC</sub>	In drive mode (C <sub>T</sub> =L)		8		mA
Current drain		In lockup protection mode (C <sub>T</sub> =H)		4.1		mA
Lockup detection capacitor charge current	I <sub>CT1</sub>	V 1PIN = 1.1V	2	2.8	4	uA
Capacitor discharge current	I <sub>CT2</sub>	V 1PIN = 1.1V	0.3	0.5	8.0	uA
Capacitor charge/discharge current ratio	R <sub>CT</sub>			5.6		
CT charge voltage	V <sub>CT1</sub>			1.9		V
CT discharge voltage	$V_{CT2}$			0.7		V
Output low level voltage	$V_{OL}$	I <sub>O</sub> =200mA		0.3	0.7	V
Output high level voltage	Voh	I <sub>O</sub> =200mA	3.9	4.1		V
Hall input sensitivity	$V_{HIN}$	Zero peak value (including offset and hysteresis)	3		15	mV
Hall input-output gain	G <sub>HO</sub>	_	320	500	680	
FG output pin low voltage	$V_{FG}$	I <sub>FG</sub> =5mA		0.3	0.5	V
FG output pin leakage current	I <sub>FG(LEAK)</sub>	V <sub>FG</sub> =15V		0	10	uA

#### ■ HALL SINGLE INPUT-OUTPUT TRUTH VALUE TABLE

H+	H-	OUT1	OUT2	FG
Н	L	Н	L	Н
L	Н	L	Н	L

<sup>\*</sup> CT=0V

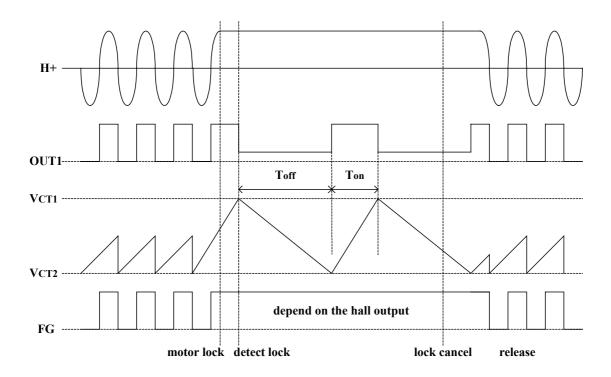
Note 2.Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

Note 3.The device is guaranteed to meet performance specification within  $0^{\circ}$ ~+70° operating temperature range and assured by design from -20°C~+100°.



## ■ LOCKUP PROTECTION / AUTOMATIC RECOVERY

Fig. 1



Ton (charge time) = 
$$\frac{C \times (VCT1-VCT2)}{ICT1}$$

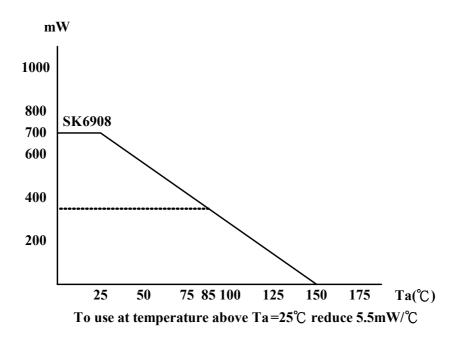
Toff (charge time) = 
$$\frac{C \times (VCT1-VCT2)}{ICT2}$$

C: Value of capcitor at CT terminal



#### **■ POWER DISSIPATION**

Fig.-2



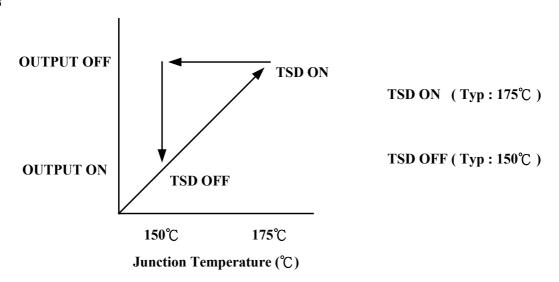
The maximum junctions temperature is 150°C in plastic packages, but for reasons of reliability, a lower number may be mandated.

$$P_T = T_{J(MAX)} - Ta_{(MAX)} / \theta_{JA}$$

 $P_T$  = Total Power Dissipated by the Device  $T_{J (MAX)}$  = Maximum Junction Temperature  $Ta_{(MAX)}$  = Maximum Ambient Temperature  $\theta_{JA}$  = I75°C/W

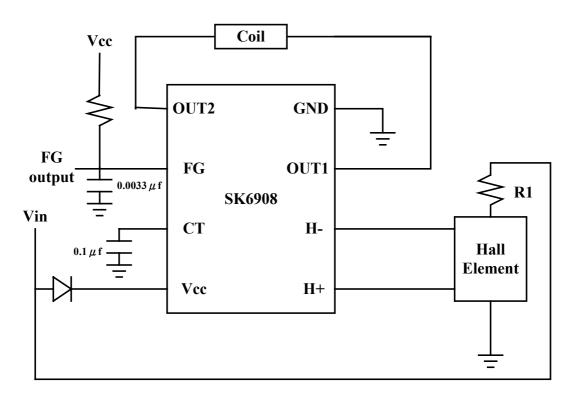
## ■ THERMAL SHUTDOWN(TSD)

Fig.-3





#### **■ TYPICAL APPLICATION CIRCUIT**



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