

August 1993 Revised April 1999

74VHC153 Dual 4-Input Multiplexer

General Description

The VHC153 is an advanced high-speed CMOS device fabricated with silicon gate CMOS technology. It achieves the high-speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation. The VHC153 is a high-speed Dual 4-Input Multiplexer with common select inputs and individual enable inputs for each section. It can select two lines of data from four sources. The two buffered outputs present data in the true (non-inverted) form. In addition to multiplexer operation, the VHC153 can act as a function generator and generate any two functions of three variables. An input protection circuit insures that OV to 7V can be applied to the input pins without regard to the supply voltage. This

device can be used to interface 5V to 3V systems and two supply systems such as battery backup. This circuit prevents device destruction due to mismatched supply and input voltages.

Features

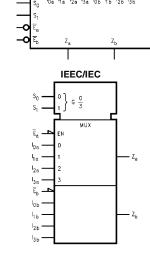
- \blacksquare High Speed: $t_{PD}=5.0$ ns at $T_A=25^{\circ}C$
- \blacksquare Low power dissipation: $I_{CC}=4~\mu\text{A}$ (max) at $T_A=25^{\circ}\text{C}$
- High noise immunity: $V_{NIH} = V_{NIL} = 28\% V_{CC}$ (min)
- Power down protection is provided on all inputs
- Pin and function compatible with 74HC153

Ordering Code:

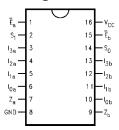
Order Number	Package Number	Package Description					
74VHC153M	M16A	16-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150 Narrow					
74VHC153SJ	M16D	16-Lead Small Outline Package (SOP), EIAJ TYPE II, 5.3mm Wide					
74VHC153MTC	MTC16	16-Lead Thin Shrink Small Outline Package (TSSOP), JEDEC MO-153, 4.4mm Wide					
74VHC153N	N16E	16-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide					

Surface mount packages are also available on Tape and Reel. Specify by appending the suffix letter "X" to the ordering code.

Logic Symbols



Connection Diagram



Pin Descriptions

Pin Names	Description
I _{0a} –I _{3a}	Side A Data Inputs
$I_{0b}-I_{3b}$	Side B Data Inputs
S ₀ , S ₁	Common Select Inputs
E _a	Side A Enable Input
\overline{E}_b	Side B Enable Input
Z _a	Side A Output
Z _b	Side B Output

Functional Description

The VHC153 is a dual 4-input multiplexer. It can select two bits of data from up to four sources under the control of the common Select inputs (S0, S1). The two 4-input $\underline{\underline{m}}$ Itiplexer circuits have individual active-LOW Enables (\overline{E}_a , \overline{E}_b) which can be used to strobe the outputs independently. When the Enables $(\overline{E}_a, \overline{E}_b)$ are HIGH, the corresponding outputs (Z_a, \overline{E}_b) Z_b) are forced LOW. The VHC153 is the logic implementation of a 2-pole, 4-position switch, where the position of the switch is determined by the logic levels supplied to the Select inputs. The logic equations for the outputs are shown below.

$$\begin{split} Z_a &= \overline{E}_a \bullet (I_{0a} \bullet \overline{S}_1 \bullet \overline{S}_0 + I_{1a} \bullet \overline{S}_1 \bullet S_0 + \\ I_{2a} \bullet S_1 \bullet S_0 + I_{3a} \bullet S_1 \bullet S_0) \\ Z_b &= \overline{E}_b \bullet (I_{0b} \bullet \overline{S}_1 \bullet \overline{S}_0 + I_{1b} \bullet \overline{S}_1 \bullet S_0 + \\ I_{2b} \bullet S_1 \bullet S_0 + I_{3b} \bullet S_1 \bullet S_0) \end{split}$$

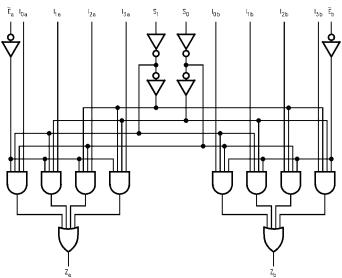
Truth Table

Select Inputs			Output				
S ₀	S ₀ S ₁		I ₀	I ₁	l ₂	l ₃	z
Х	Х	Н	Х	Х	Х	Х	L
L	L	L	L	Х	Х	Х	L
L	L	L	Н	Х	Х	Х	Н
Н	L	L	Х	L	Х	Х	L
Н	L	L	Х	Н	Х	Х	Н
L	Н	L	Х	Х	L	Х	L
L	Н	L	Х	Х	Н	Х	Н
Н	Н	L	Х	Х	Х	L	L
Н	Н	L	Х	Х	Х	Н	н

H = HIGH Voltage Level

L = LOW Voltage Level X = Immaterial

Logic Diagram



Please note that this diagram is provided only for the understanding of logic operations and should not be used to estimate propagation delays.

Absolute Maximum Ratings(Note 1)

-0.5V to +7.0V Supply Voltage (V_{CC}) DC Input Voltage (V_{IN}) -0.5V to +7.0VDC Output Voltage (V_{OUT}) -0.5V to $V_{CC} + 0.5V$ Input Diode Current (I_{IK}) -20 mA Output Diode Current (I_{OK}) ±20 mA DC Output Current (I_{OUT}) ±25 mA DC V_{CC}/GND Current (I_{CC}) ±50 mA Storage Temperature (T_{STG}) -65°C to +150°C

Lead Temperature (T_L)

(Soldering, 10 seconds)

Recommended Operating Conditions (Note 2)

Input Rise and Fall Time (t_r, t_f)

 $\begin{array}{ll} \mbox{V}_{\mbox{CC}} = 3.3 \mbox{V} \pm 0.3 \mbox{V} & \mbox{0\sim100 ns/V} \\ \mbox{V}_{\mbox{CC}} = 5.0 \mbox{V} \pm 0.5 \mbox{V} & \mbox{0\sim20 ns/V} \end{array}$

Note 1: Absolute maximum ratings are those values beyond which the device may be damaged or have its useful life impaired. The databook specifications should be met, without exception, to ensure that the system design is reliable over its power supply, temperature, and output/input loading variables. Fairchild does not recommend operation outside databook specifications.

Note 2: Unused inputs must be held HIGH or LOW. They may not float.

DC Electrical Characteristics

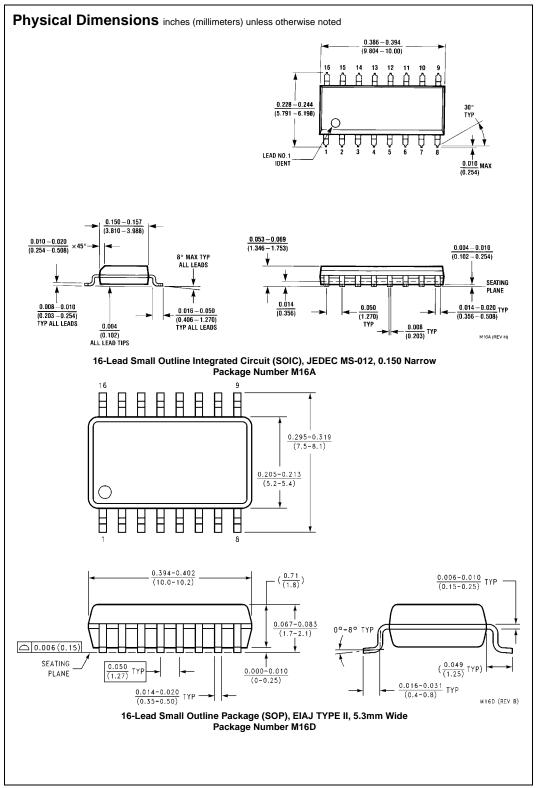
Symbol	Parameter	V _{CC}	$T_A = 25^{\circ}C$			$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$		Units	Conditions	
Cynnbon		(V)	Min	Тур	Max	Min	Max	Ullits	Conditions	
V _{IH}	HIGH Level Input	2.0	1.50			1.50		V		
	Voltage	3.0 - 5.5	$0.7\mathrm{V_{CC}}$			0.7 V _{CC}		V		
V _{IL}	LOW Level Input	2.0			0.50		0.50	V		
	Voltage	3.0 - 5.5			$0.3\ V_{\rm CC}$		$0.3~\mathrm{V}_{\mathrm{CC}}$	v		
V _{OH}	HIGH Level Output	2.0	1.9	2.0		1.9			$V_{IN} = V_{IH}$	$I_{OH} = -50 \mu A$
	Voltage	3.0	2.9	3.0		2.9		V	or V _{IL}	
		4.5	4.4	4.5		4.4				
		3.0	2.58			2.48		V	Ì	$I_{OH} = -4 \text{ mA}$
		4.5	3.94			3.80		v		$I_{OH} = -8 \text{ mA}$
V _{OL}	LOW Level Output	2.0		0.0	0.1		0.1		$V_{IN} = V_{IH}$	$I_{OL} = 50 \mu A$
	Voltage	3.0		0.0	0.1		0.1	V	or V _{IL}	
		4.5		0.0	0.1		0.1			
		3.0			0.36		0.44	V	Ì	$I_{OL} = 4 \text{ mA}$
		4.5			0.36		0.44	v		$I_{OL} = 8 \text{ mA}$
I _{IN}	Input Leakage Current	0 – 5.5			±0.1		±1.0	μА	$V_{IN} = 5.5V$	or GND
I _{CC}	Quiescent Supply Current	5.5			4.0		40.0	μА	$V_{IN} = V_{CC}$	or GND

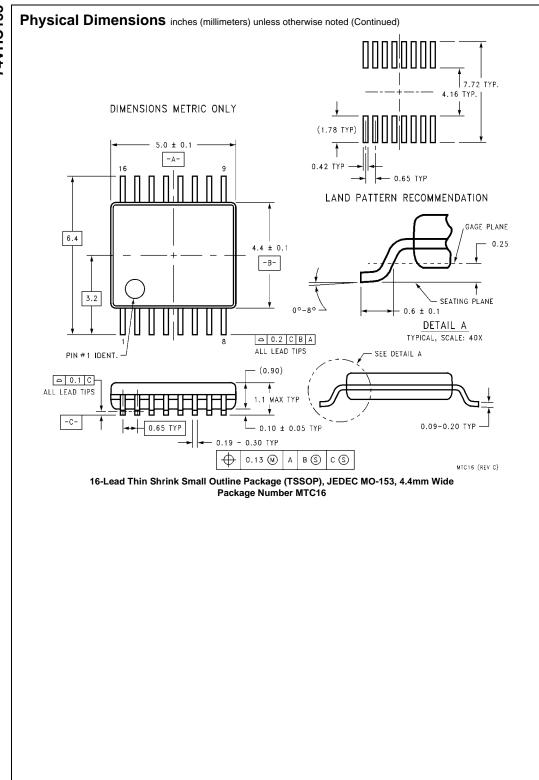
260°C

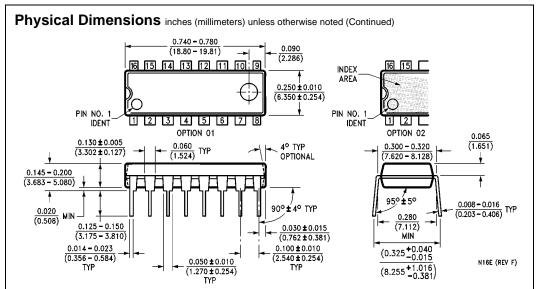
AC Electrical Characteristics

Symbol	Parameter	V _{CC}	T _A = 25°C			T _A =-40°0	C to +85°C	Units	Conditions
		(V)	Min	Тур	Max	Min	Max	Units	Conditions
PLH	Propagation Delay	3.3 ± 0.3		7.7	11.9	1.0	14.0	ns	C _L = 15 pF
PHL	I_n to Z_n			10.2	15.4	1.0	17.5		C _L = 50 pF
		5.0 ± 0.5		5.0	7.7	1.0	9.0	ns	C _L = 15 pF
				6.5	9.7	1.0	11.0	115	C _L = 50 pF
	Propagation Delay	3.3 ± 0.3		10.8	16.7	1.0	19.5	ns	C _L = 15 pF
	S _n to Z _n			13.3	20.2	1.0	23.0	115	C _L = 50 pF
		5.0 ± 0.5		6.8	9.9	1.0	11.5	ns	C _L = 15 pF
				8.3	11.9	1.0	13.5	115	C _L = 50 pF
t _{PLH}		3.3 ± 0.3		6.3	10.1	1.0	12.0	ns	C _L = 15 pF
				8.8	13.6	1.0	15.5		C _L = 50 pF
		5.0 ± 0.5		4.4	6.4	1.0	7.5	ns	C _L = 15 pF
				5.9	8.4	1.0	9.5	115	C _L = 50 pF
C _{IN}	Input Capacitance			4	10		10	pF	V _{CC} = Open
C _{PD}	Power Dissipation			20				pF	(Note 3)
	Capacitance								

Note 3: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation: I_{CC} (opr.) = C_{PD} * V_{CC} * f_{IN} + I_{CC} .







16-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide Package Number N16E

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF THE PRESIDENT OF FAIRCHILD SEMICONDUCTOR CORPORATION. As used herein:

- Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury to the user.
- A critical component in any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

www.fairchildsemi.com