·询"74AC16640DL"供应商

- Members of the Texas Instruments Widebus™ Family
- Flow-Through Architecture Optimizes
 PCB Layout
- Distributed V_{CC} and GND Pin Configuration
 Minimizes High-Speed Switching Noise
- EPIC[™] (Enhanced-Performance Implanted CMOS) 1-µm Process
- 500-mA Typical Latch-Up Immunity at 125°C
- Package Options Include Plastic 300-mil Shrink Small-Outline (DL) Packages Using 25-mil Center-to-Center Pin Spacings and 380-mil Fine-Pitch Ceramic Flat (WD) Packages Using 25-mil Center-to-Center Pin Spacings

description

The 'AC16640 are inverting 16-bit transceivers designed for asynchronous communication between data buses.

These devices can be used as two 8-bit transceivers or one 16-bit transceiver. They allow data transmission from the A bus to the B bus or from the B bus to the A bus, depending on the logic level at the direction-control (1DIR and 2DIR) inputs. The output-enable (1OE and 2OE) inputs can be used to disable the device so that the buses are effectively isolated.

54AC16640 . . . WD PACKAGE 74AC16640 . . . DL PACKAGE (TOP VIEW)

1DIR	1		10E
1B1 🛚	2] 1A1
1B2 🛚	3] 1A2
GND [4		GND
1B3	5	44	1A3
1B4	6	43] 1A4
Vcc	7] v _{cc}
1B5 🛚	8] 1A5
1B6	9	40] 1A6
GND [10	39	GND
1B7 🛚	11	38] 1A7
1B8 🛚	12	37] 1A8
2B1	13	36	2A1
2B2 🛚	14	35	2A2
GND [15		GND
2B3	16	33	2A3
2B4	17		2A4
Vcc	18] v _{cc}
2B5	19		2A5
2B6 🛚	20		2A6
GND [21		GND
2B7 🛚	22	27] 2A7
2B8 [23		2 <u>A8</u>
2DIR	24	25	20E

The 74AC16640 is packaged in TI's shrink small-outline package, which provides twice the I/O pin count and functionality of standard small-outline packages in the same printed-circuit-board area.

The 54AC16640 is characterized for operation over the full military temperature range of –55°C to 125°C. The 74AC16640 is characterized for operation from –40°C to 85°C.

FUNCTION TABLE (each section)

INP	UTS	ODED ATION
OE	DIR	OPERATION
L	L	B data to A bus
L	Н	A data to B bus
Н	Χ	Isolation



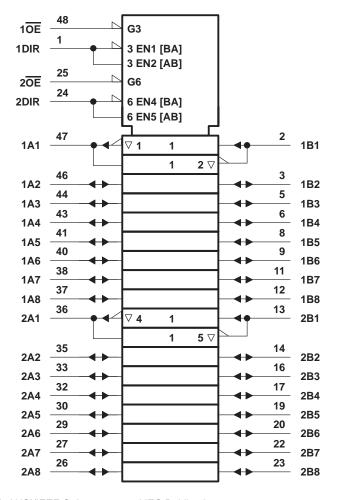
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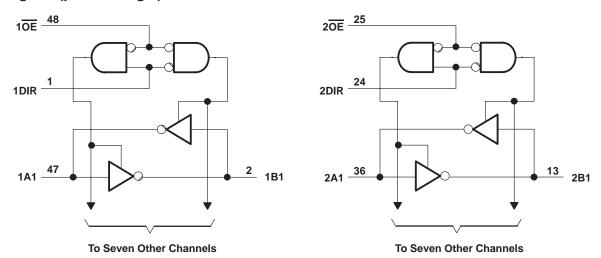
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logic symbol†



 $[\]ensuremath{^{\dagger}}$ This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

logic diagram (positive logic)





absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltage range, V _{CC}	0.5 V to 7 V
Input voltage range, V _I (see Note 1)	\dots -0.5 V to V _{CC} + 0.5 V
Output voltage range, V _O (see Note 1)	\dots -0.5 V to V _{CC} + 0.5 V
Input clamp current, I_{IK} ($V_I < 0$ or $V_I > V_{CC}$)	±20 mA
Output clamp current, I _{OK} (V _O < 0 or V _O > V _{CC})	±50 mA
Continuous output current, I_O ($V_O = 0$ to V_{CC})	±50 mA
Continuous current through V _{CC} or GND	±400 mA
Maximum power dissipation at $T_A = 55^{\circ}C$ (in still air)(see Note 2): DL package	1.2 W
Storage temperature range, T _{Stq}	–65°C to 150°C

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

recommended operating conditions (see Note 3)

			54	54AC16640			AC1664	0	UNIT
			MIN	NOM	MAX	MIN	NOM	MAX	UNII
Vcc	Supply voltage		3	5	5.5	3	5	5.5	V
		VCC = 3 V	2.1			2.1			
V_{IH}	High-level input voltage	$V_{CC} = 4.5 V$	3.15			3.15			V
		V _{CC} = 5.5 V	3.85			3.85			
		V _{CC} = 3 V			0.9			0.9	
V_{IL}	Low-level input voltage	$V_{CC} = 4.5 \text{ V}$			1.35			1.35	V
		$V_{CC} = 5.5 \text{ V}$			1.65			1.65	
VI	Input voltage		0	Ty.	VCC	0		VCC	V
VO	Output voltage		0	20	VCC	0		VCC	V
		V _{CC} = 3 V		Ć)	-4			-4	
IOH	High-level output current	$V_{CC} = 4.5 V$	5	3	-24			-24	mA
		V _{CC} = 5.5 V	S. S		-24			-24	
		V _{CC} = 3 V			12			12	
loL	Low-level output current	$V_{CC} = 4.5 V$			24			24	mA
		V _{CC} = 5.5 V			24			24	
Δt/Δν	Input transition rise or fall rate		0		10	0		10	ns/V
TA	Operating free-air temperature		-55		125	-40		85	°C

NOTE 3: Unused inputs must be held high or low to prevent them from floating.

NOTES: 1. The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

^{2.} The maximum package power dissipation is calculated using a junction temperature of 150°C and a board trace length of 750 mils.

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER		TEST CONDITIONS	V	T,	T _A = 25°C			16640	74AC16640		UNIT	
		TEST CONDITIONS	Vcc	MIN	TYP	MAX	MIN	MAX	MIN	MAX	UNII	
			3 V	2.9			2.9		2.9			
		ΙΟΗ = -50 μΑ	4.5 V	4.4			4.4		4.4			
			5.5 V	5.4			5.4		5.4			
Vон		I _{OH} = -4 mA	3 V	2.58			2.48		2.48		V	
			4.5 V	3.94			3.8		3.8			
		I _{OH} = -24 mA	5.5 V	4.94			4.8		4.8			
		$I_{OH} = -75 \text{ mA}^{\dagger}$	5.5 V				3.85	EN	3.85			
			3 V			0.1		0.1		0.1		
		I _{OL} = 50 μA	4.5 V			0.1	4	0.1		0.1		
			5.5 V			0.1	() ()	0.1		0.1		
VOL		I _{OL} = 12 mA	3 V			0.36	200	0.44		0.44	V	
		I _{OL} = 24 mA	4.5 V			0.36). V	0.44		0.44		
		10L = 24 111A	5.5 V			0.36	/	0.44		0.44		
		I _{OL} = 75 mA [†]	5.5 V					1.65		1.65		
ТĮ	Control inputs	V _I = V _{CC} or GND	5.5 V			±0.1		±1		±1	μΑ	
loz [‡]	A or B ports	$V_O = V_{CC}$ or GND	5.5 V			±0.5		±5		±5	μΑ	
ICC		$V_I = V_{CC}$ or GND, $I_O = 0$	5.5 V			8		80		80	μΑ	
Ci	Control inputs	V _I = V _{CC} or GND	5 V		4.5						pF	
C _{io}	A or B ports	$V_O = V_{CC}$ or GND	5 V		16						pF	

[†] Not more than one output should be tested at a time, and the duration of the test should not exceed 10 ms.

switching characteristics over recommended operating free-air temperature range, V_{CC} = 3.3 V \pm 0.3 V (unless otherwise noted) (see Figure 1)

PARAMETER	FROM	то	T,	_Δ = 25°C	;	54AC1	6640	74AC1	6640	UNIT
PARAMETER	(INPUT)	(OUTPUT)	MIN	TYP	MAX	MIN	MAX	MIN	MAX	UNIT
t _{PLH}	A or B	B or A	2.2	6.9	9.1	2.2	10	2.2	10	no
tpHL	AUIB	BULA	3	8.5	11	3	11.9	3	11.9	ns
^t PZH		A or D	3	8.2	11	3	12.3	3	12.3	
^t PZL	ŌĒ	A or B	3.9	10.9	14	3.9	15.5	3.9	15.5	ns
^t PHZ	<u> </u>	A or B	5.1	8.3	10.6	5.1	11.2	5.1	11.2	ne
t _{PLZ}	ŌĒ	AUIB	4.3	7.8	10.1	4.3	10.6	4.3	10.6	ns

switching characteristics over recommended operating free-air temperature range, V_{CC} = 5 V \pm 0.5 V (unless otherwise noted) (see Figure 1)

PARAMETER	FROM	то	T,	չ = 25°C	;	54AC1	6640	74AC1	16640	UNIT
PARAMETER	(INPUT)	(OUTPUT)	MIN	TYP	MAX	MIN	MAX	MIN	MAX	UNIT
^t PLH	A or B	B or A	1.8	4.7		1.8	7.3	1.8	7.3	ne
tPHL	AUIB	BUIA	2.6	5.7		2.6	8.6	2.6	8.6	ns
^t PZH	ŌĒ	A or B	2.4	5.6		2.4	8 11.7	2.4	8	ns
t _{PZL}	OE	AOIB	3	6.6		3	9.9	3	9.9	115
^t PHZ	ŌĒ	A or B	5	7.5		9 5	9.9	5	9.9	ns
^t PLZ	OE	AOIB	4.1	6.5		4.1	9	4.1	9	115

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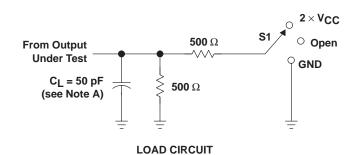
[‡] For I/O ports, the parameter IOZ includes the input leakage current.

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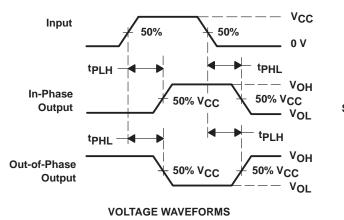
operating characteristics, V_{CC} = 5 V, T_A = 25°C

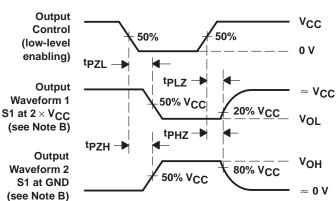
PARAMETER			TEST CO	TYP	UNIT	
C _{pd} Power dissipation capacitance per transceiver		Outputs enabled	C: 50 pF		55	~F
		Outputs disabled	$C_L = 50 \text{ pF},$	f = 1 MHz	8	pΕ

PARAMETER MEASUREMENT INFORMATION



TEST	S1			
tPLH/tPHL	Open			
t _{PLZ} /t _{PZL}	$2 \times V_{CC}$			
tPHZ/tPZH	GND			





VOLTAGE WAVEFORMS

NOTES: A. C_L includes probe and jig capacitance.

- B. Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
- C. All input pulses are supplied by generators having the following characteristics: PRR \leq 1 MHz, $Z_O = 50 \Omega$, $t_f = 3 \text{ ns}$, $t_f = 3 \text{ ns}$.
- D. The outputs are measured one at a time with one input transition per measurement.

Figure 1. Load Circuit and Voltage Waveform

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