

查询"SKM145GB123D_06"供应商



SEMITRANS® 2

IGBT Modules

SKM 145GB123D

SKM 145GAL123D

Features

- MOS input (voltage controlled)
- N channel, Homogeneous Si
- Low inductance case
- Very low tail current with low temperature dependence
- High short circuit capability, self limiting to $6 \times I_{\text{cnom}}$
- Latch-up free
- Fast & soft inverse CAL diodes
- Isolated copper baseplate using DCB Direct Copper Bonding
- Large clearance (10 mm) and creepage distances (20 mm)

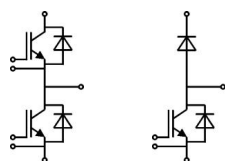
Typical Applications

- Switching (not for linear use)
- AC inverter drives

Characteristics					
Symbol	Conditions	min.	typ.	max.	Units
Inverse Diode					
$V_F = V_{EC}$	$I_{Fnom} = 100 \text{ A}; V_{GE} = 0 \text{ V}$	$T_j = 25^\circ\text{C}_{\text{chiplev.}}$	2	2,5	V
		$T_j = 125^\circ\text{C}_{\text{chiplev.}}$	1,8		V
V_{F0}		$T_j = 25^\circ\text{C}$	1,1	1,4	V
		$T_j = 125^\circ\text{C}$			V
r_F		$T_j = 25^\circ\text{C}$	9	11	mΩ
		$T_j = 125^\circ\text{C}$			mΩ
I_{RRM}	$I_{Fnom} = 100 \text{ A}$	$T_j = 25^\circ\text{C}$	35		A
Q_{rr}	$di/dt = 1000 \text{ A}/\mu\text{s}$		5		μC
E_{rr}	$V_{GE} = 0 \text{ V}; V_{CC} = 600 \text{ V}$				mJ
$R_{th(j-c)D}$	per diode			0,36	K/W
Freewheeling Diode					
$V_F = V_{EC}$	$I_{Fnom} = 150 \text{ A}; V_{GE} = 0 \text{ V}$	$T_j = 25^\circ\text{C}_{\text{chiplev.}}$	2	2,5	V
		$T_j = 125^\circ\text{C}_{\text{chiplev.}}$	1,8		V
V_{F0}		$T_j = 25^\circ\text{C}$	1,1	1,4	V
		$T_j = 125^\circ\text{C}$			V
r_F		$T_j = 25^\circ\text{C}$	9	11	V
		$T_j = 125^\circ\text{C}$			V
I_{RRM}	$I_{Fnom} = 150 \text{ A}$	$T_j = 25^\circ\text{C}$	55		A
Q_{rr}			8		μC
E_{rr}	$V_{GE} = 0 \text{ V}; V_{CC} = 600 \text{ V}$				mJ
$R_{th(j-c)FD}$	per diode			0,3	K/W
Module					
L_{CE}				30	nH
$R_{CC'+EE'}$	res., terminal-chip	$T_{\text{case}} = 25^\circ\text{C}$	0,75		mΩ
		$T_{\text{case}} = 125^\circ\text{C}$	1		mΩ
$R_{th(c-s)}$	per module			0,05	K/W
M_s	to heat sink M6		3	5	Nm
M_t	to terminals M5		2,5	5	Nm
w				160	g

This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, Chapter IX.

This technical information specifies semiconductor devices but promises no characteristics. No warranty or guarantee expressed or implied is made regarding delivery, performance or suitability.



GB

GAL

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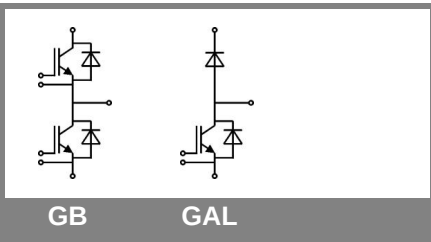
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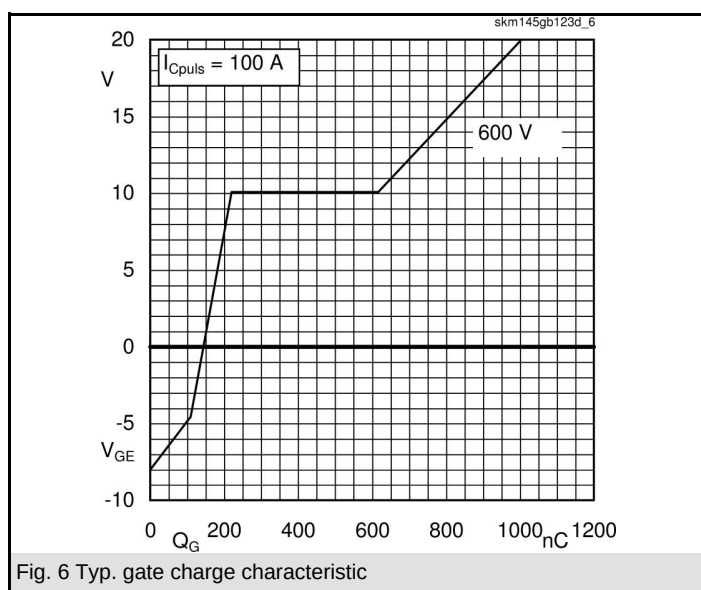
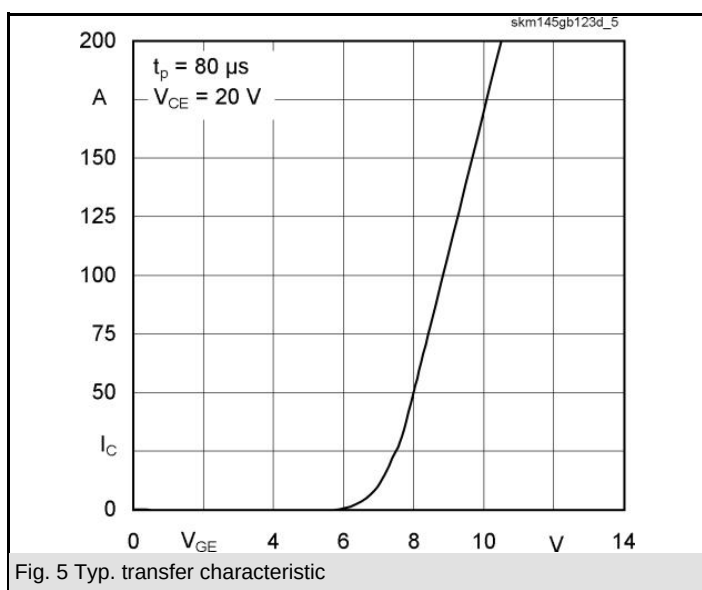
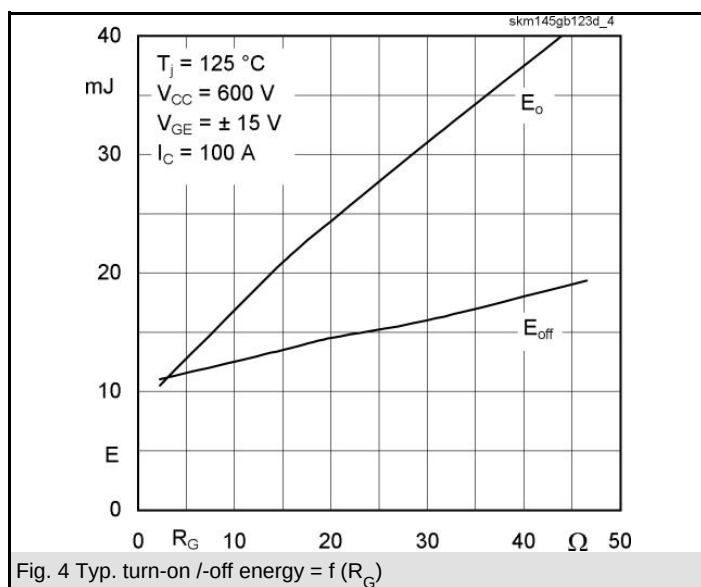
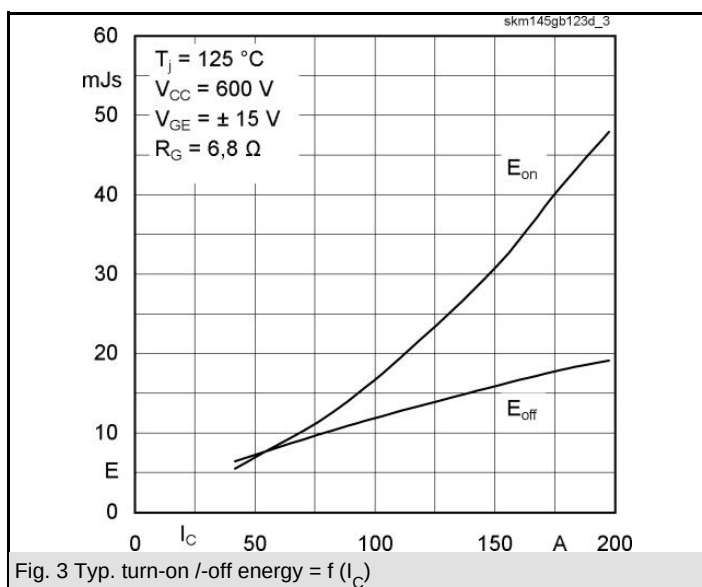
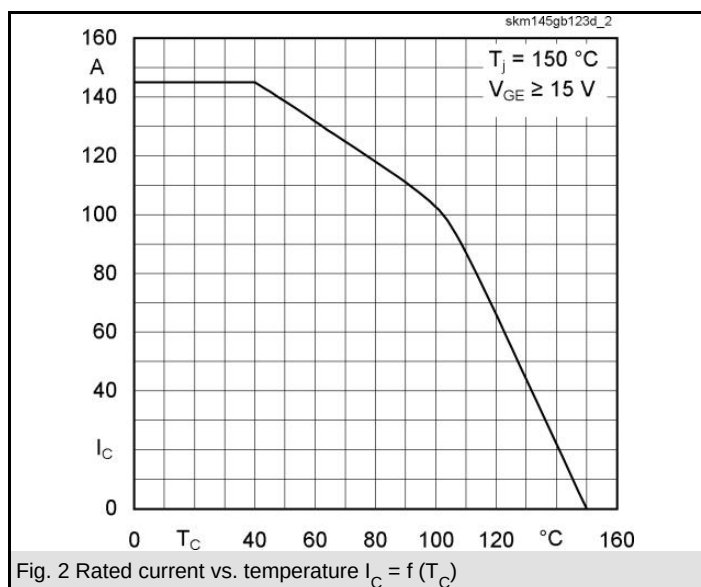
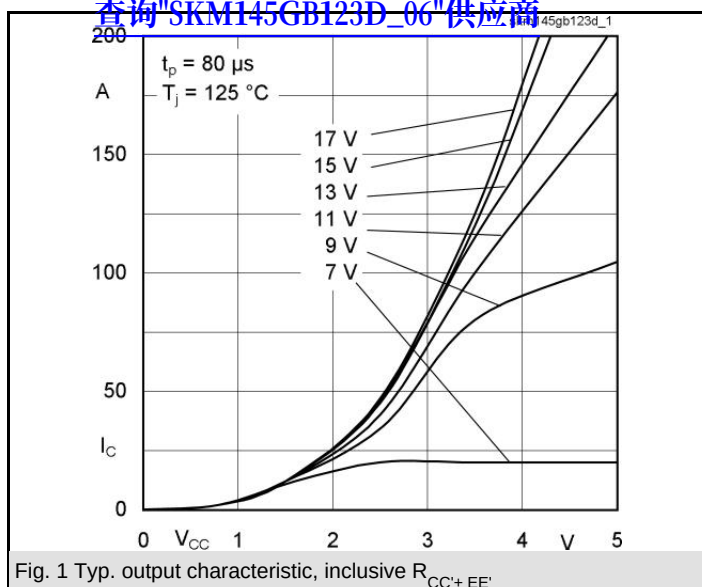
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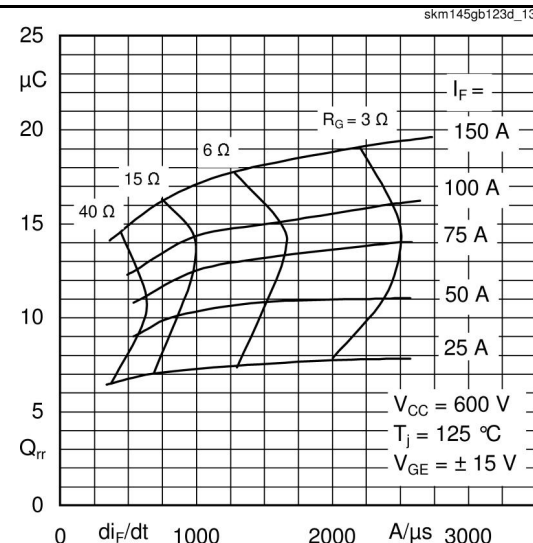
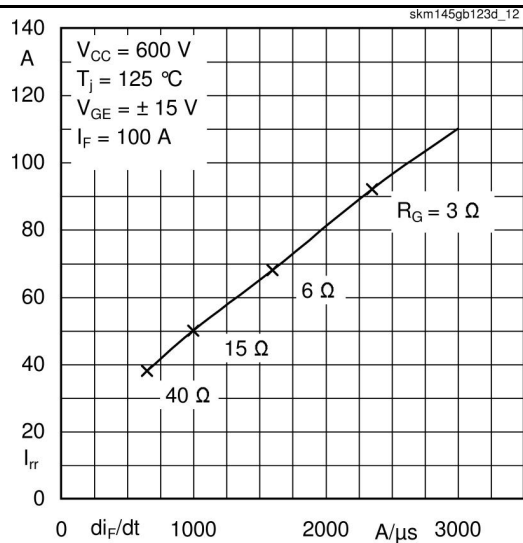
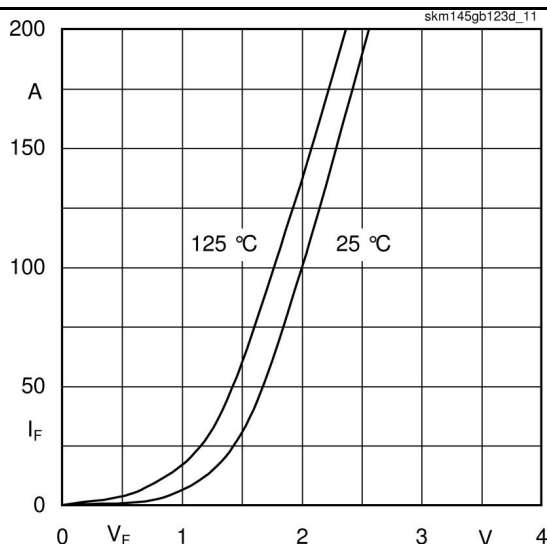
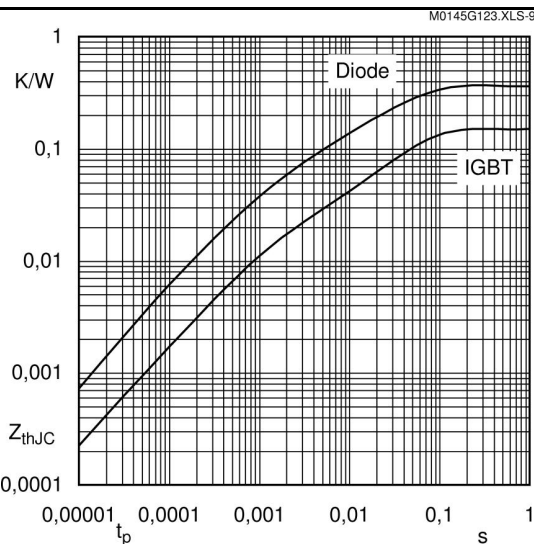
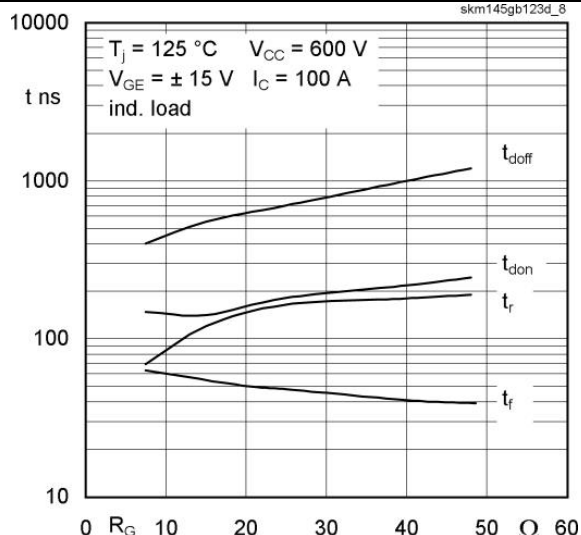
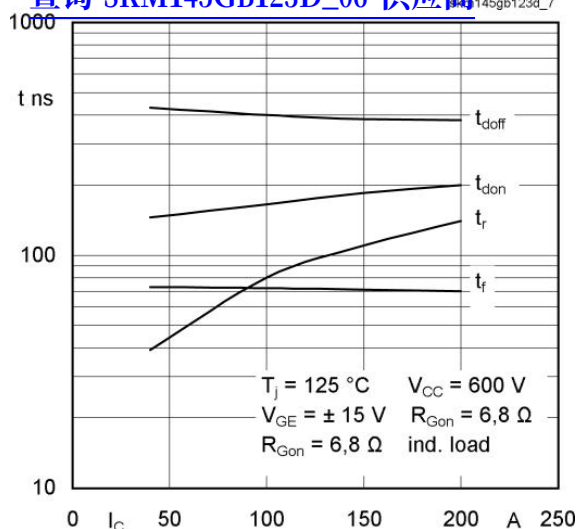


Symbol	Conditions	Values	Units
$Z_{th(j-c)I}$			
R_i	$i = 1$	100	mk/W
R_i	$i = 2$	38	mk/W
R_i	$i = 3$	10	mk/W
R_i	$i = 4$	2	mk/W
τ_{ui}	$i = 1$	0,03	s
τ_{ui}	$i = 2$	0,0287	s
τ_{ui}	$i = 3$	0,0012	s
τ_{ui}	$i = 4$	0,0002	s
$Z_{th(j-c)D}$			
R_i	$i = 1$	240	mk/W
R_i	$i = 2$	95	mk/W
R_i	$i = 3$	22	mk/W
R_i	$i = 4$	3	mk/W
τ_{ui}	$i = 1$	0,054	s
τ_{ui}	$i = 2$	0,0113	s
τ_{ui}	$i = 3$	0,0012	s
τ_{ui}	$i = 4$	0,005	s

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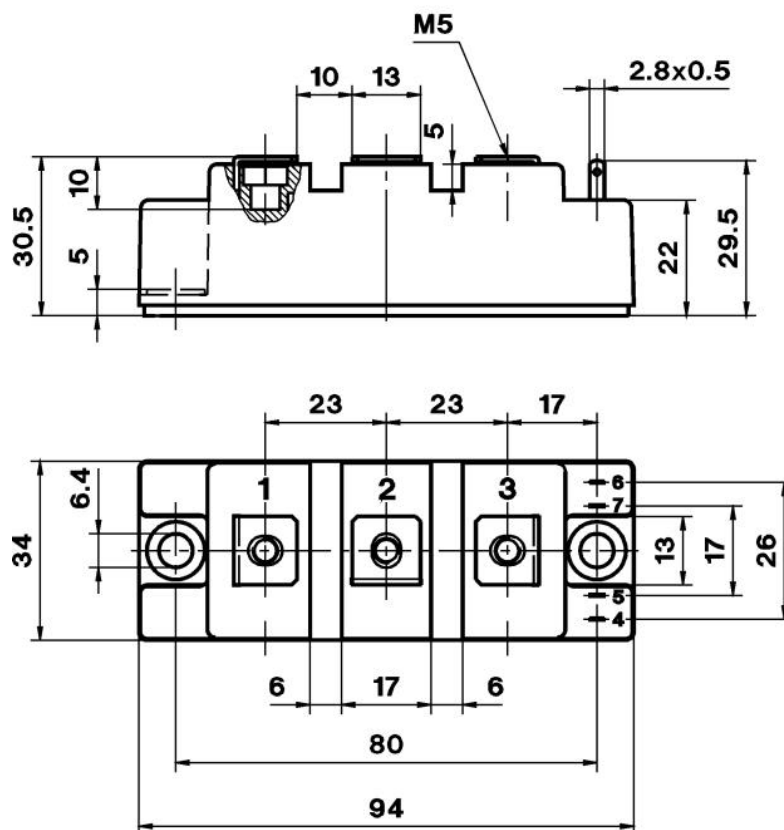


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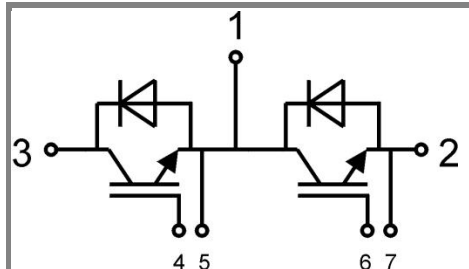
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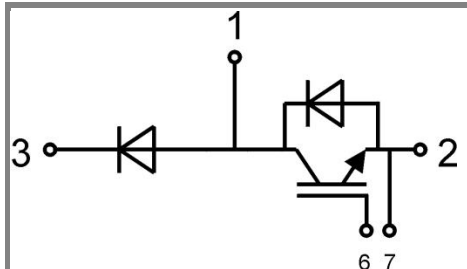


Case D 61



GB

Case D 61



GAL

Case D 62 (→ D 61)