

LCA05C thru LCA24C

LOW CAPACITANCE TVS ARRAY

APPLICATIONS

- ✔ Ethernet 10/100 Base T
- ✔ RS-485
- ✔ xDSL & ATM
- ✔ SCSI & USB
- ✔ Audio/Video I/O Ports

IEC COMPATIBILITY (EN61000-4)

- ✔ 61000-4-2 (ESD): Air 15kV, Contact 8kV
- ✔ 61000-4-4 (EFT): 40A 5/50ns
- ✓ 61000-4-5 (Surge): 24A, 8/20µs Level 2 (Line-Ground) & Level 3 (Line-Line)

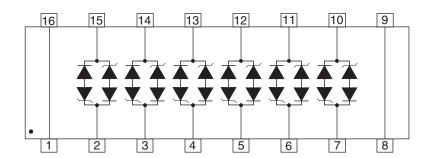
FEATURES

- ✓ 800 Watts Peak Pulse Power per Line (tp=8/20µs)
- ✓ Bidirectional Configuration
- ✓ ESD Protection > 40 kilovolts
- ✔ Available in Five (5) Voltage Types: 5V to 24V
- ✓ Standard Dual-In-Line Package
- ✓ Protects Up to Six (6) Lines
- ✔ LOW CAPACITANCE: 15pF

MECHANICAL CHARACTERISTICS

- ✔ Molded 16 Pin Dual-In-Line (DIP) Package
- ✓ Weight 1.2 grams (Approximate)
- ✔ Flammability rating UL 94V-0
- ✔ Packaging: 25 Pieces Per Tube
- ✔ Marking: Logo, Part Number, Date Code & Pin One Defined By Dot on Top of Package

PINCONFIGURATION





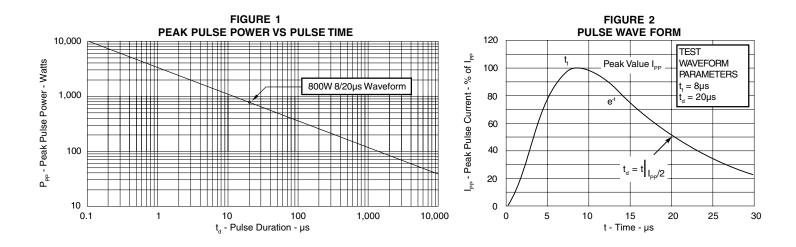
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DEVICE CHARACTERISTICS

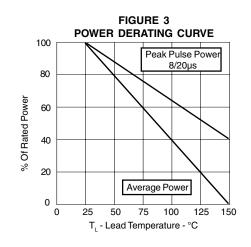
MAXIMUM RATINGS @ 25°C Unless Otherwise Specified							
PARAMETER	SYMBOL	VALUE UNITS					
Peak Pulse Power ($t_p = 8/20\mu s$) - See Figure 1	P _{PP}	800	Watts				
Operating Temperature	TJ	-55°C to 150°C	°C				
Storage Temperature	T _{STG}	-55°C to 150°C	℃°				

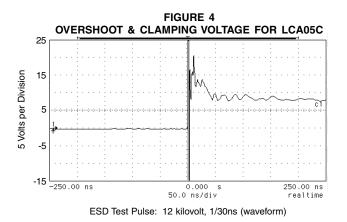
ELECTRICAL CHARACTERISTICS PER LINE @ 25°C Unless Otherwise Specified									
PART NUMBER (Notes 1)	RATED STAND-OFF VOLTAGE	MINIMUM BREAKDOWN VOLTAGE	MAXIMUM CLAMPING VOLTAGE (See Fig. 2)	MAXIMUM CLAMPING VOLTAGE (See Fig. 2)	MAXIMUM LEAKAGE CURRENT	MAXIMUM CAPACITANCE	TEMPERATURE COEFFICIENT OF V _(BR)		
	V _{WM} VOLTS	@ 1mA V _(BR) VOLTS	@ I _p = 1 A V _c VOLTS	@ 8/20µs V _c @ I _{PP}	@V _{wM} Ι _D μΑ	@ 0V, 1 MHz C pF	θV _(BR) mV/°C		
LCA05C LCA08C LCA12C LCA15C LCA24C	5.0 8.0 12.0 15.0 24.0	6.0 8.5 13.3 16.7 26.7	9.8 12.3 19.0 25.5 40.0	24V @ 45A 25.5V @ 40A 32V @ 34A 38V @ 27A 48V @ 22A	100 10 4 4 4	15 15 15 15 15	3 9 16 17 26		

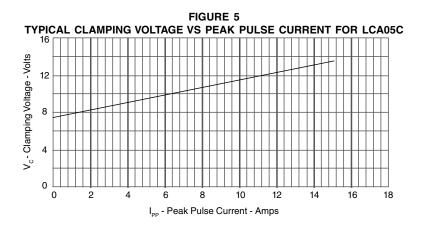
Note 1: Tested on pin pairs 1 & 16, 2 & 15, 3 & 14, 4 & 13, 5 & 12, 6 & 11, 7 & 10 and 8 & 9.



GRAPHS







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APPLICATION NOTE

The LCA Series are low capacitance, bidirectional TVS arrays that are designed to protect I/O or high speed data lines from the damaging effects of ESD or EFT. This product series has a surge capability of 800 Watts P_{pp} per line for an 8/20µs waveshape and offers ESD protection > 40kV.

BIDIRECTIONAL COMMON-MODE CONFIGURATION (Figure 1)

Ideal for use in USB applications, the LCA Series provides up to six (6) lines of protection in a common-mode configuration as depicted in Figure 1.

Circuit connectivity is as follows:

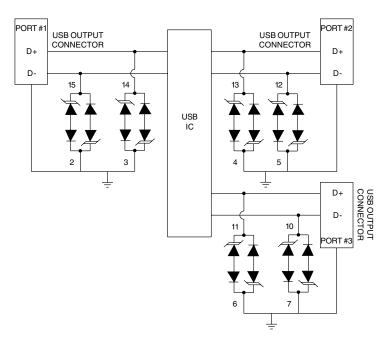
- ✓ Pins 2, 3, 4, 5, 6, and 7 are connected to ground.
- Pins 15 and 14 connected to Port #1 D- and D+.
- ✓ Pins 13 and 12 connected to Port #2 D+ and D-.
- ✔ Pins 11 and 10 connected to Port #3 D+ and D-.

CIRCUIT BOARD LAYOUT RECOMMENDATIONS

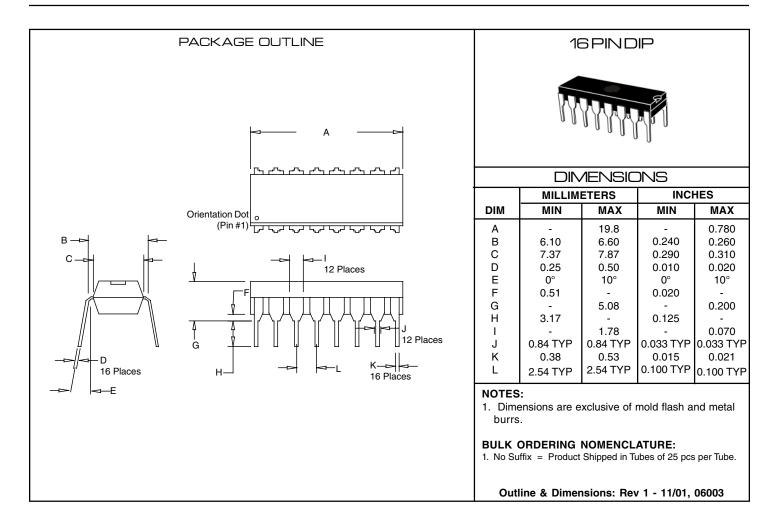
Circuit board layout is critical for Electromagnetic Compatibility (EMC) protection. The following guidelines are recommended:

- The protection device should be placed near the input terminals or connectors. By placing the TVS close to the connectors, the device will divert the transient current immediately before it can be coupled into the nearby traces.
- ✓ The path length between the TVS devices and the protected line should be minimized
- ✓ All conductive loops including power and ground loops should be minimized
- The transient current return path to ground should be kept as short as possible to reduce parasitic inductance.
- ✔ Ground planes should be used whenever possible. For Multilayer PCBs, use ground vias.

Figure 1. Typical Common-Mode USB Protection Circuit



PACKAGE OUTLINE & DIMENSIONS



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