查询BUK6211-75C BUK6211-75C

N-channel TrenchMOS FET Rev. 02 — 28 September 2010

Product data sheet

Product profile 1.

1.1 General description

Standard and logic level gate drive N-channel enhancement mode Field-Effect Transistor (FET) in a plastic package using advanced TrenchMOS technology. This product has been designed and qualified to the appropriate AEC Q101 standard for use in high performance automotive applications.

1.2 Features and benefits

- AEC Q101 compliant
- Suitable for intermediate level gate drive sources

1.3 Applications

- 12 V Automotive systems
- Electric and electro-hydraulic power steering
- Engine management
- Motors, lamps and solenoid control

1.4 Quick reference data

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- Suitable for thermally demanding environments due to 175 °C rating WWW.DZSC.COM
- Start-Stop micro-hybrid applications
- Transmission control
- Ultra high performance power switching

Table 1.	Quick reference data					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{DS}	drain-source voltage	T _j ≥ <mark>25 °C; T_j ≤ 175 °C</mark>	-	-	75	V
ID	drain current	V _{GS} = 10 V; T _{mb} = 25 °C; see <u>Figure 1</u>	-	-	74	A
Ptot	total power dissipation	T _{mb} = 25 °C; see <u>Figure 2</u>	-	-	158	W
Static cha	aracteristics					
R _{DSon}	drain-source on-state resistance	V _{GS} = 10 V; I _D = 25 A; T _i = 25 °C; see Figure 11	-	9.3	11	mΩ





Table 1.	Quick reference data	continued				
Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
Avalanche	e ruggedness					
E _{DS(AL)S}	non-repetitive drain-source avalanche energy	$ \begin{split} &I_D = 74 \text{ A}; \text{V}_{\text{sup}} \leq 75 \text{ V}; \\ &R_{\text{GS}} = 50 \Omega; \text{V}_{\text{GS}} = 10 \text{ V}; \\ &T_{j(\text{init})} = 25 ^{\circ}\text{C}; \text{ unclamped} \end{split} $	-	-	127	mJ
Dynamic of	characteristics					
Q_{GD}	gate-drain charge	$I_D = 25 \text{ A}; V_{DS} = 60 \text{ V};$ $V_{GS} = 10 \text{ V}; \text{ see } \frac{\text{Figure } 13}{\text{See } \frac{\text{Figure } 14}{\text{Figure } 14}}$	-	30	-	nC

2. Pinning information

Table 2.	Pinning	g information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate		-
2	D	drain	mb	
3	S	source		
mb	D	mounting base; connected to drain		mbb076 S
			SOT428 (DPAK)	

3. Ordering information

Table 3. Ordering information						
Type number	Package					
	Name	Description	Version			
BUK6211-75C	DPAK	plastic single-ended surface-mounted package (DPAK); 3 leads (one lead cropped)	SOT428			

4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{DS}	drain-source voltage	T _j ≥ 25 °C; T _j ≤ 175 °C		-	75	V
V _{GS}	gate-source voltage	DC	<u>[1]</u>	-16	16	V
		Pulsed	[2]	-20	20	V
I _D	drain current	T_{mb} = 25 °C; V_{GS} = 10 V; see Figure 1		-	74	А
		T_{mb} = 100 °C; V_{GS} = 10 V; see Figure 1		-	52	А
I _{DM}	peak drain current	$T_{mb} = 25 \text{ °C}; t_p \le 10 \mu\text{s}; \text{ pulsed};$ see <u>Figure 3</u>		-	297	A
P _{tot}	total power dissipation	T _{mb} = 25 °C; see <u>Figure 2</u>		-	158	W
T _{stg}	storage temperature			-55	175	°C
Tj	junction temperature			-55	175	°C
Source-drain	n diode					
ls	source current	T _{mb} = 25 °C		-	74	А
I _{SM}	peak source current	$t_p \le 10 \ \mu s$; pulsed; $T_{mb} = 25 \ ^{\circ}C$		-	297	А
Avalanche ru	uggedness					
E _{DS(AL)S}	non-repetitive drain-source avalanche energy	I_D = 74 A; $V_{sup} \le$ 75 V; R_{GS} = 50 Ω; V_{GS} = 10 V; $T_{j(init)}$ = 25 °C; unclamped		-	127	mJ
E _{DS(AL)R}	repetitive drain-source avalanche energy		[3][4][5]	-	-	J

[1] -16V accumulated duration not to exceed 168 hrs.

[2] Accumulated pulse duration not to exceed 5 mins.

[3] Single-pulse avalanche rating limited by maximum junction temperature of 175 °C.

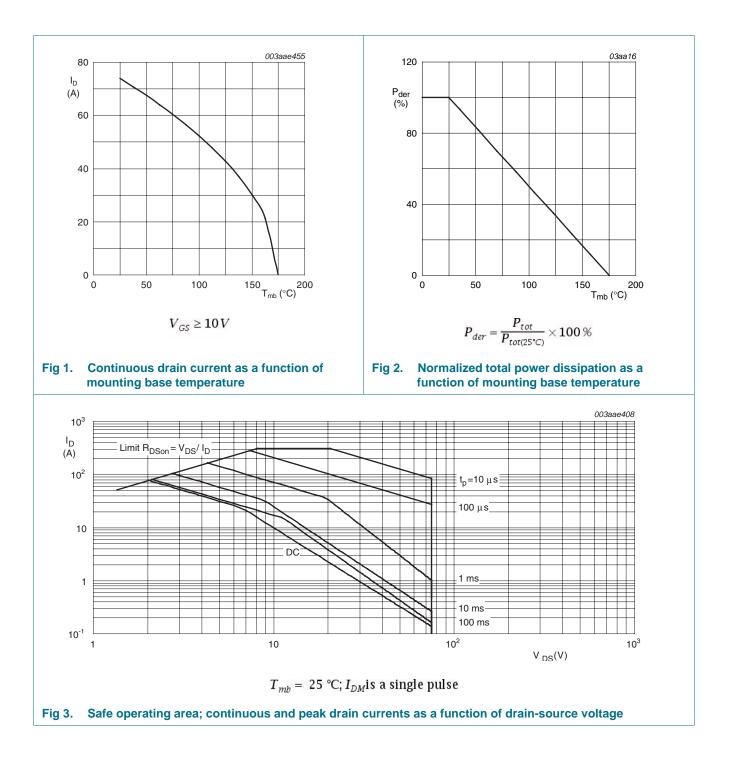
[4] Repetitive avalanche rating limited by an average junction temperature of 170 °C.

[5] Refer to application note AN10273 for further information.

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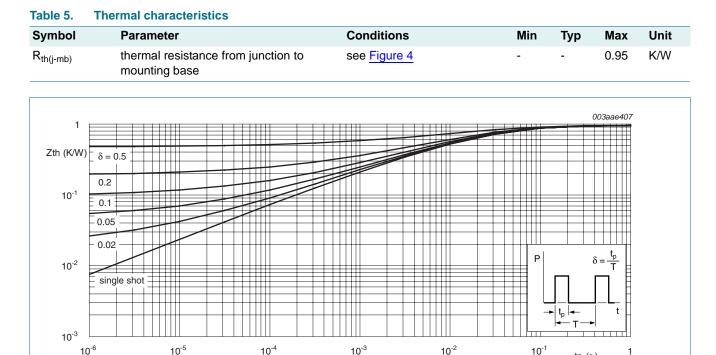
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tp (s)

Thermal characteristics 5.



Transient thermal impedance from junction to mounting base as a function of pulse duration Fig 4.

10⁻⁴

10⁻⁶

6. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	cteristics					
V _{(BR)DSS} drain-source voltage	drain-source breakdown	I _D = 250 μA; V _{GS} = 0 V; T _j = 25 °C	75	-	-	V
	voltage	$I_D = 250 \ \mu\text{A}; \ V_{GS} = 0 \ V; \ T_j = -55 \ ^\circ\text{C}$	68	-	-	V
V _{GS(th)}	gate-source threshold voltage	$I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = 25 \text{ °C};$ see <u>Figure 9</u> ; see <u>Figure 10</u>	1.8	2.3	2.8	V
		$I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = -55 \text{ °C};$ see Figure 9	-	-	3.3	V
		$I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = 175 \text{ °C};$ see Figure 9	0.8	-	-	V
I _{DSS}	drain leakage current	$V_{DS} = 75 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 175 \text{ °C}$	-	-	500	μΑ
		$V_{DS} = 75 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	0.02	1	μA
I _{GSS}	gate leakage current	$V_{DS} = 0 \text{ V}; V_{GS} = 20 \text{ V}; T_j = 25 \text{ °C}$	-	5	100	nA
		V_{DS} = 0 V; V_{GS} = -20 V; T_j = 25 °C	-	5	100	nA
R _{DSon} drain-source on-state resistance		V _{GS} = 10 V; I _D = 25 A; T _j = 25 °C; see <u>Figure 11</u>	-	9.3	11	mΩ
		V _{GS} = 4.5 V; I _D = 25 A; T _j = 25 °C; see <u>Figure 11</u>	-	11	15	mΩ
		V _{GS} = 5 V; I _D = 25 A; T _j = 25 °C; see <u>Figure 11</u>	-	10.4	13.2	mΩ
		V _{GS} = 10 V; I _D = 25 A; T _j = 175 °C; see <u>Figure 12</u>	-	-	28.6	mΩ
Dynamic ch	aracteristics					
Q _{G(tot)}	total gate charge	$I_D = 25 \text{ A}; V_{DS} = 60 \text{ V}; V_{GS} = 5 \text{ V};$ see Figure 13; see Figure 14	-	52	-	nC
		$I_D = 25 \text{ A}; V_{DS} = 60 \text{ V}; V_{GS} = 10 \text{ V};$ see Figure 14; see Figure 13	-	81	-	nC
Q _{GS}	gate-source charge	$I_D = 25 \text{ A}; V_{DS} = 60 \text{ V}; V_{GS} = 10 \text{ V};$	-	11	-	nC
Q _{GD}	gate-drain charge	see <u>Figure 13;</u> see <u>Figure 14</u>	-	30	-	nC
C _{iss}	input capacitance	$V_{GS} = 0 V; V_{DS} = 25 V; f = 1 MHz;$	-	3938	5251	pF
C _{oss}	output capacitance	$T_j = 25 \text{ °C}; \text{ see } \frac{\text{Figure } 16}{100}$	-	310	372	pF
C _{rss}	reverse transfer capacitance		-	206	282	pF
t _{d(on)}	turn-on delay time	$V_{DS} = 55 \text{ V}; \text{ R}_{L} = 2.2 \Omega; V_{GS} = 10 \text{ V};$	-	18	-	ns
tr	rise time	$R_{G(ext)} = 10 \Omega$	-	40	-	ns
t _{d(off)}	turn-off delay time		-	165	-	ns
t _f	fall time		-	80	-	ns
LD	internal drain inductance	from upper edge of drain mounting base to centre of die ; $T_j = 25 \text{ °C}$	-	3.5	-	nH
L _S	internal source inductance	from source lead to source bond pad ; $T_i = 25 \text{ °C}$	-	7.5	-	nH

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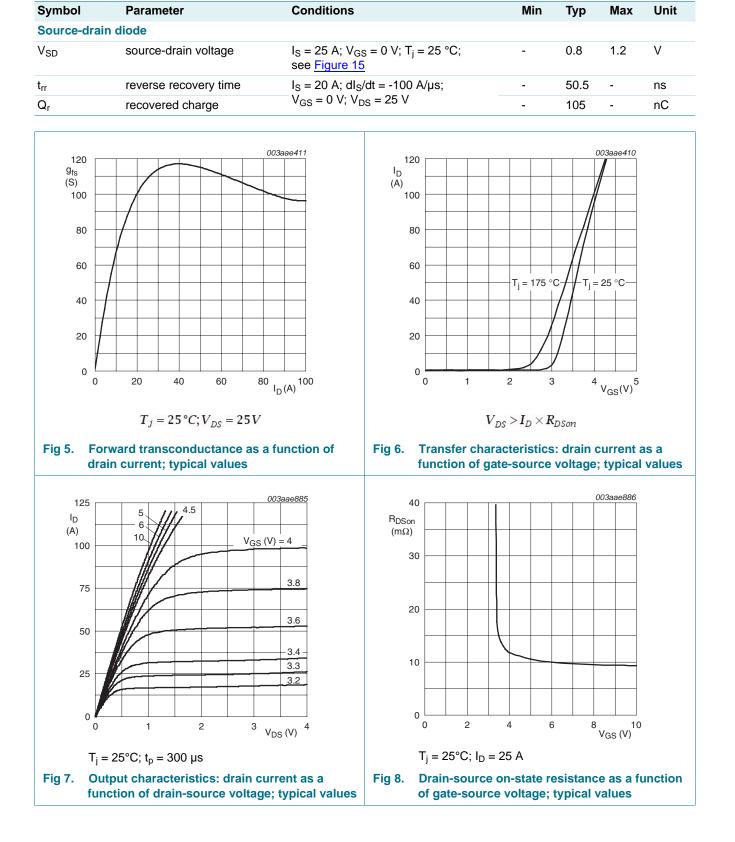
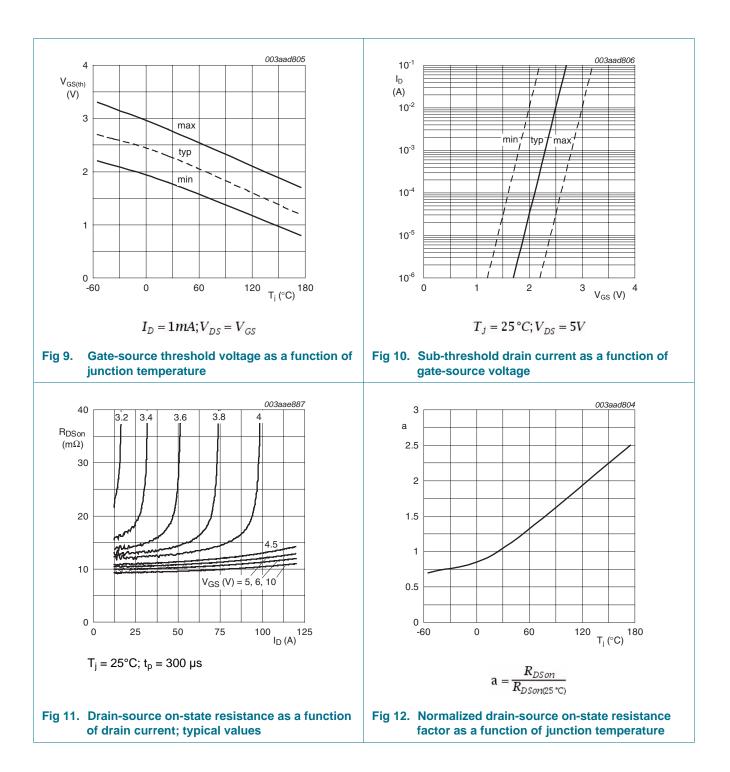


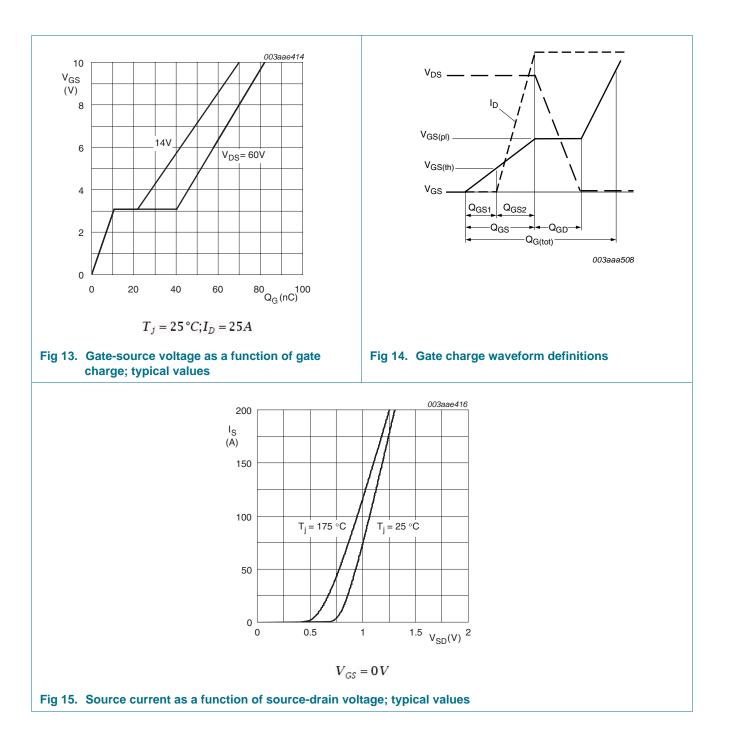
Table 6. Characteristics ...continued

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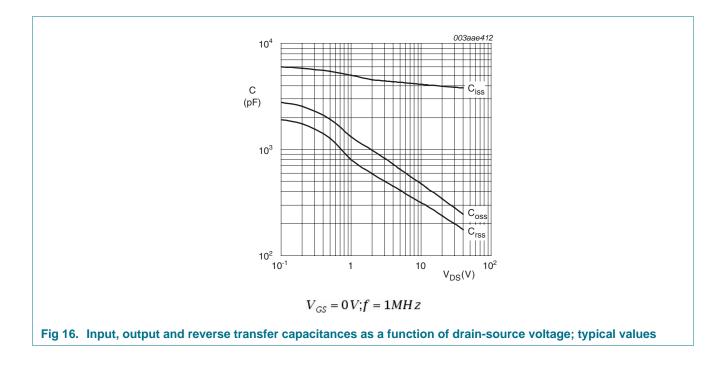
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7. Package outline

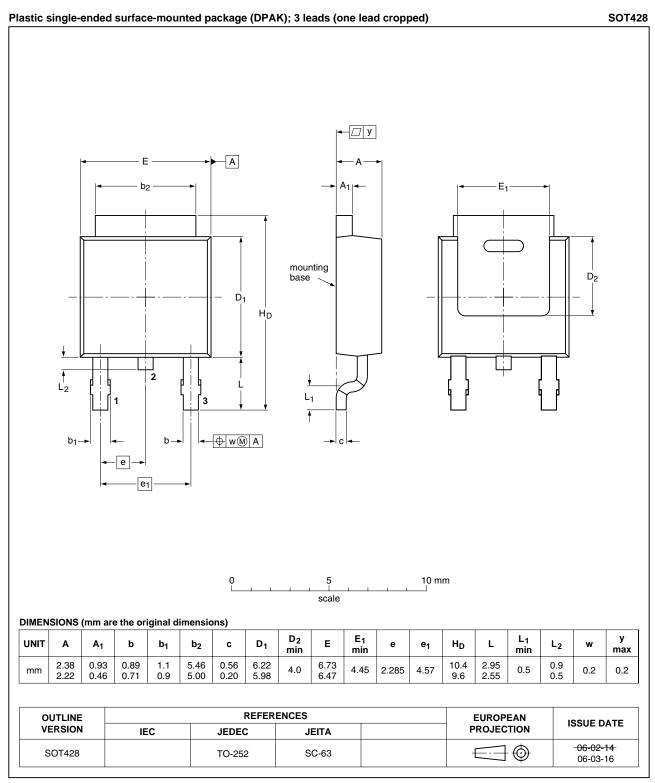


Fig 17. Package outline SOT428 (DPAK)

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8. Revision history

Table 7.Revision	history			
Document ID	Release date	Data sheet status	Change notice	Supersedes
BUK6211-75C v.2	20100928	Product data sheet	-	BUK6211-75C v.1
Modifications:	 Status change 	d from objective to product.		
	 Various chang 	es to content.		
BUK6211-75C v.1	20100908	Objective data sheet	-	-

9. Legal information

9.1 Data sheet status

Document status[1][2]	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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[2] The term 'short data sheet' is explained in section "Definitions".

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