

# LM2747 Evaluation Board

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# LM2747 Evaluation Board

National Semiconductor  
Application Note 1449  
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April 2006



## Introduction

This application notes describes the LM2747 printed circuit board (PCB) design and provides an example typical application circuit. The demo board allows component design flexibility in order to demonstrate the versatility of the LM2747 IC.

The demo board contains a voltage-mode, high-speed synchronous buck regulator controller. Though the control sections of the IC are rated for 3 to 6V ( $V_{CC}$ ), the driver sections are designed to accept input supply rails ( $V_{IN}$ ) as high as 14V.

The demo board design regulates to an output voltage of 1.2V at 3.5A with a switching frequency of 1MHz from a 1 MHz clock source that has an amplitude from 0V to  $V_{CC}$ . Note, the demo board is optimized for a 1MHz, 14V input voltage compensation design with  $V_{CC} = 3.3V$ . If a slower switching frequency and input voltage is desired, please consult the device data sheet for control loop compensation procedures. For additional design modifications refer to the Design Consideration section of the LM2747/8 data sheet.

The demo board accommodates the use of banana clips to clip onto pads on the board, if preferred, the pads inner diameters are 100mils, for which a solder terminal can be placed (Newark 40F6004). The PCB is designed on two layers with 1oz. copper on a 62mil FR4 laminate.

## Additional Footprints

An additional footprint D1 is available for a Schottky diode to be placed in parallel with the low side MOSFET. This component can improve efficiency, due to the lower forward drop than the low side MOSFET body diode conducting during the anti-shoot through period. Select a Schottky diode that maintains a forward drop around 0.4 to 0.6V at the maximum load current (consult the I-V curve). In addition select the reverse breakdown voltage to have sufficient margin above the maximum input voltage.

Footprint C13 is available for a multilayer ceramic capacitor (MLCC) connected flush to the source of the low side MOSFET and drain of the high side MOSFET, in order to provide low supply impedance. For example, component C13 is used in combination with aluminum electrolytic input filter capacitors, placed in designators C12 and C14. If MLCCs are used in designators C12 and C14 component C13 is not necessary.

## Typical Application Circuit

The typical application circuit in figure 1 provides the component designators used on the demo board.

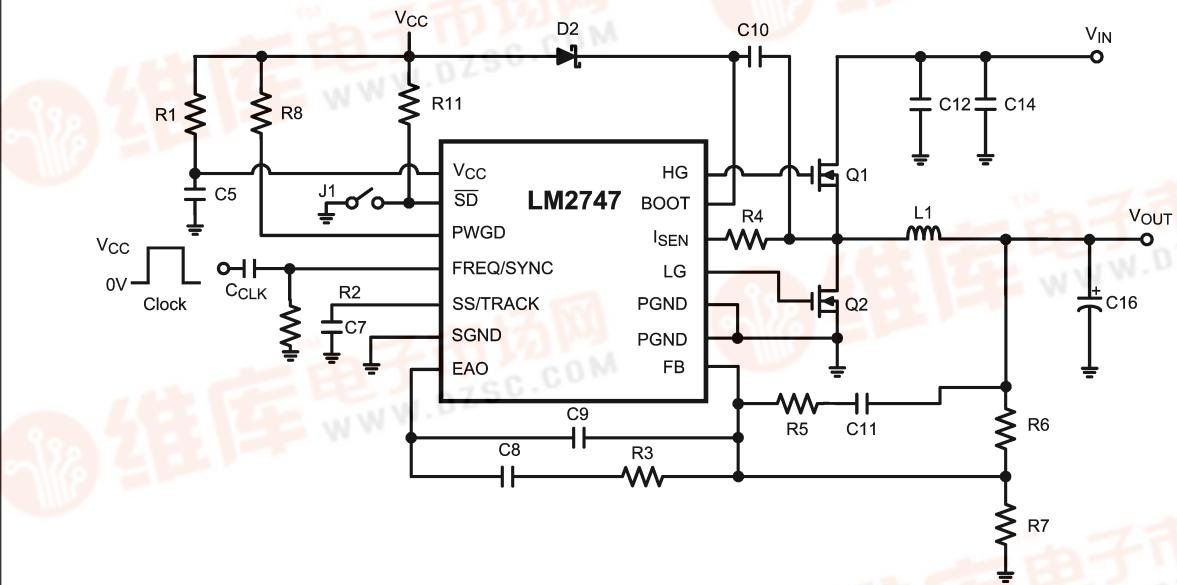
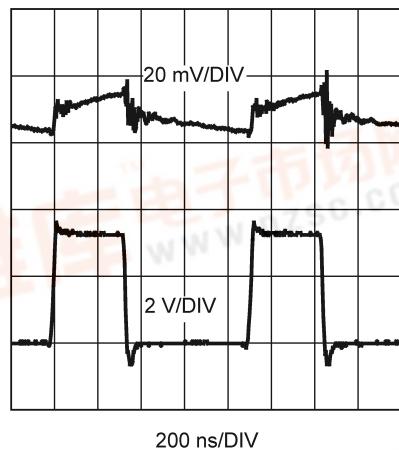


FIGURE 1. Typical Application

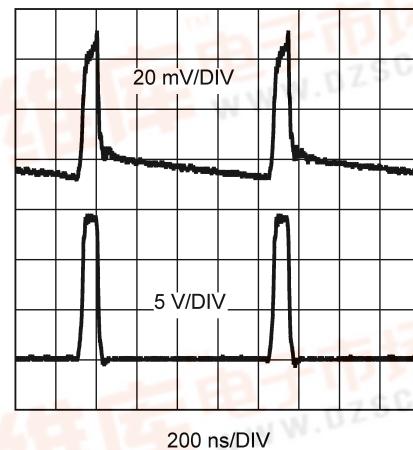
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## Performance Characteristics

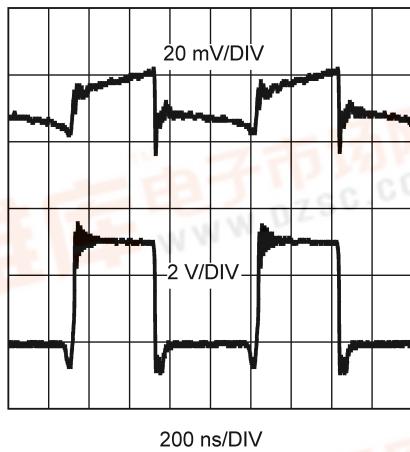
(Output Ripple Voltage and Switch Node Voltage)



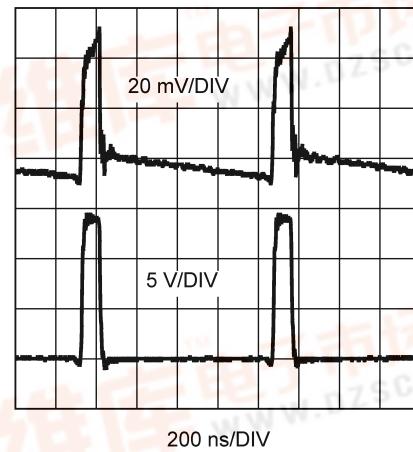
**FIGURE 2.**  $V_{IN} = V_{CC} = 3.3V$ ,  
 $V_{OUT} = 1.2V$ ,  $I_{LOAD} = 0A$ ,  $f_{SW} = 1MHz$ .  
20 MHz Bandwidth Limit



**FIGURE 4.**  $V_{IN} = 14V$ ,  $V_{CC} = 5V$ ,  
 $V_{OUT} = 1.2V$ ,  $I_{LOAD} = 0A$ ,  $f_{SW} = 1MHz$ .  
20 MHz Bandwidth Limit

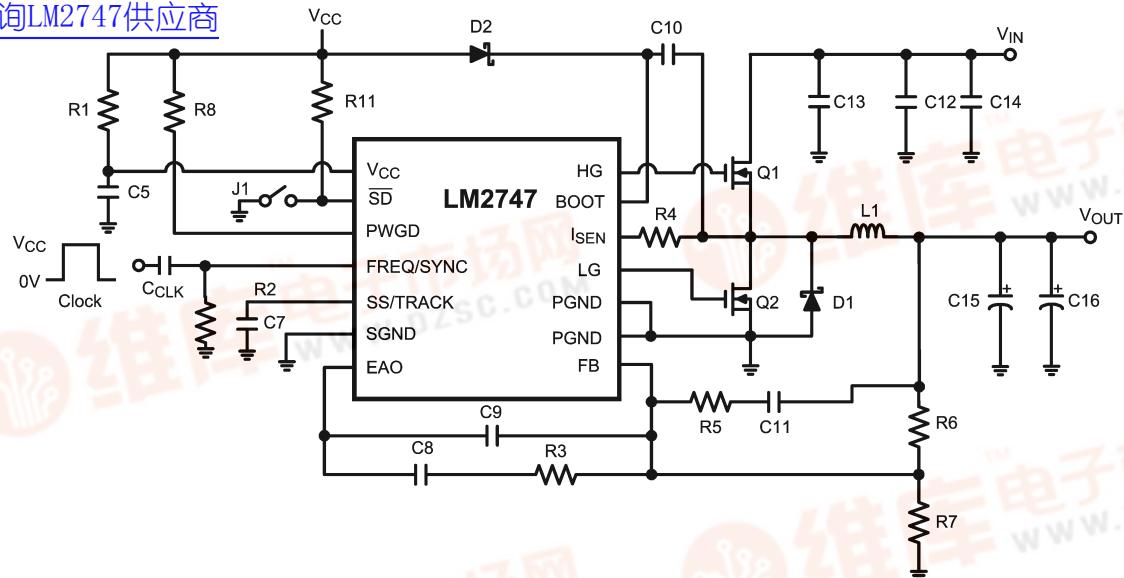


**FIGURE 3.**  $V_{IN} = V_{CC} = 3.3V$ ,  
 $V_{OUT} = 1.2V$ ,  
 $I_{LOAD} = 3.5A$ ,  $f_{SW} = 1MHz$ .  
20 MHz Bandwidth Limit



**FIGURE 5.**  $V_{IN} = 14V$ ,  $V_{CC} = 5V$ ,  
 $V_{OUT} = 1.2V$ ,  $I_{LOAD} = 3.5A$ ,  $f_{SW} = 1MHz$ .  
20 MHz Bandwidth Limit

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FIGURE 6. Complete Demo Board Schematic

## Bill of Materials

Designator	Function	Part Description	Part Number
U1	Controller	LM2747 TSSOP14	National Semiconductor
C5	VCC Decoupling	Cer Cap 1 $\mu$ F 25V 10% 0805	Murata GRM216R61E105KA12B
C7	Soft Start Cap	Cer Cap 12 nF 25V 10% 0805	Vishay VJ0805Y123KXX
C8	Comp Cap	Cer Cap 1.5 nF 25V 10% 0805	Vishay VJ0805Y152KXX
C9	Comp Cap	Cer Cap 18 pF 25V 10% 0805	Vishay VJ0805A180KAA
C10	Cboot	Cer Cap 0.1 $\mu$ F 25V 10% 0805	Vishay VJ0805Y104KXX
C11	Comp Cap	Cer Cap 1.8nF 25V 10% 0805	Vishay VJ0805Y182KXX
C12	Input Filter Cap	Cer Cap 10 $\mu$ F 25V 10% 1210	AVX 12103D106MAT
C14	Input Filter Cap	Cer Cap 10 $\mu$ F 25V 10% 1210	AVX 12103D106MAT
C15	Output Filter Cap	470 $\mu$ F, 6.3V, 10m $\Omega$ ESR POScap	Sanyo 6TPD470
R1	Filter Resistor	Res 10 $\Omega$ .25W 0805	Vishay CRCW08051000F
R2	Frequency Adjust Res	Res 18.7k $\Omega$ .25W 0805	Vishay CRCW08052187F
R3	Comp Res	Res 17.4k $\Omega$ .25W 0805	Vishay CRCW08051742F
R4	Current Limit Res	Res 3.16k $\Omega$ .25W 0805	Vishay CRCW08053161F
R5	Comp Res	Res 2.94k $\Omega$ .25W 0805	Vishay CRCW08052941F
R6	Res Divider, upper	Res 10.0k $\Omega$ .25W 0805	Vishay CRCW08051002F
R7	Res Divider, lower	Res 10.0k $\Omega$ .25W 0805	Vishay CRCW08051002F
R8	PWGD Pull-Up	Res 100k $\Omega$ .25W 0805	Vishay CRCW08051003F
R11	Shutdown Pull-Up	Res 100k $\Omega$ .25W 0805	Vishay CRCW080561003F
D2	Bootstrap Diode	Schottky Diode, SOD-123	MBR0530LTI
L1	Output Filter Inductor	Inductor 1 $\mu$ H, 5.3Arms, 10.2m $\Omega$	Cooper DR73-1R0
Q1-Q2	Top and Bottom FETs	Dual N-MOSFET, $V_{DS} = 20V$ , 24m $\Omega$ @ 2.5V	Vishay 9926BDY
C <sub>CLK</sub>	Sync AC Coupling Cap	Cer Cap 56 pF 25V 10% 0805	Vishay VJ0805A560KXAA

## PCB Layout Diagrams

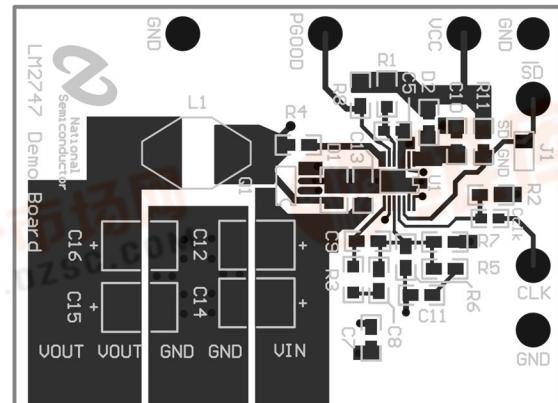
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FIGURE 7. Top Layer and Top Overlay

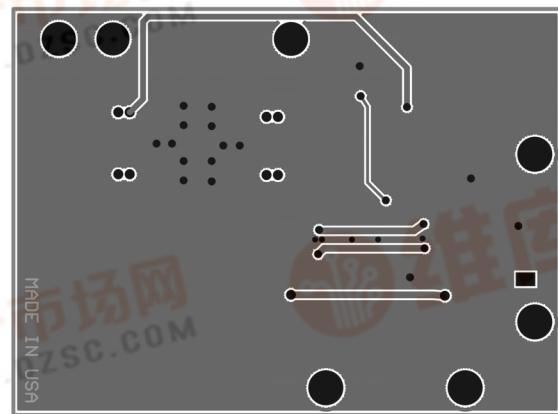


FIGURE 8. Bottom Layer

## Notes

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