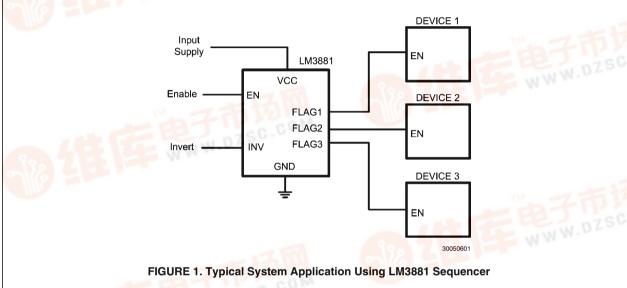
LM3881 Power Sequencer Evaluation Board

National Semiconductor Application Note 1785 Timothy Hegarty August 27, 2009



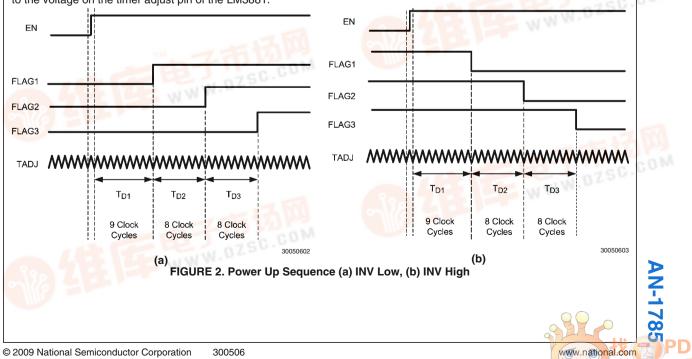
Introduction

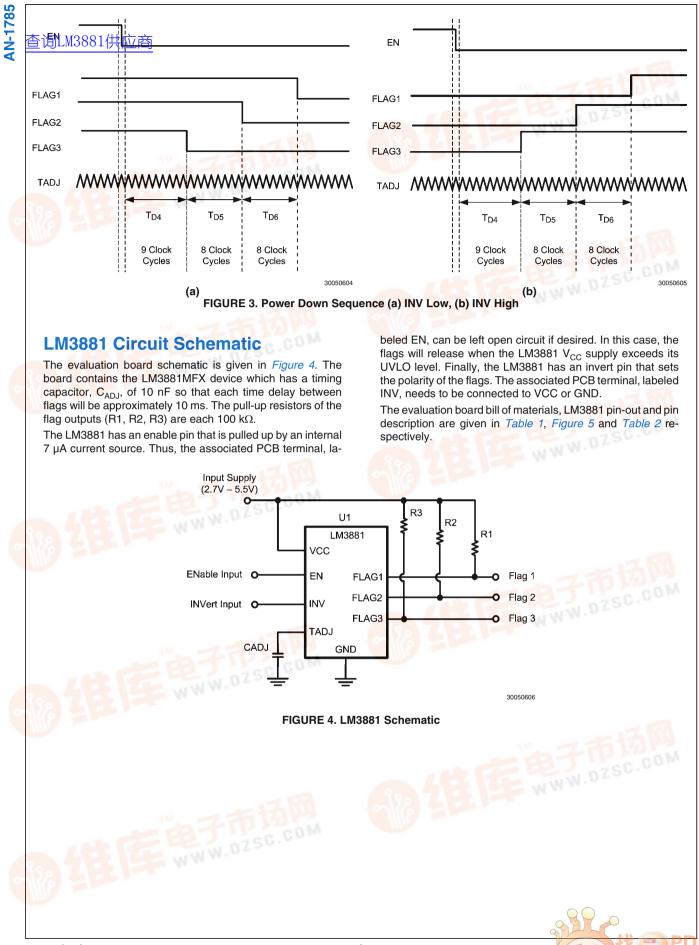
The LM3881 evaluation board has been designed to permit the designer to connect it directly to the Enable or Remote ON/OFF pins of power supply devices of an existing system to facilitate system sequencing. The block diagram of a typical system application is given in *Figure 1*. Upon enabling the device, the three open drain output flags will rise in sequential order, 1-2-3. Once the part is disabled, the shutdown sequence will occur in reverse order 3-2-1. Therefore the last power supply that started up will be the first to shutdown.



Timing Sequence

Figure 2 and *Figure 3* present the power up and power down timing sequence of the output flags with the INV at logic level low and high respectively. The waveform labeled TADJ refers to the voltage on the timer adjust pin of the LM3881.





LM3881 Evaluation Board Bill of Materials 查询LM3881供应商

TABLE 1.

Ref Des	Description	Case Size	Manufacturer	Manufacturer P/N
U1	LM3881 Sequencer	MSOP-8	National Semiconductor	LM3881MFX
R1	100 kΩ	0603	Vishay Dale	CRCW06031003F-e3
R2	100 kΩ	0603	Vishay Dale	CRCW06031003F-e3
R3	100 kΩ	0603	Vishay Dale	CRCW06031003F-e3
CADJ	10 nF ±10% X7R 16V	0603	Murata	GRM188R71C103KA01

LM3881 Pin-Out

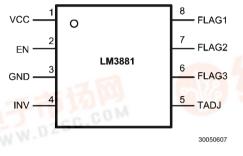


FIGURE 5. LM3881 Pin-Out

LM3881 Pin Descriptions

TABLE 2.

Pin #	Name	Function
1	VCC	Input Supply
2	EN	Precision Enable
3	GND	Ground
4	INV	Output Logic Invert
5	TADJ	Timer Adjust
6	FLAG3	Open Drain Output #3
7	FLAG2	Open Drain Output #2
8	FLAG1	Open Drain Output #1

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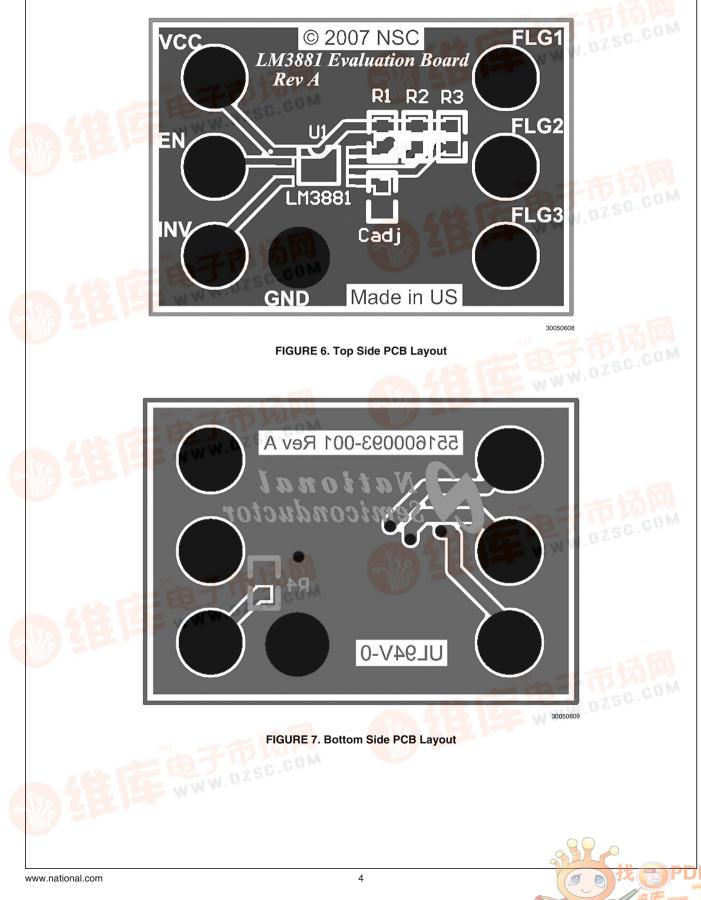
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PC Board Layout

前正3881任(京) The evaluation board is based on a small 1.09" x 0.76" FR4 PCB with two layers of copper. The actual layout can be seen in *Figure 6* and *Figure 7* below. When looking at the top layer, pin 1 of the LM3881 is on the upper left. An optional component, assigned reference designator R4, is placed on the bottom side of the PCB to facilitate connection of INV to GND.

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Notes

Pr	oducts	Design Support		
Amplifiers	www.national.com/amplifiers	WEBENCH® Tools	www.national.com/webench	
Audio	www.national.com/audio	App Notes	www.national.com/appnotes	
Clock and Timing	www.national.com/timing	Reference Designs	www.national.com/refdesigns	
Data Converters	www.national.com/adc	Samples	www.national.com/samples	
Interface	www.national.com/interface	Eval Boards	www.national.com/evalboards	
LVDS	www.national.com/lvds	Packaging	www.national.com/packaging	
Power Management	www.national.com/power	Green Compliance	www.national.com/quality/green	
Switching Regulators	www.national.com/switchers	Distributors	www.national.com/contacts	
LDOs	www.national.com/Ido	Quality and Reliability	www.national.com/quality	
LED Lighting	www.national.com/led	Feedback/Support	www.national.com/feedback	
Voltage Reference	www.national.com/vref	Design Made Easy	www.national.com/easy	
PowerWise® Solutions	www.national.com/powerwise	Solutions	www.national.com/solutions	
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