RICOH

RP153L SERIES

150mA DUAL LDO REGULATOR WITH 2 INPUT PINS

NO. EA-201-100112

OUTLINE

The RP153L Series are CMOS-based voltage regulator ICs with high output voltage accuracy, low supply current, low dropout, and high ripple rejection. Each of these voltage regulator ICs consists of a voltage reference unit, an error amplifier, resistors for setting output voltage, a current limit circuit, and a chip enable circuit.

These ICs perform with low dropout voltage due to built-in transistor with low ON resistance, and a chip enable function prolongs the battery life of each system. The line transient response and load transient response of the RP153L Series are excellent, thus these ICs are very suitable for the power supply for hand-held communication equipment.

The output voltage of these ICs is internally fixed with high accuracy. Since the package for these ICs are DFN1216-8, dual LDO regulators are included in each package are high density mounting of the ICs on boards is possible.

In RP153L, the power supply of each circuit can be individually supplied. The transient response characteristic of D and E Version is improved.

FEATURES

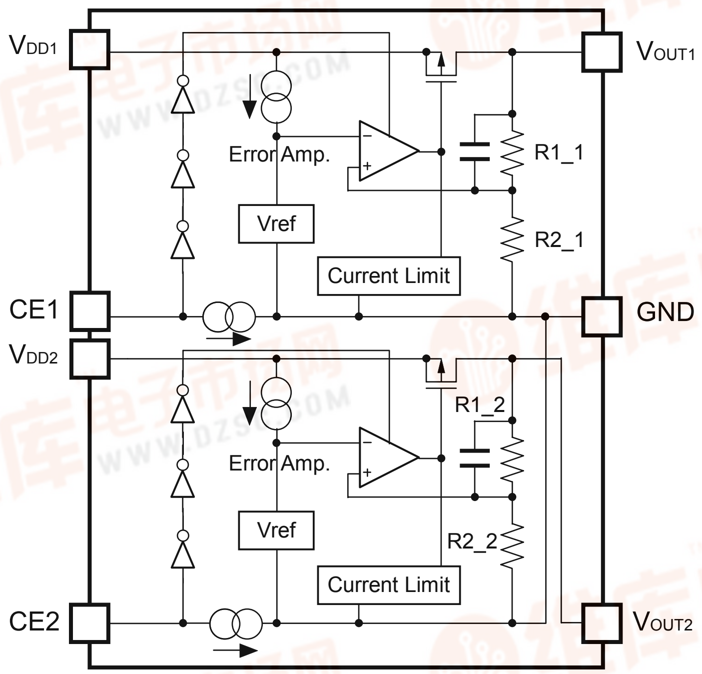
- Supply Current .... Typ. 40μA×2 (VR1&VR2)
- Supply Current (D/E Version) .... Typ. 85μA×2 (VR1&VR2)
- Standby Current .... Typ. 0.1μA×2 (VR1&VR2)
- Ripple Rejection .... Typ. 70dB (f=1kHz)
- Input Voltage Range .... 1.4V to 5.25V
- Output Voltage Range .... 0.8V to 3.6V (0.1V steps)
  (For details, please refer to MARK INFORMATIONS.)
- Output Voltage Accuracy .... ±1.0% (V_{OUT}>2.0V, T_{opt}=25°C)
- Temperature-Drift Coefficient of Output Voltage .... Typ. ±80ppm/°C
- Dropout Voltage .... Typ. 0.22V (I_{OUT}=150mA, V_{OUT}=2.8V)
- Line Regulation .... Typ. 0.02%/V
- Packages DFN1216-8
- Built-in Fold Back Protection Circuit .... Typ. 40mA (Current at short mode)
- Ceramic capacitors are recommended to be used with this IC .... 0.22μF or more

APPLICATIONS

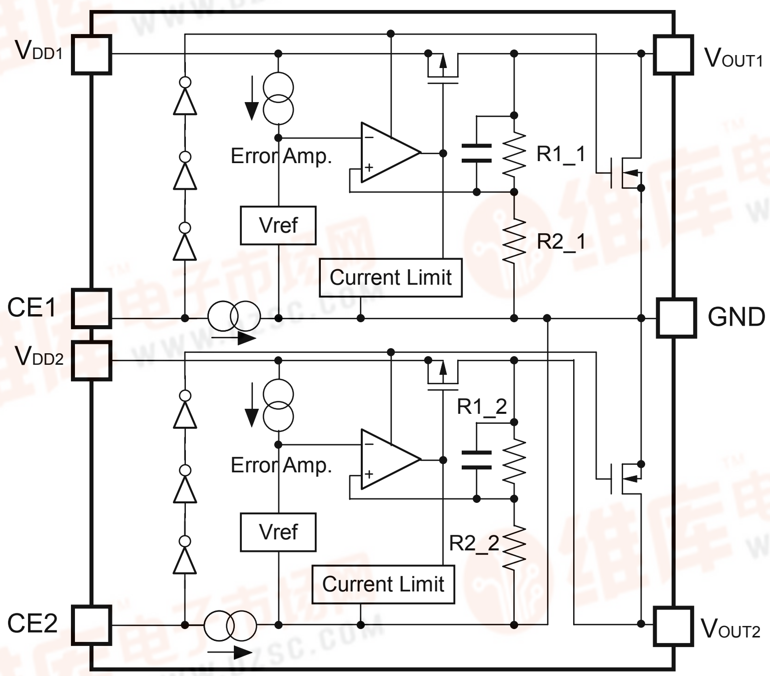
- Power source for portable communication equipment.
- Power source for electrical appliances such as cameras, VCRs and camcorders.
- Power source for battery-powered equipment.

BLOCK DIAGRAMS

RP153LxxxA/D



RP153LxxxB/E



SELECTION GUIDE

The output voltage, auto discharge function, package, and the taping type, etc. for the ICs can be selected at the user's request.

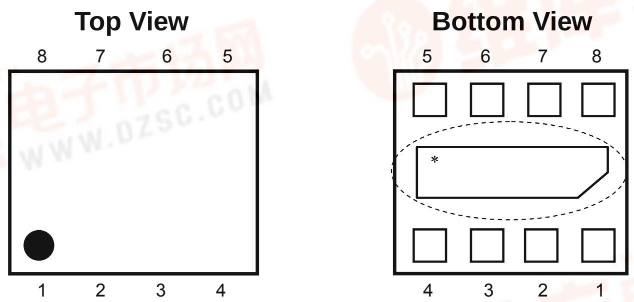
Product Name	Package	Quantity per Reel	Pb Free	Halogen Free
RP153Lxxx∗-E2	DFN1216-8	5,000 pcs	Yes	Yes

xxx: The combination of output voltage for each channel can be designated by serial numbers. (from 001)
The output voltage for each channel can be set in the range from 0.8V to 3.6V in 0.1V steps.
(For details, please refer to MARK INFORMATIONS.)

∗ : Designation of Mask Option:
(A) without auto-discharge function at off state
(B) with auto-discharge function at off state
(D) without auto-discharge function at off state, (the transient response improved type)
(E) with auto-discharge function at off state, (the transient response improved type)

PIN CONFIGURATIONS

- **DFN1216-8**



PIN DESCRIPTIONS

- **DFN1216-8**

Pin No.	Symbol	Description
1	GND	Ground Pin
2	V_{OUT1}	Output Pin 1
3	V_{OUT2}	Output Pin 2
4	GND	Ground Pin
5	CE2	Chip Enable Pin 2 ("H" Active)
6	V_{DD2}	Input Pin 2
7	V_{DD1}	Input Pin 1
8	CE1	Chip Enable Pin 1 ("H" Active)

∗) Tab is GND level. (They are connected to the reverse side of this IC.)
The tab is better to be connected to the GND, but leaving it open is also acceptable.

ABSOLUTE MAXIMUM RATINGS

Symbol	Item	Rating	Unit
V_{IN}	Input Voltage	6.0	V
V_{CE}	Input Voltage (CE Pin)	−0.3 to 6.0	V
V_{OUT1}, V_{OUT2}	Output Voltage	−0.3 to V_{IN}+0.3	V
I_{OUT1}, I_{OUT2}	Output Current	180	mA
P_D	Power Dissipation (DFN1216-8)*	625	mW
Topt	Operating Temperature Range	−40 to 85	°C
Tstg	Storage Temperature Range	−55 to 125	°C

*) For Power Dissipation, please refer to PACKAGE INFORMATION.

ABSOLUTE MAXIMUM RATINGS

Electronic and mechanical stress momentarily exceeded absolute maximum ratings may cause the permanent damages and may degrade the life time and safety for both device and system using the device in the field. The functional operation at or over these absolute maximum ratings is not assured.

ELECTRICAL CHARACTERISTICS

- **RP153L**

V_{IN}=Set V_{OUT}+1.0V (V_{OUT}>1.5V), V_{IN}=2.5V (V_{OUT}≤1.5V), I_{OUT}=1mA, C_{IN}=C_{OUT}=0.22μF, unless otherwise noted.
The specification in ☐ is checked and guaranteed by design engineering at –40°C ≤ Topt ≤ 85°C.

VR1/VR2 Topt=25°C

Symbol	Item	Conditions		Min.	Typ.	Max.	Unit
V_{OUT}	Output Voltage	Topt=25°C	V_{OUT} > 2.0V	×0.99		×1.01	V
			V_{OUT} ≤ 2.0V	–20		+20	mV
		–40°C ≤ Topt ≤ 85°C	V_{OUT} > 2.0V	×0.97		×1.03	V
			V_{OUT} ≤ 2.0V	–60		+60	mV
I_{OUT}	Output Current			150			mA
$\Delta V_{OUT}/\Delta I_{OUT}$	Load Regulation	1mA ≤ I_{OUT} ≤ 150mA	0.8V ≤ V_{OUT} < 1.1V		10	40	mV
			1.1V ≤ V_{OUT} < 1.6V		15	50	
			1.6V ≤ V_{OUT} < 2.0V		15	55	
			2.0V ≤ V_{OUT} ≤ 3.6V		15	60	
V_{DIF}	Dropout Voltage	Refer to the following table.					
I_{SS}	Supply Current	I_{OUT}=0mA	RP153LxxxA/B		40	60	μA
			RP153LxxxD/E		85	120	
$I_{standby}$	Standby Current	V_{CE}=0V			0.1	1.0	μA
$\Delta V_{OUT}/\Delta V_{IN}$	Line Regulation	Set V_{OUT}+0.5V ≤ V_{IN} ≤ 5.0V			0.02	0.10	%/V
RR	Ripple Rejection	f=1kHz, Ripple 0.2Vp-p V_{IN}=Set V_{OUT}+1V, I_{OUT}=30mA (In case that V_{OUT} ≤ 2.0V, V_{IN}=3V)			70		dB
V_{IN}	Input Voltage*			1.40		5.25	V
$\Delta V_{OUT}/\Delta Topt$	Output Voltage Temperature Coefficient	–40°C ≤ Topt ≤ 85°C			±80		ppm/°C
I_{SC}	Short Current Limit	V_{OUT}=0V			40		mA
I_{PD}	CE Pull-down Current				0.3		μA
V_{CEH}	CE Input Voltage "H"			1.0			V
V_{CEL}	CE Input Voltage "L"					0.4	V
en	Output Noise	BW=10Hz to 100kHz			60		μVrms
R_{LOW}	Low Output Nch Tr. ON Resistance (of B/E Version)	V_{IN}=4.0V, V_{CE}=0V			50		Ω

All of units are tested and specified under load conditions such that Tj≈Topt=25°C except for Output Noise, Ripple Rejection, Output Voltage Temperature Coefficient.

*) The maximum Input Voltage of the ELECTRICAL CHARACTERISTICS is 5.25V. In case of exceeding this specification, the IC must be operated on condition that the Input Voltage is up to 5.5V and the total operating time is within 500hrs.

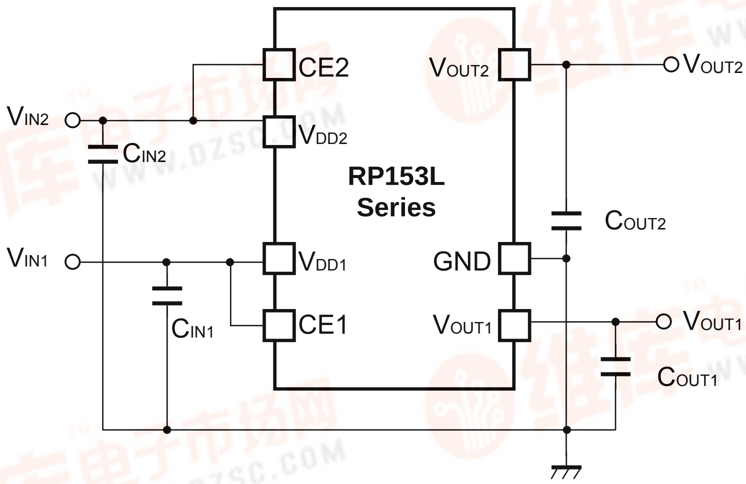
- **Dropout Voltage by Output Voltage**

Output Voltage V_{OUT} (V)	Dropout Voltage V_{DIF} (V)		
	Condition	Typ.	Max.
V_{OUT}=0.8	I_{OUT}=150mA	0.63	0.87
V_{OUT}=0.9		0.55	0.80
$1.0 \le V_{OUT} < 1.2$		0.50	0.72
$1.2 \le V_{OUT} < 1.4$		0.42	0.62
$1.4 \le V_{OUT} < 1.7$		0.37	0.55
$1.7 \le V_{OUT} < 2.1$		0.30	0.46
$2.1 \le V_{OUT} < 2.5$		0.25	0.39
$2.5 \le V_{OUT} < 3.0$		0.23	0.35
$3.0 \le V_{OUT} \le 3.6$		0.21	0.32

RECOMMENDED OPERATING CONDITIONS (ELECTRICAL CHARACTERISTICS)

All of electronic equipment should be designed that the mounted semiconductor devices operate within the recommended operating conditions. The semiconductor devices cannot operate normally over the recommended operating conditions, even if when they are used over such conditions by momentary electronic noise or surge. And the semiconductor devices may receive serious damage when they continue to operate over the recommended operating conditions.

TYPICAL APPLICATIONS



$C_{IN1}=C_{IN2}=C_{OUT1}=C_{OUT2}$=Ceramic 0.22μF
(External Components)
Murata : GRM155B31A224KE18B

TECHNICAL NOTES

When using these ICs, consider the following points:

Phase Compensation

In these ICs, phase compensation is made for securing stable operation even if the load current is varied.

For this purpose, use capacitors (0.22μF or more) for C_{OUT1} and C_{OUT2} with good frequency characteristics and ESR (Equivalent Series Resistance).

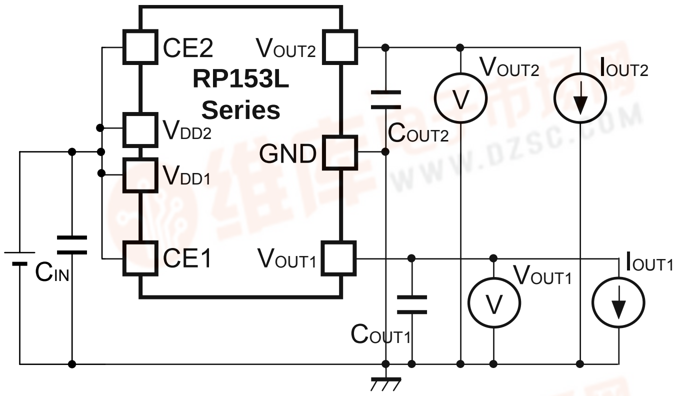
(Note: If additional ceramic capacitors are connected with parallel to the output pin with an output capacitor for phase compensation, the operation might be unstable. Because of this, test these ICs with as same external components as ones to be used on the PCB.)

PCB Layout

Make V_{DD} and GND lines sufficient. If their impedance is high, noise pickup or unstable operation may result. Connect capacitors with a capacitance value as much as 0.22μF or more between V_{DD} and GND pin, and as close as possible to the pins (C_{IN1}/C_{IN2}).

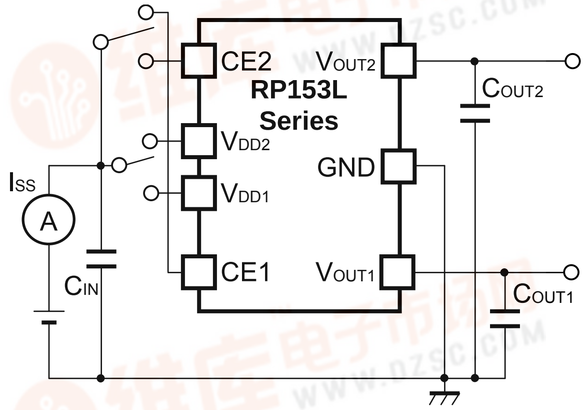
Set external components, especially the output capacitors, as close as possible to the ICs, and make wiring as short as possible (C_{OUT1}/ C_{OUT2}).

TEST CIRCUITS



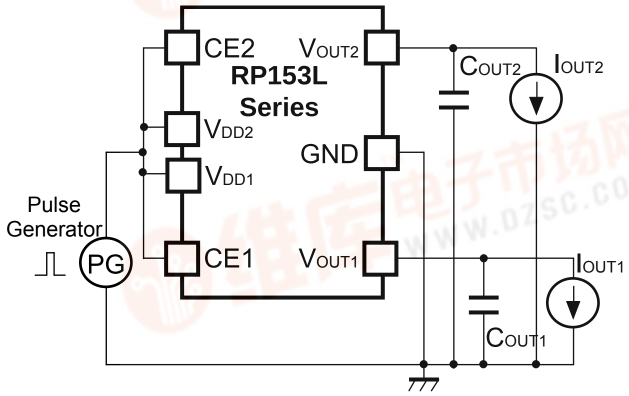
CIN=COUT1=COUT2=0.22µF

Basic Test Circuit



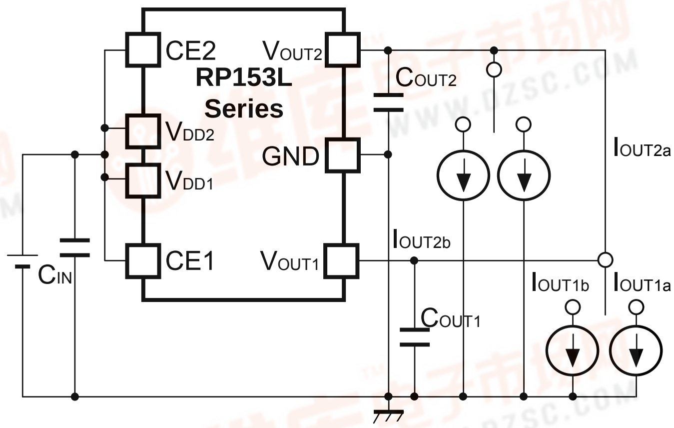
CIN=COUT1=COUT2=0.22µF

Supply Current Test Circuit



COUT1=COUT2=0.22µF

Ripple Rejection & Line Transient Response Test Circuit



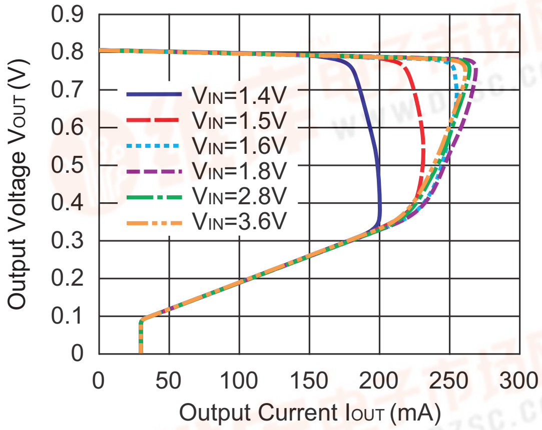
CIN=COUT1=COUT2=0.22µF

Load Transient Response Test Circuit

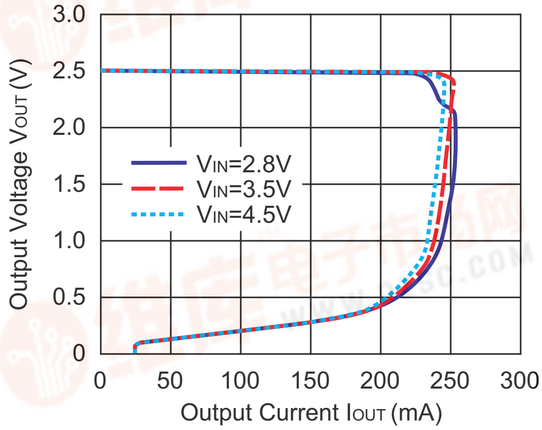
TYPICAL CHARACTERISTICS

1) Output Voltage vs. Output Current (Topt=25°C)

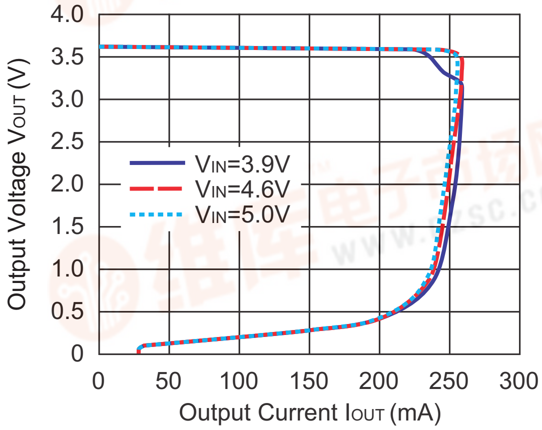
0.8V(VR1/VR2)



2.5V(VR1/VR2)

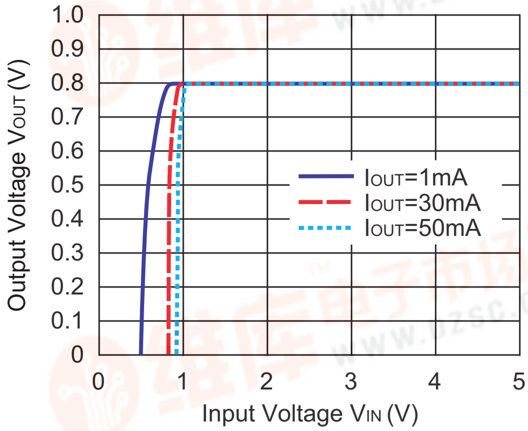


3.6V(VR1/VR2)

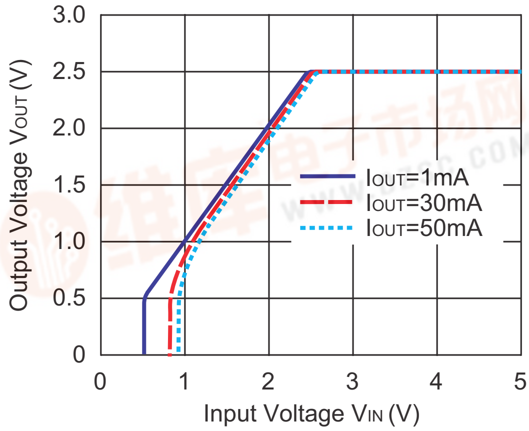


2) Output Voltage vs. Input Voltage (Topt=25°C)

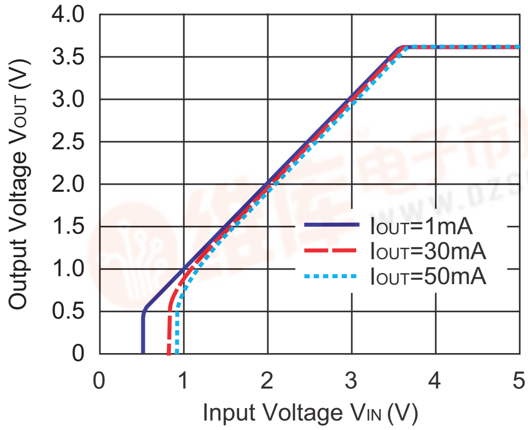
0.8V(VR1/VR2)



2.5V(VR1/VR2)

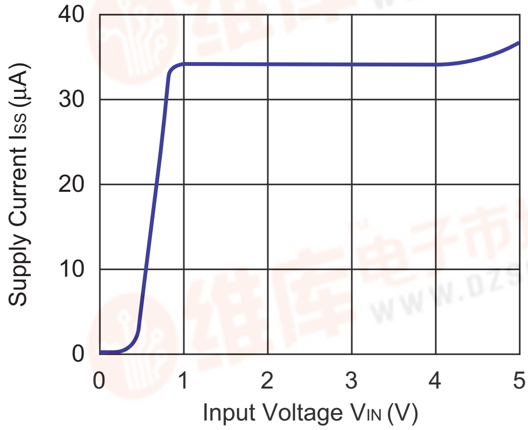


3.6V(VR1/VR2)

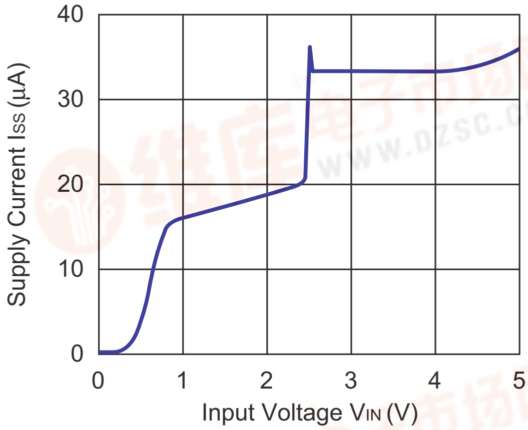


3) Supply Current vs. Input Voltage

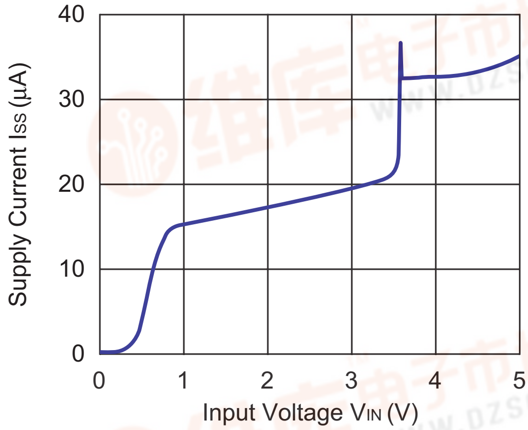
0.8V(VR1/VR2)



2.5V(VR1/VR2)

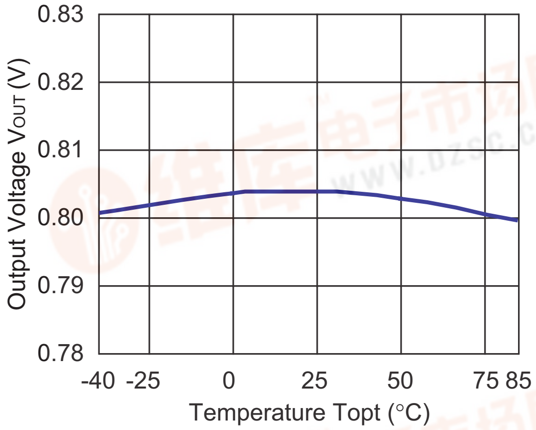


3.6V(VR1/VR2)

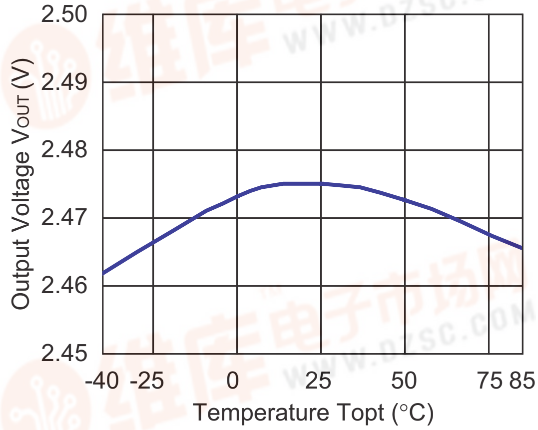


4) Output Voltage vs. Temperature

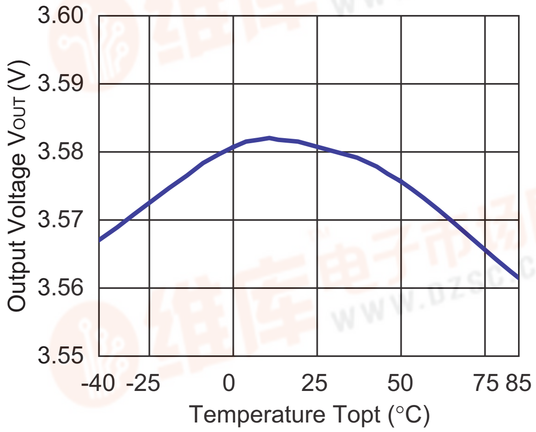
0.8V(VR1/VR2)



2.5V(VR1/VR2)

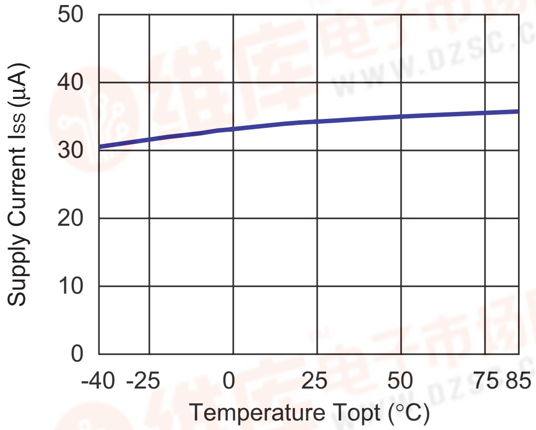


3.6V(VR1/VR2)

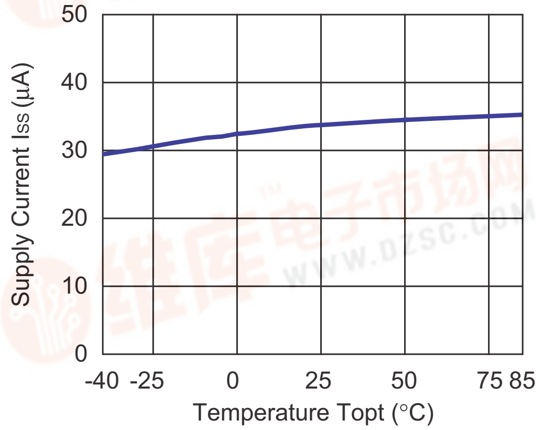


5) Supply Current vs. Temperature

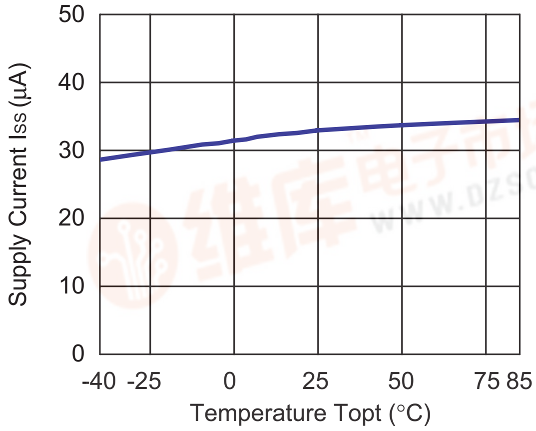
0.8V(VR1/VR2)



2.5V(VR1/VR2)

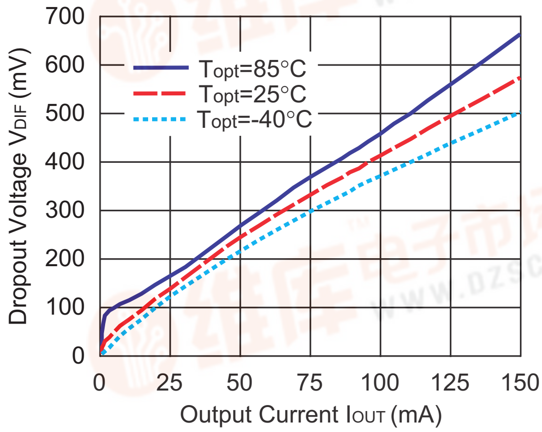


3.6V(VR1/VR2)

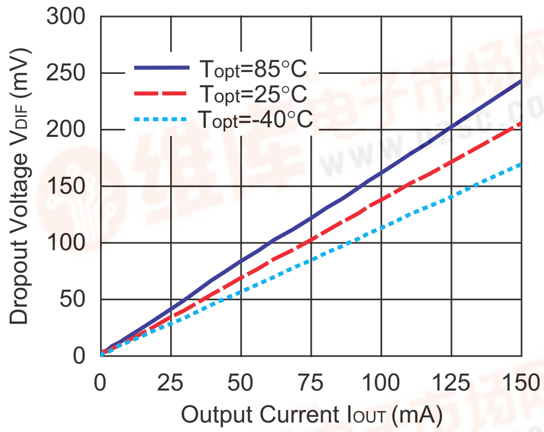


6) Dropout Voltage vs. Output Current

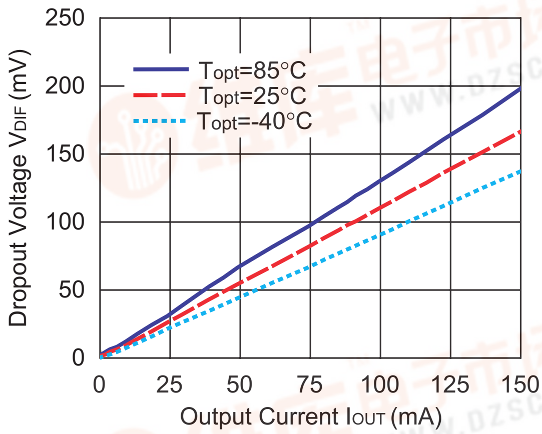
0.8V(VR1/VR2)



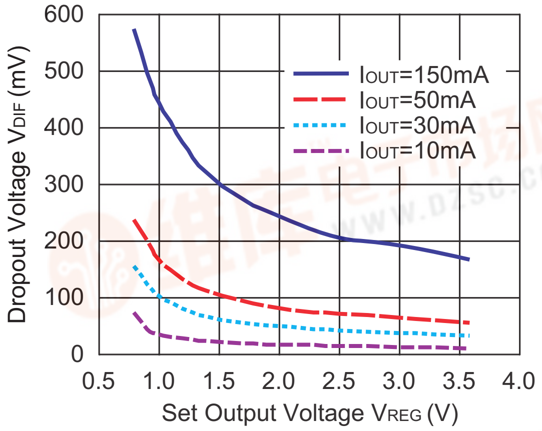
2.5V(VR1/VR2)



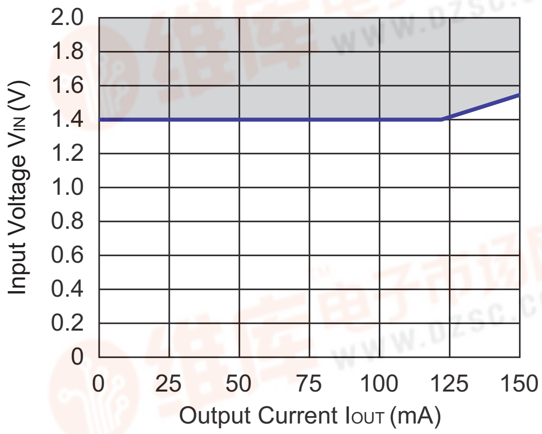
3.6V(VR1/VR2)



7) Dropout Voltage vs. Set Output Voltage



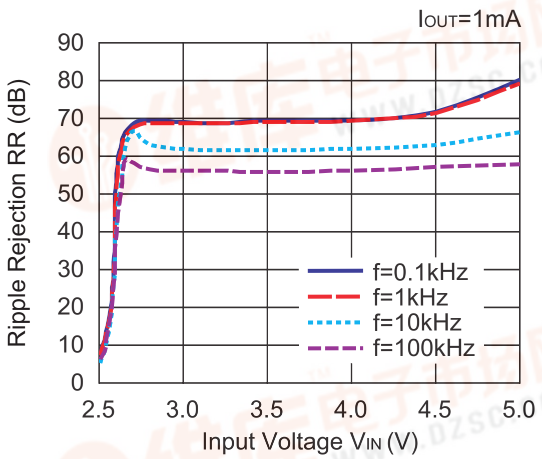
8) Minimum Operating Voltage



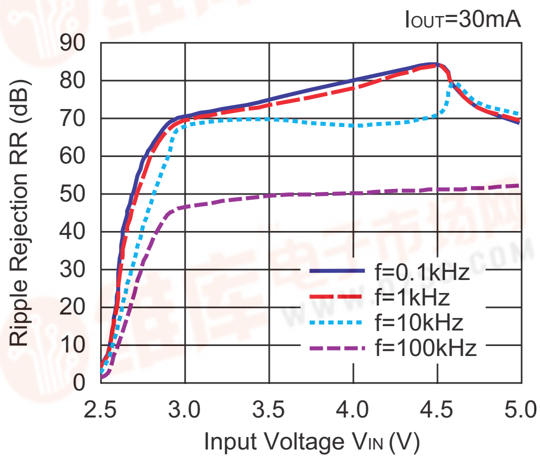
Hatched area is available
for 0.8V output

9) Ripple Rejection vs. Input Voltage (C_{IN}=none, C_{OUT1}=C_{OUT2}=Ceramic 0.22µF, Ripple=0.2Vp-p, T_{opt}=25°C)

2.5V(VR1/VR2)

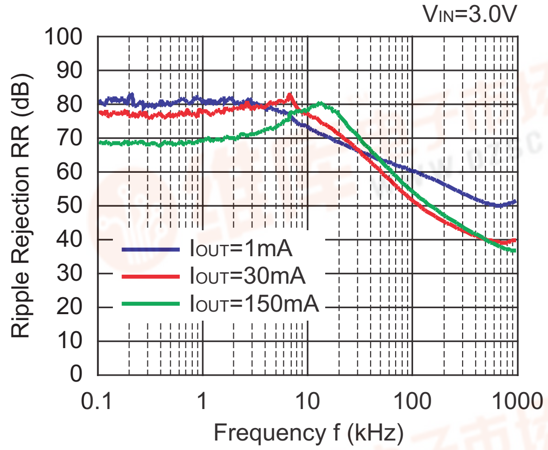


2.5V(VR1/VR2)

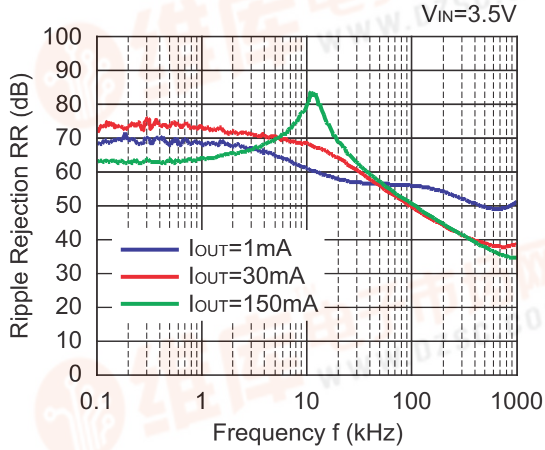


10) Ripple Rejection vs. Frequency (C_{IN}=none, C_{OUT1}=C_{OUT2}=Ceramic 0.22µF, Ripple=0.2Vp-p,Topt=25°C)

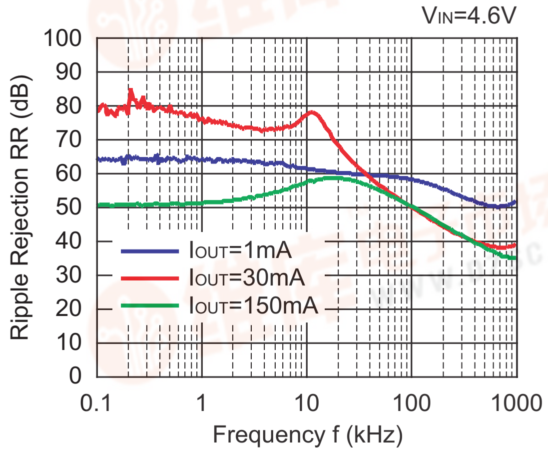
0.8V(VR1/VR2)



2.5V(VR1/VR2)

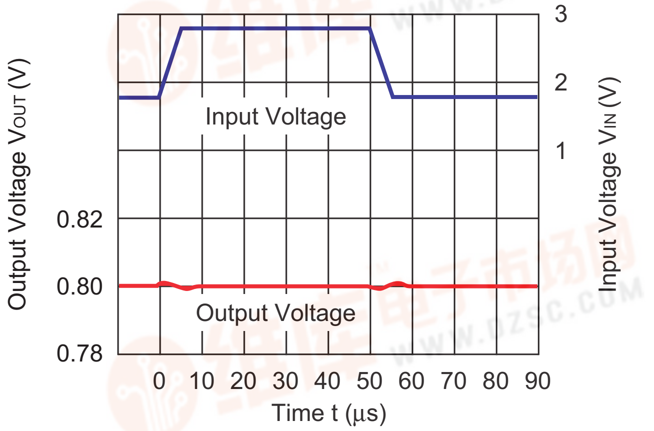


3.6V(VR1/VR2)

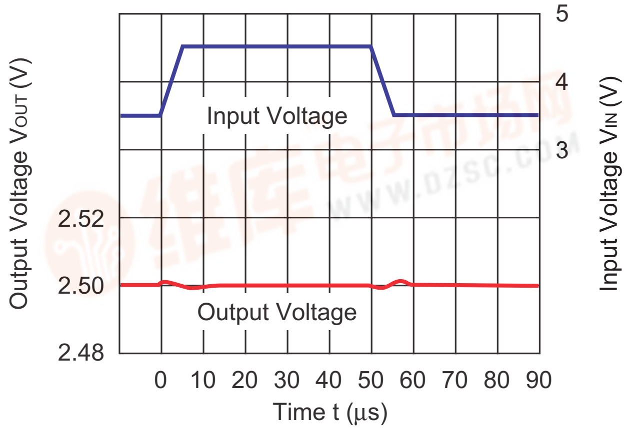


11) Input Transient Response (I_{OUT}=30mA, tr=tf=5µs, C_{IN}=none, C_{OUT1}=C_{OUT2}=0.22µF, Topt=25°C)

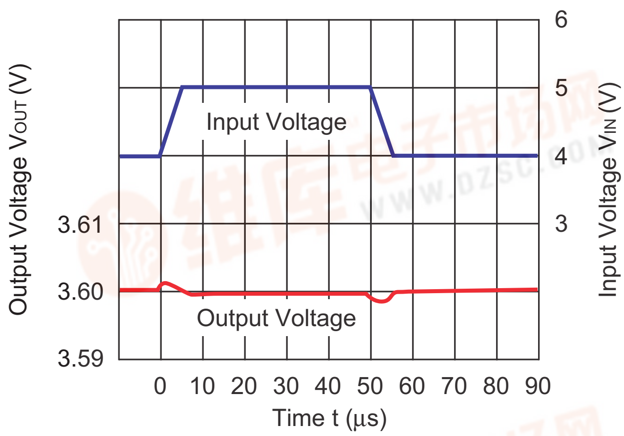
0.8V(VR1/VR2)



2.5V(VR1/VR2)

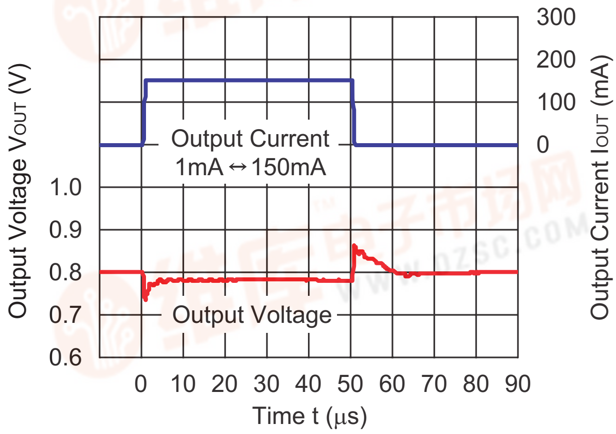


3.6V(VR1/VR2)

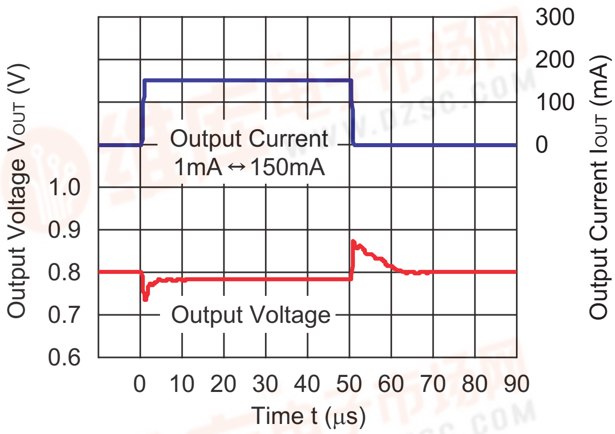


12) Load Transient Response (VR1/VR2) (tr=tf=0.5μs, C_{IN}= C_{OUT1}=C_{OUT2}=Ceramic0.22μF, Topt=25°C)

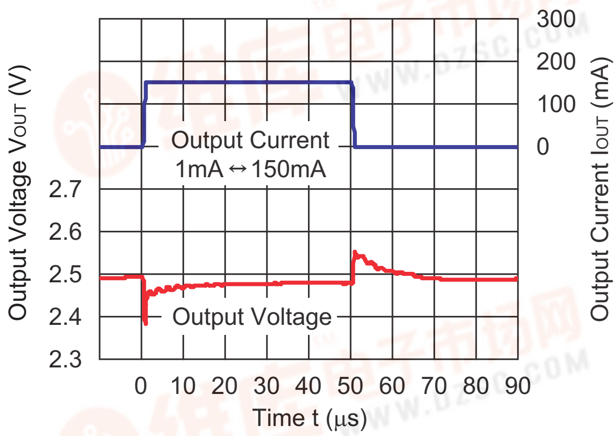
0.8V(A/B version)



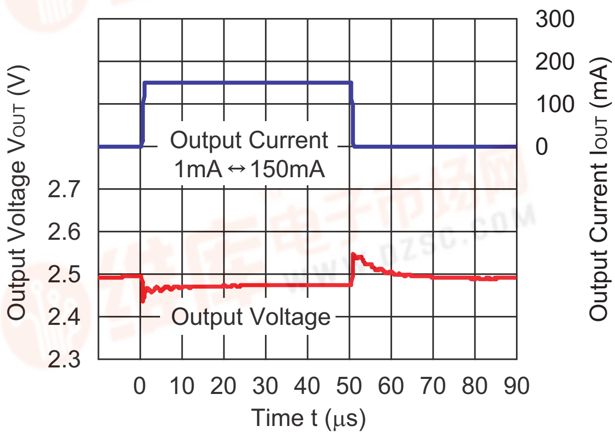
0.8V(D/E version)



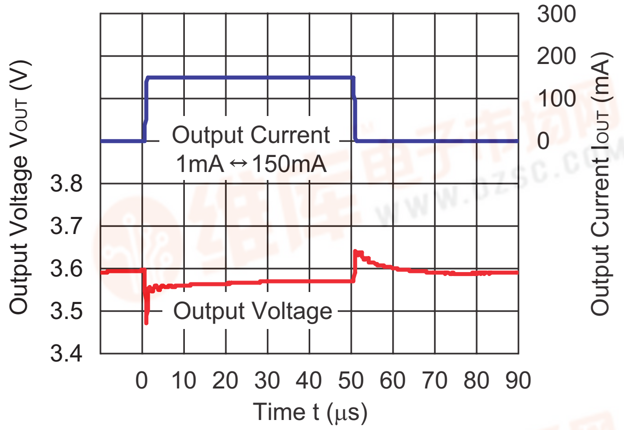
2.5V(A/B version)



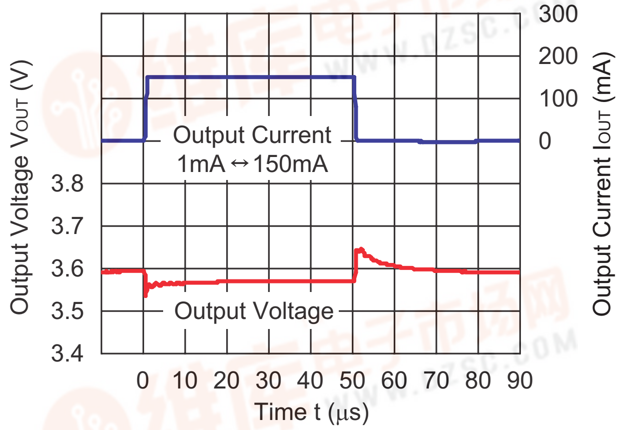
2.5V(D/E version)



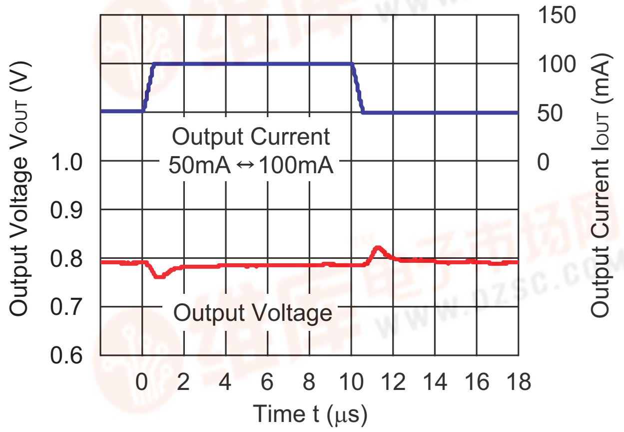
3.6V(A/B version)



3.6V(D/E version)



0.8V(A/B version)



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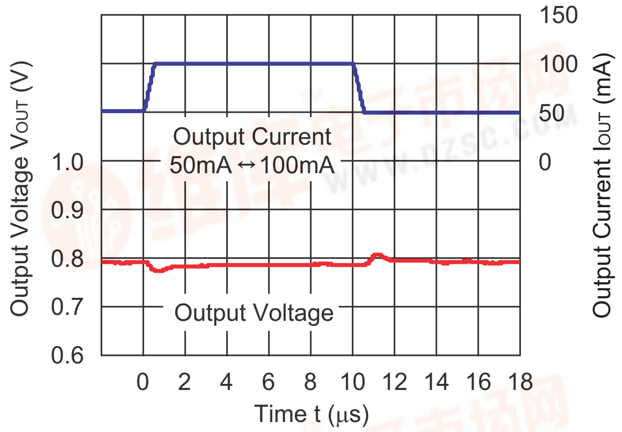
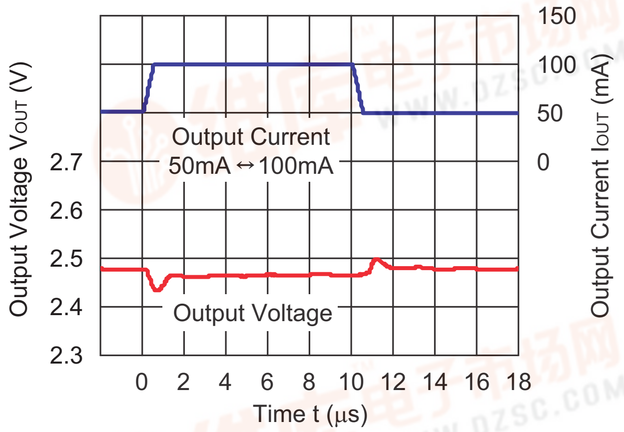
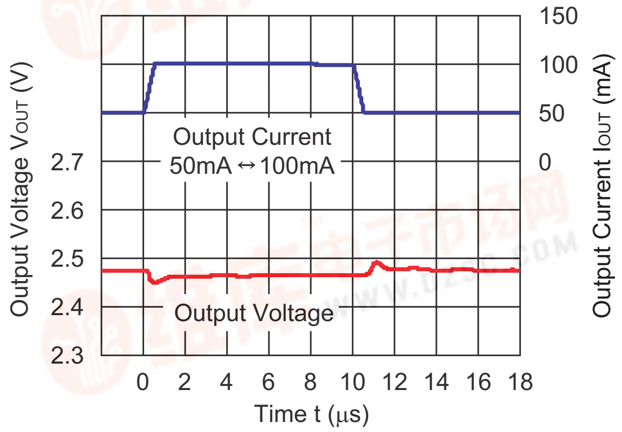


2.5V(A/B version)



2.5V(D/E version)



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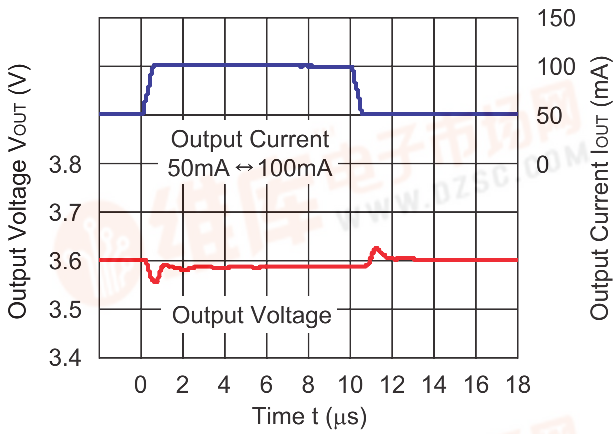
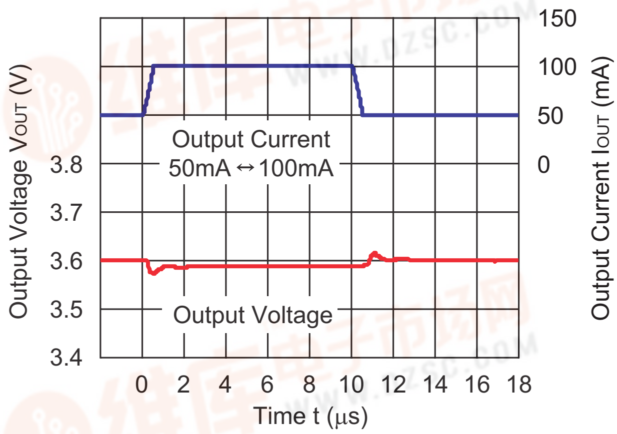
3.6V(A/B version)

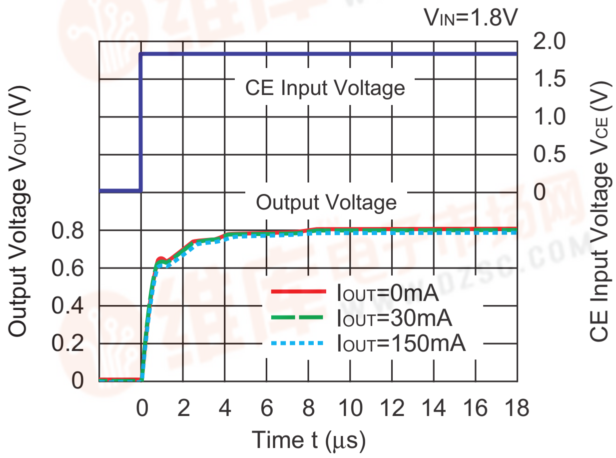


3.6V(D/E version)

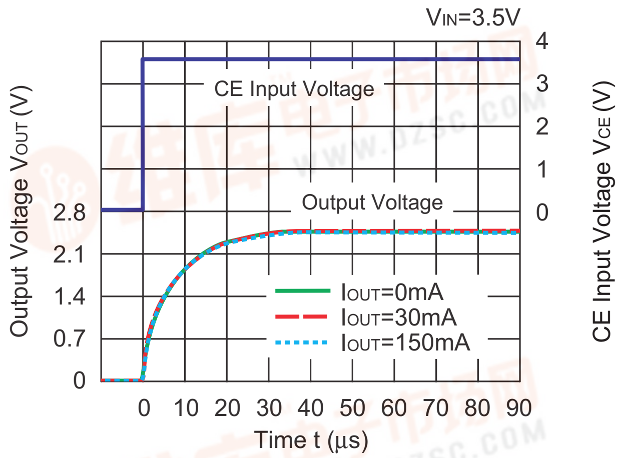


13) Turn On Speed with CE pin ($C_{IN}=C_{OUT1}=C_{OUT2}=0.22\mu F$, $T_{opt}=25°C$)

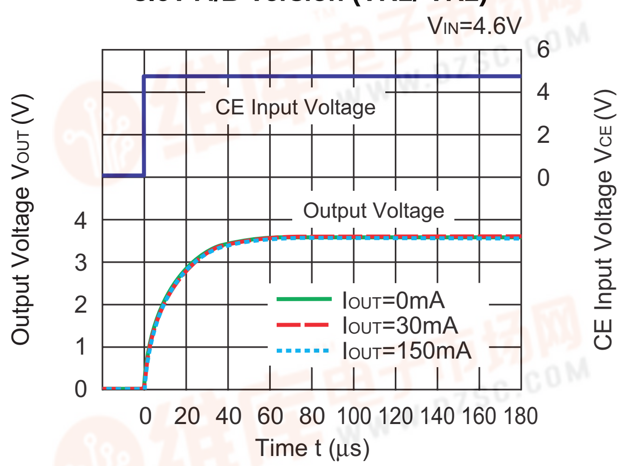
0.8V A/B Version (VR1/ VR2)



2.5V A/B Version (VR1/ VR2)

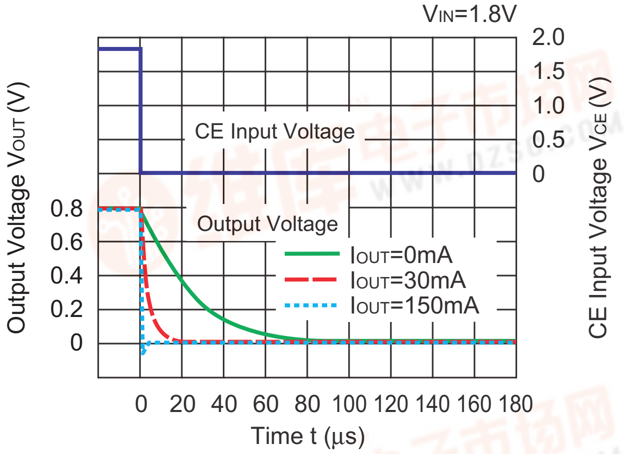


3.6V A/B Version (VR1/ VR2)

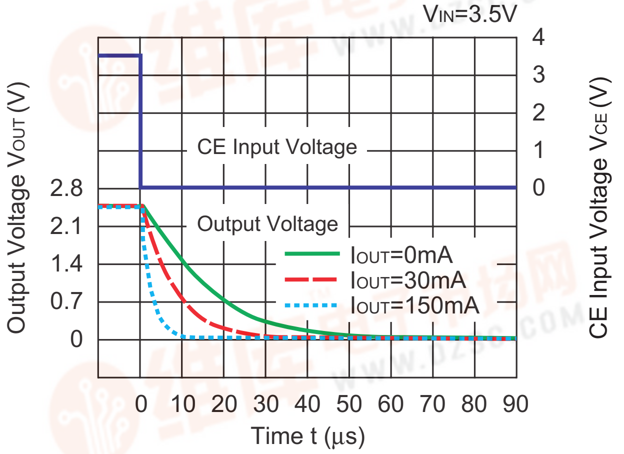


14) Turn Off Speed with CE pin ($C_{IN}=C_{OUT1}=C_{OUT2}=0.22\mu F$, Topt=25°C)

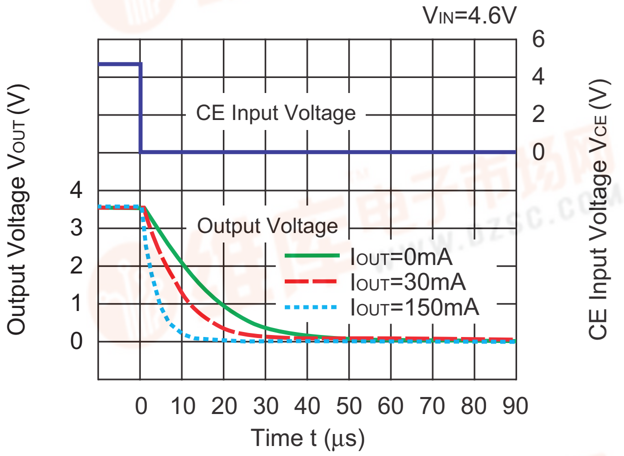
0.8V B Version (VR1/ VR2)



2.5V B Version (VR1/ VR2)



3.6V B Version (VR1/ VR2)



ESR vs. Output Current

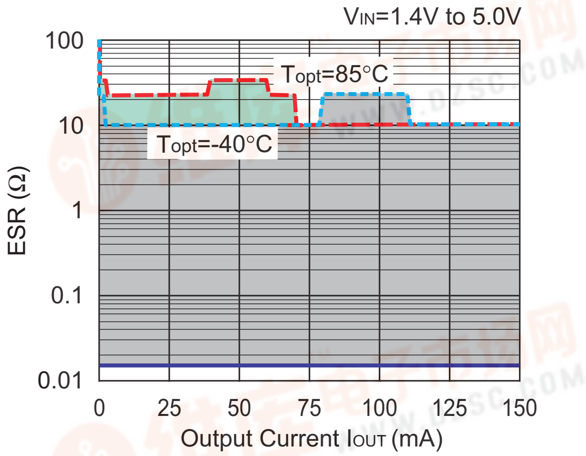
When using these ICs, consider the following points:

The relations between I_{OUT} (Output Current) and ESR of an output capacitor are shown below.
The conditions when the white noise level is under 40µV (Avg.) are marked as the hatched area in the graph.

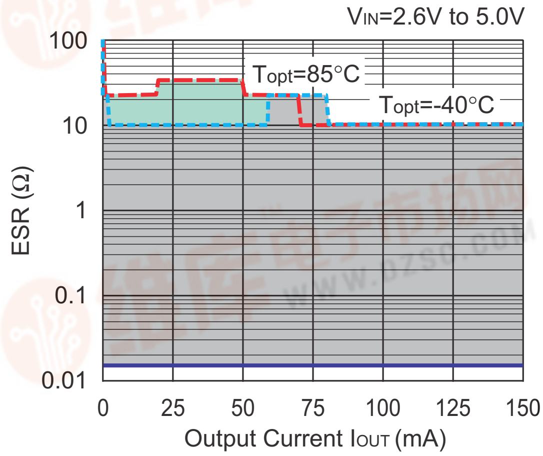
Measurement conditions

Frequency Band : 10Hz to 2MHz
Temperature : −40°C to 85°C
C_{IN}, C_{OUT1} ,C_{OUT2} : 0.22µF (Murata , GRM155B10J224KE01)

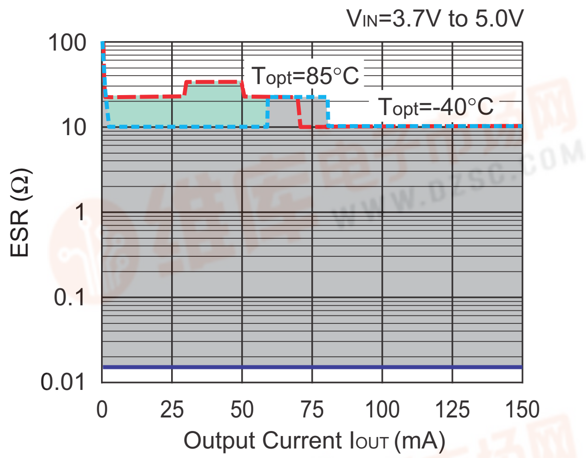
0.8V(VR1/ VR2)



2.5V(VR1/ VR2)



3.6V(VR1/ VR2)



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日本経営品質賞 1999年度受賞

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