

ETR0407-005

Step-Up DC/DC Converter-Backlight LED Driver

■GENERAL DESCRIPTION

The XC9116 series is a fixed frequency, constant current step-up DC/DC converter ideal for driving LEDs used in backlighting applications such as cellular phones, PDAs and digital cameras. Output voltage of up to 17.5V can be derived, therefore four white LEDs in series can be driven. Since an LED current can be set by an external resistor when LEDs are in-series connection, all white LEDs can be turned on equably. A network of two parallel legs with three in each LED can be also driven. Luminance of the LEDs is controlled by changing the duty cycle of a PWM signal applied to the CE pin. Efficiency is high with the low feedback reference voltage ensuring the RLED losses are minimal. In addition, an internal MOSFET with an RDSON of 2.0 Ω is used. A low profile and small board area solution can be achieved using a chip coil and an ultra small ceramic output capacitor (CL) of 0.22 [F as a result of the high 1.0MHz switching frequency.

The 'B' type of XC9116 has an additional fault detection circuit. If the LEDs are disconnected or damaged, excess or run away output voltage is prevented by stopping the drive of the internal MOSFET.

APPLICATIONS

- For White LED drivers
- Mobile phones, PHS
- PDAs
- Digital still cameras



FEATURES

Input Voltage Range Output Voltage Range

Oscillation Frequency ON Resistance Efficiency

- : Up to 17.5V externally set-up : Reference voltage 0.2V <u>+</u>5%
- : 1.0MHz, ±20%

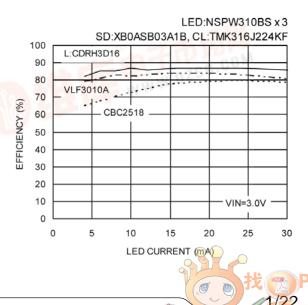
: 2.5V ~ 6.0V

- : 2.0Ω
- : 86% (XC9116B Type)

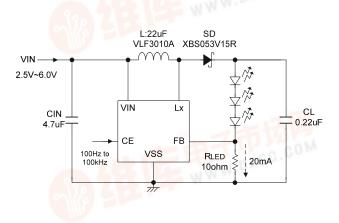
	: 84% (XC9116D Type)
	(VIN=3.6V, ILED=20mA when
	driving 3 white LEDs in series)
Control	: PWM control
Standby current	: ISTB=1.0 µ A (MAX.)
Load Capacitor	: 0.22 μ F, ceramic
Lx Limit Current	: 325mA
Lx Overvoltage Limit	: XC9116B series
No Lx Overvoltage Limit	: XC9116D series
Package	: SOT-25, USP-6B
Environmentally Friendly	: EU RoHS Compliant, Pb Free

TYPICAL PERFORMANCE CHARACTERISTICS

●XC9116B Type



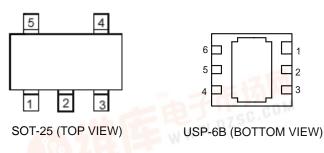
■TYPICAL APPLICATION CIRCUIT



XC9116 Series

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PIN CONFIGURATION



*The dissipation pad of the USP-6B package should be left open. If the circuit needs to be connected to other pin, it should be connected to the Vss pin.

PIN ASSIGNMENT



■CE PIN FUNCTION

10	
CE PIN	OPERATIONAL STATE
Н	Operation
900 LA - 1	Shut-down
01// L-1 - 1/	

■PRODUCT CLASSIFICATION

Ordering Information

XC9116123456-7^(*1)

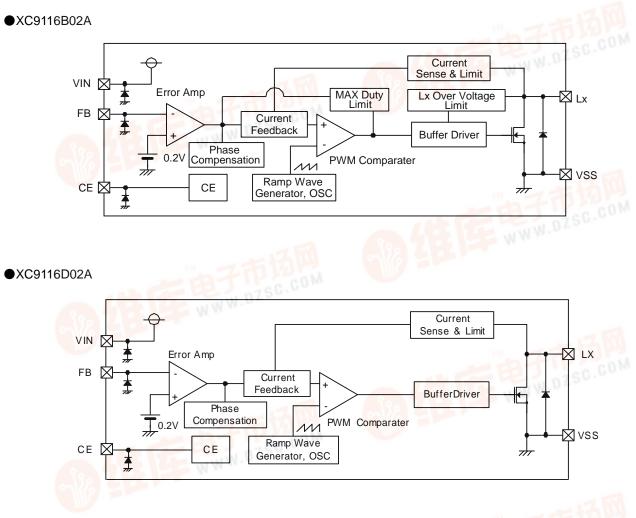
DESIGNATOR	DESCRIPTION	SYMBOL	DESCRIPTION
1		В	Available
U	Lx Overvoltage Limit	D	Not available
23	FB Voltage	02	0.2V
4	Oscillation Frequency	А	1MHz
	(5)6-7 Packages Taping Type (*2)	MR	SOT-25
56-7		DR-G	USP-6B
		DR	USP-6B

⁽¹⁾ The "-G" suffix indicates that the products are Halogen and Antimony free as well as being fully RoHS compliant.

^('2) The device orientation is fixed in its embossed tape pocket. For reverse orientation, please contact your local Torex sales office or representative. (Standard orientation: (5R-7), Reverse orientation: (5L-7)



■BLOCK DIAGRAMS



■ ABSOLUTE MAXIMUM RATINGS

PARAMETE	R ,	SYMBOL	RATINGS	UNITS	
VIN Pin Volta	age	VIN	Vss – 0.3 ~ 7.0	V	
Lx Pin Volta	ge	VLx	Vss – 0.3 ~ 22.0	V	
FB Pin Volta	ige	Vout	Vss – 0.3 ~ 7.0	V	
CE Pin Voltage		VCE	Vss – 0.3 ~ 7.0	V	
Lx Pin Current		ILx	1000	mA	
Rower Dissinction	SOT-25	Pd	250	mW	
Power Dissipation	USP-6B	Fu	120	IIIV	
Operating Temperat	ure Range	Topr	- 40 ~ + 85	°C	
Storage Temperature Range		Tstg	- 55 ~ +125	Oo	



■ELECTRICAL CHARACTERISTICS

XC9116B02AMR						Т	a = 25 ⁰ C
PARAMETER	SYMBOL	CONDITIONS	MIN.	TYP.	MAX.	UNIT.	CIRCUIT
FB Control Voltage	Vfb	FB	0.19	0.20	0.21	V	1
Output Voltage Range	Voutset		Vin		17.5	SV	1
Input Voltage Range	Vin		2.5	- W	6.0	V	1)
Supply Current 1	IDD1	-15111 01/2		450	750	μA	2
Supply Current 2	IDD2	VIN=Lx, V _{FB} =0.4V	-	60	140	μA	3
Standby Current	I _{STB}	V _{CE} =0V, V _{LX} =5.0V	-	0	1.0	μA	3
Oscillation Frequency	fosc	W.BLS	0.8	1.0	1.2	MHz	2
Maximum Duty Cycle (*1)	MAXDTY		86	92	98	%	2
Efficiency (*2)	EFFI	When connected to ext. components, VIN=3.6V, RLED=20 Ω	-	86	-	%	0
Current Limit	ILIM	When connected to ext. components, VIN=3.6V	225	325	425	mA	4
Lx Overvoltage Limit	Vlxovl	Voltage which Lx pin voltage holding "H" level VIN ≥ 2.5 V	18.0	19.0	22.0	V	2
Lx ON Resistance	Rswon	VIN=3.6V, VLx=0.4V	1 × - 12	2.0	-	Ω	2
Lx Leakage Current	ILxL	Same as I _{STB}	-	0	1.0	μA	3
CE "High" Voltage	Vсен	CE applied voltage when Lx starts oscillation	0.65	-	-	V	2
CE "Low" Voltage	VCEL	CE applied voltage which Lx pin voltage holding "H" level	-	-	0.2	V	2
CE "High" Current	Ісен	Same as IDD2	-0.1	-	0.1	μA	3
CE "Low" Current	ICEL	Same as I _{STB}	-0.1		0.1	μA	3
FB "High" Current	lfbh	Same as IDD2	-0.1		0.1	μA	3
FB "Low" Current	IFBL	Same as I _{STB}	-0.1	- W	0.1	μA	3

Unless otherwise stated, VIN=3.0V, V_{CE}=3.0V, V_{FB}=0V, Vpull=5.0V

NOTE: *1: The duty ratio is forcibly reduced when maximum duty cycle periods are repeated.

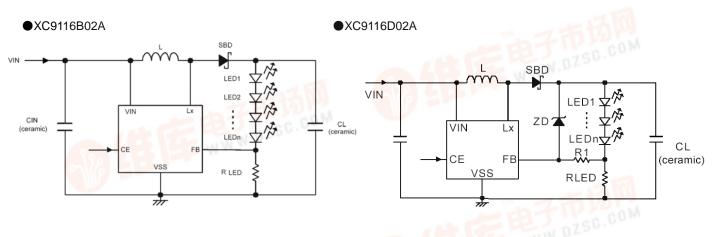
*2 : LED : NSPW310BS x 3, EFFI = {[(output voltage) x (output current)] / [(input voltage) x (input current)]} x 100

XC9116D02AMR						Ta	$= 25^{\circ}C$
PARAMETER	SYMBOL	CONDITIONS	MIN.	TYP.	MAX.	UNIT	CIRCUIT
FB Control Voltage	Vfb	FB	0.19	0.20	0.21	V	1
Output Voltage Range	VOUTSET		Vin	-0	19.5	V	1
Lx Operating Voltage Range	VLx		- 1	w	20.0	V	0
Operating Voltage Range	Vin	-1311 01/2	2.5	-	6.0	V	1
Supply Current 1	IDD1	THIM	-	450	750	μA	2
Supply Current 2	IDD2	VIN=Lx, VFB=0.4V	-	50	120	μA	3
Standby Current	I _{STB}	VCE=0V, VLx=5V	-	0	1.0	μA	3
Oscillation Frequency	fosc		0.8	1.0	1.2	MHz	2
Maximum Duty Cycle	MAXDTY		86	92	98	%	2
Efficiency (*1)	EFFI	When connected to ext. components, VIN=3.6V, RLED=20 Ω	-	84	27	%	0
Current Limit	ILIM	When connected to ext. components, VIN=3.6V	225	325	425	mA	4
Lx ON Resistance	Rswon	VIN=3.6V, VLx=0.4V, Rpull=10 Ω		2.0	3.6	Ω	2
Lx Leakage Current	lLxL	Same as I _{STB}	-	0	1.0	μA	3
CE "High" Voltage	Vсен	CE applied voltage when Lx starts oscillation	0.65	-	-	V	2
CE "Low" Voltage	VCEL	CE applied voltage which Lx pin voltage holding "H" level	-	-	0.2	V	2
CE "High" Current	Ісен	Same as IDD2	-0.1	-	0.1	μA	3
CE "Low" Current	ICEL	Same as I _{STB}	-0.1	-	0.1	μA	3
FB "High" Current	IFBH	Same as IDD2	-0.1	-	0.1	μA	3
FB "Low" Current	IFBL	Same as I _{STB}	-0.1	-	0.1	μA	3

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Test conditions: Unless otherwise stated, VIN=3.0V, VCE=3.0V, VFB=0V, Vpull=5.0V, Rpull=100 Ω Notes: *1: LED: NSPW310BS x 3, EFFI = {[(output voltage) x (output current)] / [(input voltage) x (input current)]} x 100

■TYPICAL APPLICATION CIRCUITS



EXTERNAL COMPONENTS

SYMBOL	VALUE	PART NUMBER	MANUFACTURER
L	22 μ H	VLF3010A-220MR	TDK
SBD ^(*1)	WWW.V	XBS053V15R-G (*2)	TOREX
SDD		MA2Z720	PANASONIC
CIN	4.7 μ F	JMK107BJ475MA-B	TAIYO YUDEN
CL (*3)	0.22 μ F	TMK107BJ224KA-B	TAIYO YUDEN
ZD ^(*4)	18V	MAZ8180	PANASONIC
R1	100 Ω		WWW.DZ3

NOTE :

*1: Please use a Schottky barrier diode (SBD) with a low junction capacitance, Cj.

*2: For using the XBS053V15R-G with four white LEDs in series, please be noted with a direct reverse voltage (VR=20V) and a repetitive peak reverse voltage (VR=30V).

*3: Use ceramic capacitors processing a low temperature coefficient.

*4: Please refer to the LED Open-circuit Protection at Application Information for setting the Zener diode.

■OPERATIONAL EXPLANATION

The series consists of a reference voltage source, ramp wave circuit, error amplifier, PWM comparator, phase compensation circuit, Lx overvoltage limit circuit, N-channel MOS driver transistor, current limiter circuit and others. The series ICs compare, using the error amplifier, the voltage of the internal voltage reference source with the feedback voltage from the FB pin. Phase compensation is performed on the resulting error amplifier output, to input a signal to the PWM comparator to determine the turn-on time during switching. The PWM comparator compares, in terms of voltage level, the signal from the error amplifier with the ramp wave from the ramp wave circuit, and delivers the resulting output to the N-channel MOS driver transistor to cause the Lx pin to output a switching duty cycle. This process is continuously performed to ensure stable output voltage. The current feedback circuit detects the N-channel MOS driver transistor's current for each switching operation, and modulates the error amplifier output signal. This enables a stable feedback loop even when a low ESR capacitor, such as a ceramic capacitor, is used, ensuring stable output voltage.

<Reference Voltage Source>

The reference voltage source provides the reference voltage to ensure stable output voltage of the IC.

<Ramp Wave Circuit>

The ramp wave circuit determines switching frequency. The 1MHz (TYP.) of frequency is fixed internally. Clock pulses generated in this circuit are used to produce ramp waveforms needed for PWM operation.

<Error Amplifier>

The error amplifier is designed to monitor output voltage. The amplifier compares the reference voltage with the FB pin voltage. When a voltage lower than the reference voltage is fed back, the output voltage of the error amplifier is increased. Gain and frequency characteristics of the error amplifier output are fixed internally as an optimize signal.



XC9116 Series

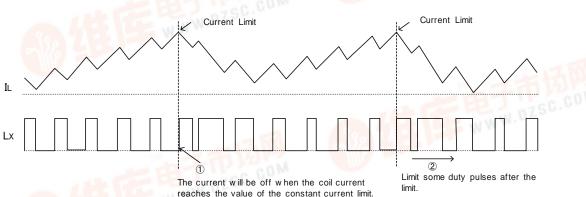
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OPERATIONAL EXPLANATIONS (Continued)

<Current Limit>

The current limit circuit of the XC9116 series monitors the current flowing through the N-channel MOS driver transistor connected to the Lx pin, and features a combination of the constant-current type current limit mode and the duty cycle limit of the next pulse.

^①When the driver current is greater than a specific level, the constant-current type current limit function operates to turn off the pulses from the Lx pin at any given timing.



The IC controls the next pulse to be smaller than the first pulse.

<Lx Overvoltage Limit Circuit>

XC9116B series' Lx overvoltage limit circuit monitors the Lx pin voltage. When the Lx pin voltage exceeds than 19V (TYP.), the IC performs the function of latching the OFF state of the driver transistor, and goes into operation suspension mode. In suspension mode, operations can be resumed by restoring power to the VIN pin. The suspension mode does not mean a complete shutdown, but a state in which pulse output is suspended; therefore, the internal circuitry remains in operation.

<Maximum Duty Cycle Limit>

The XC9116B series' maximum duty cycle limit circuit monitors the duty cycle. When the maximum duty cycle is repeated for a certain time, the IC controls the error amplifier output so that the duty cycle of the next pulse becomes smaller than that of the first pulse.

<CE Pin Function>

The operation of the XC9116 series will enter into the shut down mode when a low level signal is input to the CE pin. During the shut down mode, the supply current is $0 \mu A$ (TYP.), with high impedance at the Lx pin. The IC starts its operation with a high level signal to the CE pin. The input to the CE/MODE pin is a CMOS input and the sink current is $0 \,\mu$ A (TYP.). 100 μ s after disable, the IC goes into suspension mode and supply current is minimal. After this, the IC will be in stand-by mode and the supply current will be $0 \mu A$ (TYP.).

NOTES ON USE

<Lx (Pin 1): Switch Pin>

Please connect the anode of a Schottky barrier diode and an inductor to the Lx pin.

<FB (Pin 3): Voltage Feedback Pin>

The reference voltage is 200mV (TYP.). A resistor (RLED) should be connected to the FB pin for setting the cathode of LEDs and a constant current value. The resistance value can be calculated by the following equation. WWW.DZ

RIED=0.2 / ILED

ILED=Setting constant current value

Typical example:

· · ·	CIIIM		
ILED	RLED	ILED	Rled
5mA	40 Ω	13.3mA	15 Ω
10mA	20 Ω	20mA	10 Ω

<CE (Pin 4): Chip Enable Pin>

An ENABLED state is reached when the CE voltage exceeds 0.65V and a DISABLED state when the CE Voltage falls below 0.2V.

<VIN (Pin 5): Power Supply Pin>

Please connect an inductor and an input by-pass capacitor (CIN) to the VIN pin.



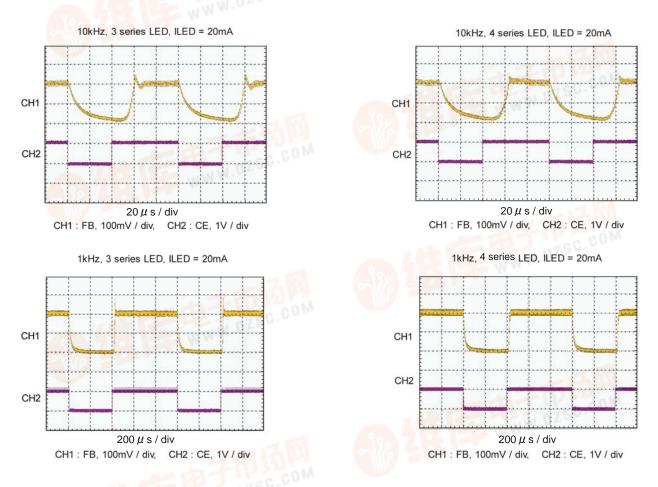
■APPLICATION INFORMATION

<Dimming Control>

1. Applying PWM signal to the CE pin

The XC9116 repeats on/off operations by a PWM signal applied to the CE pin. The magnitude of LED current, ILED, when the diode is on, is determined by RLED. The magnitude is zero when the diode is off. The average of LED current is proportional to the positive duty ratio of the PWM signal.

The frequency of the PWM signal can be controlled to the optimum value between 100Hz and 10kHz. With regard to the amplitude of the PWM signal, the high level should be higher than the "H" voltage of CE, VCEH, and the low level, lower than the "L" voltage of CE, VCEL.



2. Step-Wise Regulation of LED Current

In some applications, it may be necessary to incorporate step-wise regulation of LED current, ILED. Step-wise regulation of LED illumination is achieved by connecting a switch element SW1 in parallel with RLED and in series with RLED1 and turning SW1 on and off, as shown below. Choose a resistance of RLED so that the minimum necessary current is gained when switch element SW1 is off. The resistance of RLED1 should be such that a desired increase of current passed through the LED is gained when the switch element is on.

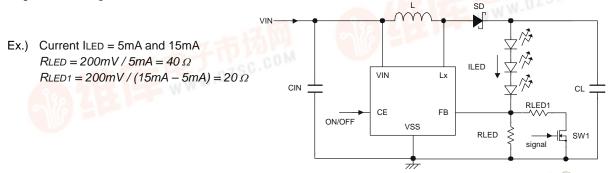


Figure : Circuit using Step-wise Regulation of LED Current

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APPLICATION INFORMATION (Continued)

<Dimming Control (Continued)>

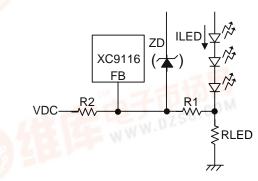
3. Using DC Voltage

If in an application it is necessary to control the LED current by a variable DC voltage, illumination control of LED is achieved by connecting R1 and R2 and applying a direct-current voltage to R2, as shown below.

When R1>>RLED, ILED which flows into LEDs can be calculated by the following equation;

$$ILED = (VREF - R1 / R2 (VDC - VREF)) / RLED$$
$$VREF = 0.2V (TYP.)$$

Ex.1) When R1 = $10k \Omega$, R2 = $100k \Omega$, RLED = 10Ω , In the range of 0.2V to 2.2V DC, ILED (LED current) varies between 20mA to 0mA.





Ex.2) When R1 = $10k \Omega$, R2 = $100k \Omega$, R3 = $10k \Omega$, C1 = 0.1μ F, RLED = 10Ω , the average LED current will be 10mA by inputting a PWM signal of CE 'H' level: 2.2V, CE 'L' level: 0V, duty cycle: 50%, oscillation frequency: 100Hz. As well as the way of dimming control by applying the PWM signal to the CE pin, the average LED current increases proportionally with the positive duty cycle of the PWM signal.

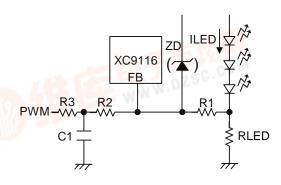
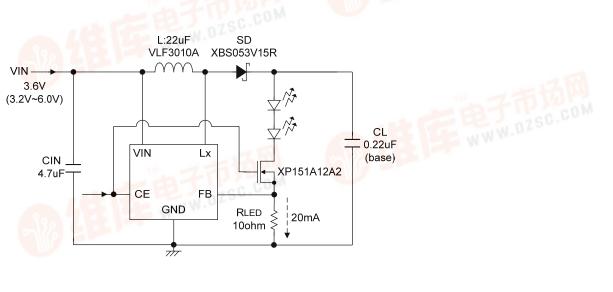


Figure : Circuit inputting a PWM signal to the FB pin

<Prevent Emission Caused by White LEDs Leakage>

When the input voltage (VIN) is high, minimum illumination may occur even if the CE pin is in the disable state. If this happens, please connect a transistor to between the LED and the FB pin. By driving the CE signal in-phase and cutting the pass to current, the minimum illumination can be prevented.

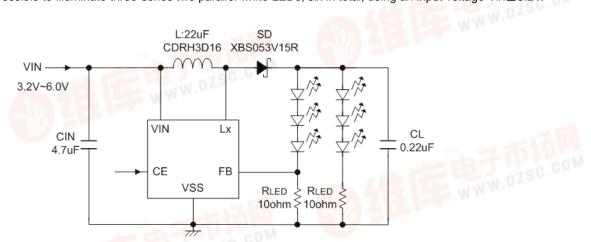




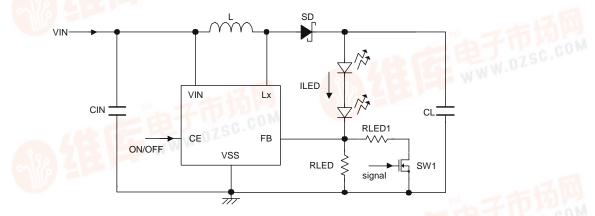
■APPLICATION INFORMATION (Continued)

<Illumination of Six in Total White LEDs>

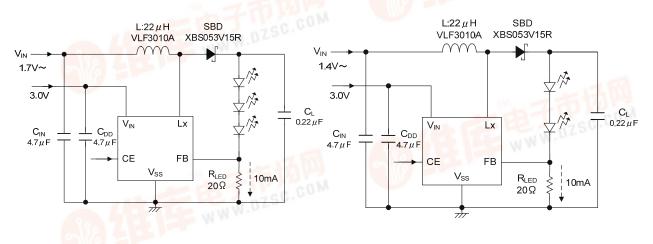
It is possible to illuminate three-series two parallel white LEDs, six in total, using an input voltage VIN≥3.2V. 00



<Use as Flash> An LED current 65mA (MAX.) can be supplied to two white LEDs.



<Separate Supply Source of the Step-up Circuit (VIN) from VIN Pin> Supply source of the step-up circuit can be used separately from VIN pin.



Circuit example of separating supply source of the step-up circuit from VIN pin (3 LEDs)

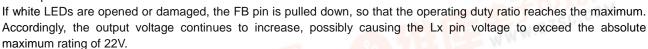
Circuit example of separating supply source of the step-up circuit from VIN pin (2 LEDs)



Note: Please input 2.5V~6V to the VIN pin when you use.

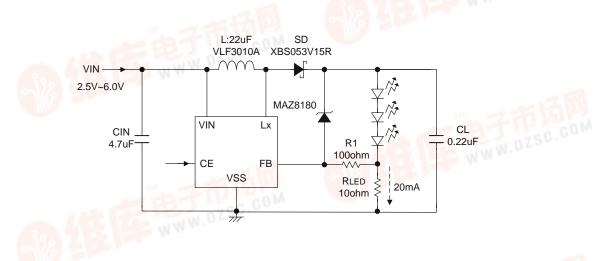
■APPLICATION INFORMATION (Continued)

<LED Open-circuit Protection>



In the case of the B type products, if white LEDs are opened or damaged, the detector built in the Lx pin causes the IC to stop oscillating, preventing excessive increase of the output voltage. However, the detector may detect an overvoltage if the Lx pin voltage exceeds 18V, which is the overvoltage limit, even when no LEDs are open. Therefore, care must be taken if four LEDs each having a forward voltage of 4.45V or more are connected in series.

In the case of the D type products (no overvoltage limit circuit), a Zener diode (ZD) and a resistor (R1) can be externally connected to serve as a load in the case that LEDs are opened, preventing the increase of the Lx pin voltage. The ZD voltage should be set to no more than 20V and no less than the product of each white LED's maximum forward voltage multiplied by the number of the connected LEDs, so that the Zener diode will not load the LEDs during normal operation. If the Zener diode becomes load, the current that runs through the white LEDs is reduced, decreasing illumination.



Note : The XC9116B02A series requires neither the Zener diode (ZD) nor the resistor 1 (R1).

<Startup Inrush Current>

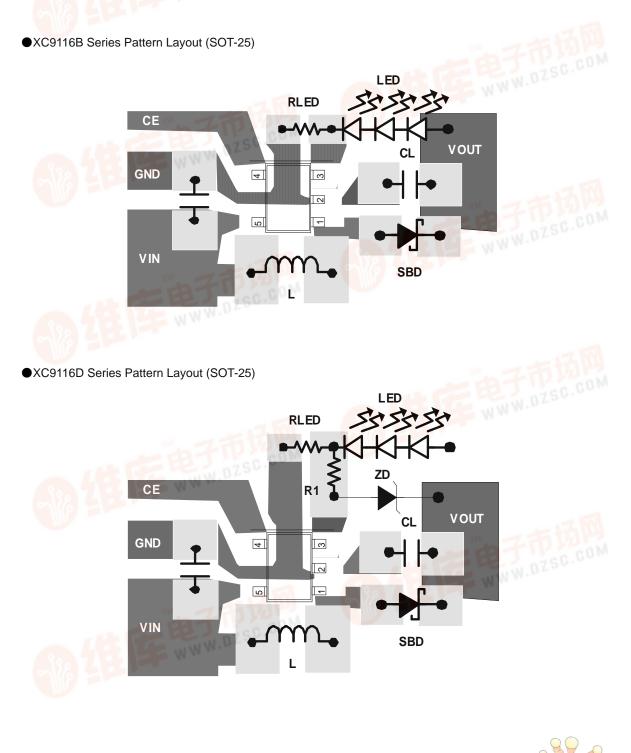
The XC9116 series has no soft-start circuit built-in in order to minimize delay at startup. The inrush current can reach up to the current limit, ILIM. In some cases, overshoot can occur.



■APPLICATION INFORMATION (Continued)

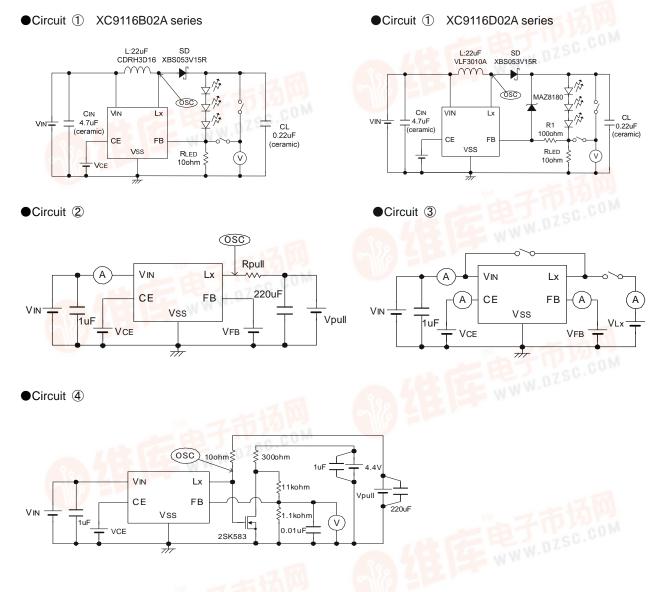
<Instruction on Pattern Layout>

- 1. In order to stabilize VIN's voltage level, we recommend that an input by-pass capacitor (CIN) be connected as close as possible to the VIN & VSs pins.
- 2. Please mount each external component as close to the IC as possible.
- 3. Wire external components as close to the IC as possible and use thick, short connecting traces to reduce the circuit impedance.
- 4. Make sure that the PCB GND traces are as thick as possible, as variations in ground potential caused by high ground currents at the time of switching may result in instability of the IC.



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XC9116 Series 查询XC9116供应商 TEST CIRCUITS



1. The measurement method of LX ON Resistance Rswon

Using the circuit ②, Lx ON resistance can be measured by adjusting Vpull voltage to set Lx voltage VLx 0.4V when the driver transistor is ON.

The oscilloscope is used for measuring the Lx voltage when the driver transistor is ON.

 $R_{SWON} = 0.4 / ((V_{pull} - 0.4) / 10)$

2. The measurement method of current limit ILIM

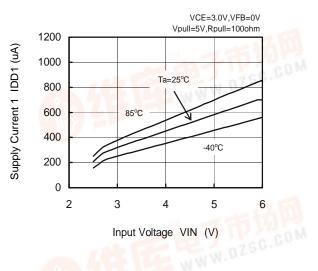
Using the circuit ④, current limit ILIM can be calculated by the equation including Vpull voltage when FB voltage is decreased while Vpull voltage is adjusted and Lx voltage VLx when the driver transistor is ON. The oscilloscope is used for measuring the Lx voltage when the driver transistor is ON.

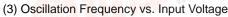
ILIM = (Vpull - VLx) / Rpull

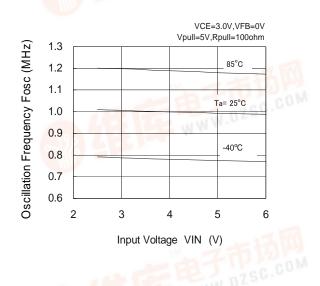


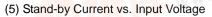
■ TYPICAL PERFORMANCE CHARACTERISTICS

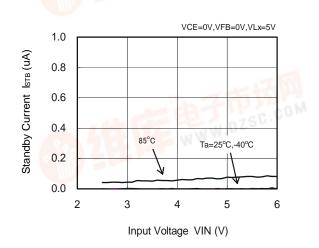
(1) Supply Current 1 vs. Input Voltage



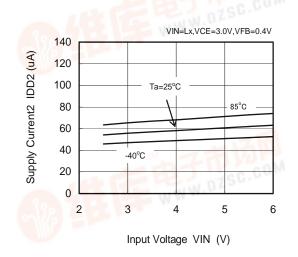




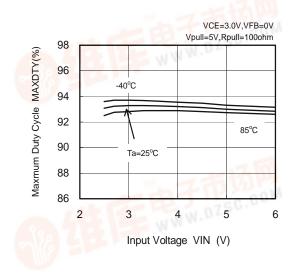




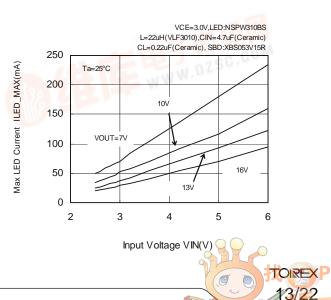
(2) Supply Current 2 vs. Input Voltage







(6) Maximum LED Current vs. Input Voltage

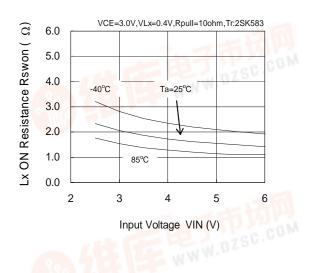


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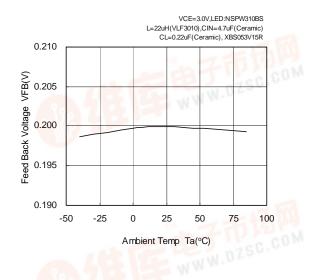


(7) Lx On Resistance vs. Input Voltage

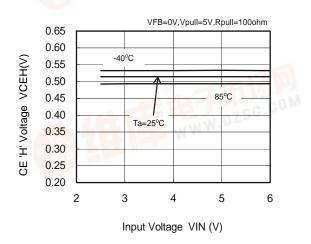
XC9116 Series

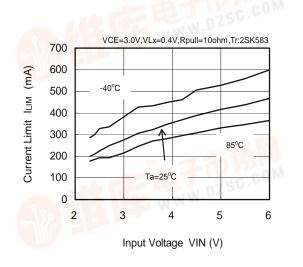


(9) FB Voltage vs. Ambient Temperature



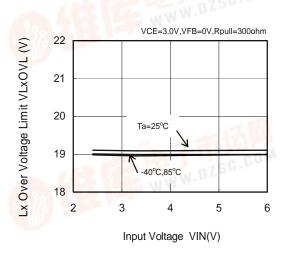
(11) CE 'H' Voltage vs. Input Voltage



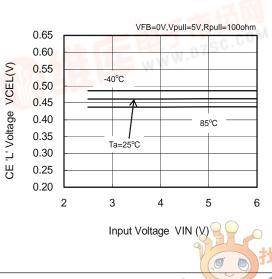


(8) Current Limit vs. Input Voltage



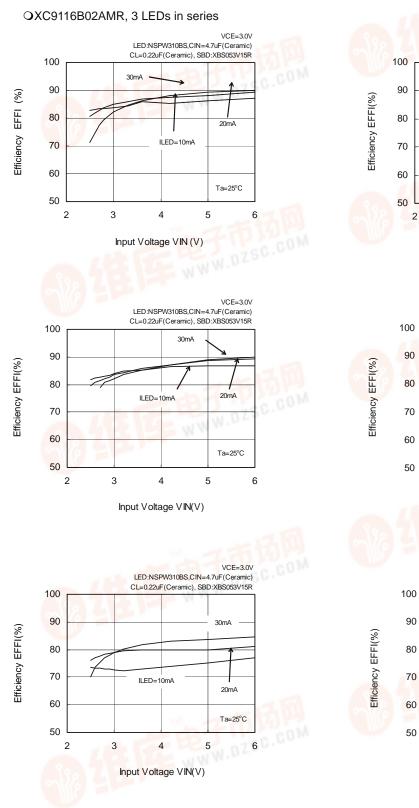


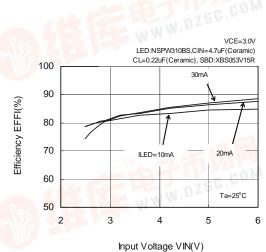
(12) CE 'L' Voltage vs. Input Voltage

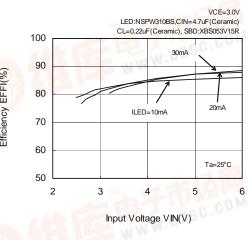


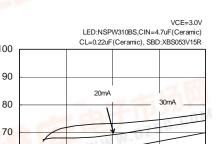
TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

(13) Efficiency vs. Input Voltage









Ta=25°C 5 3 4

ILED=10mA

2

Input Voltage VIN(V)

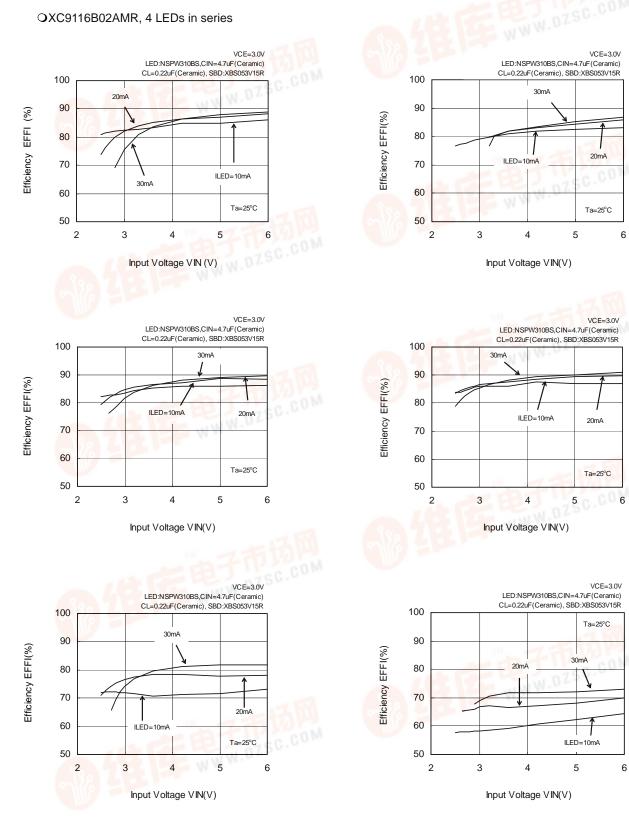


6



(13) Efficiency vs. Input Voltage (Continued)

XC9116 Series

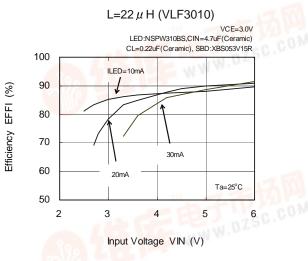




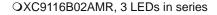
■ TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

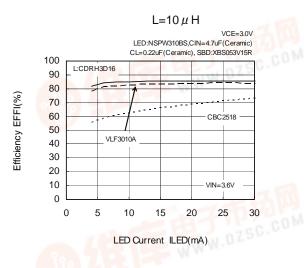
(13) Efficiency vs. Input Voltage (Continued)

OXC9116B02AMR, 2 parallel legs with 3 LEDs per leg



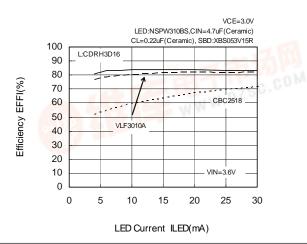
(14) Efficiency vs. LED Current

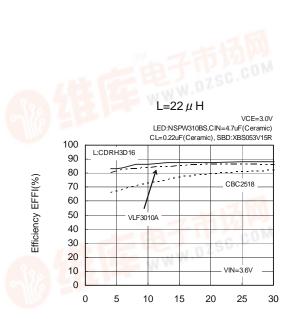




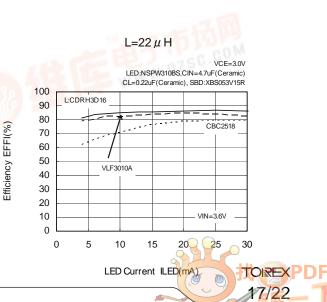




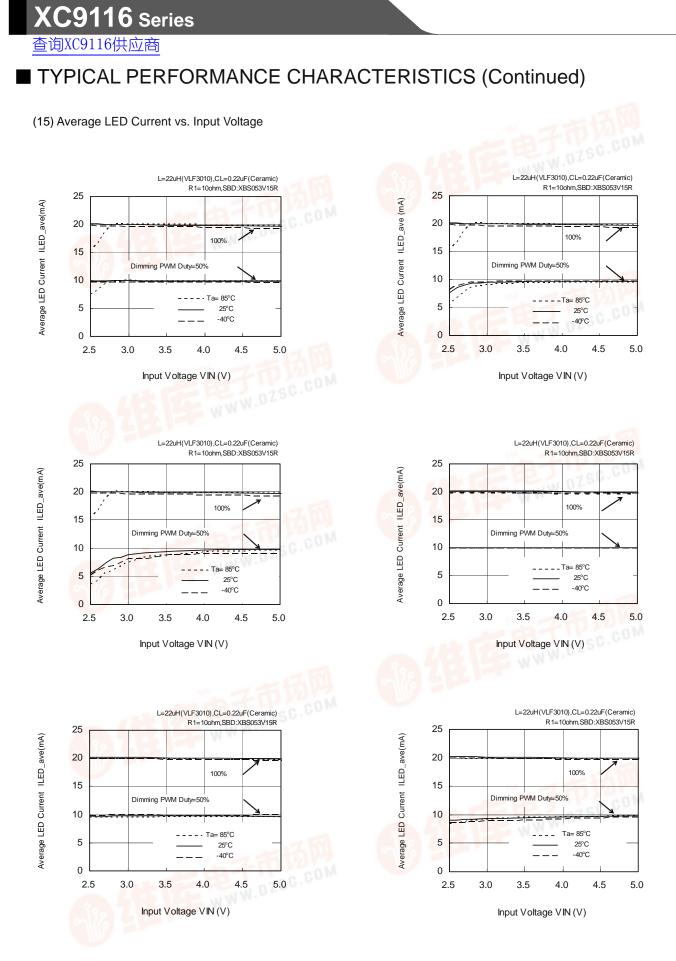




LED Current ILED(mA)



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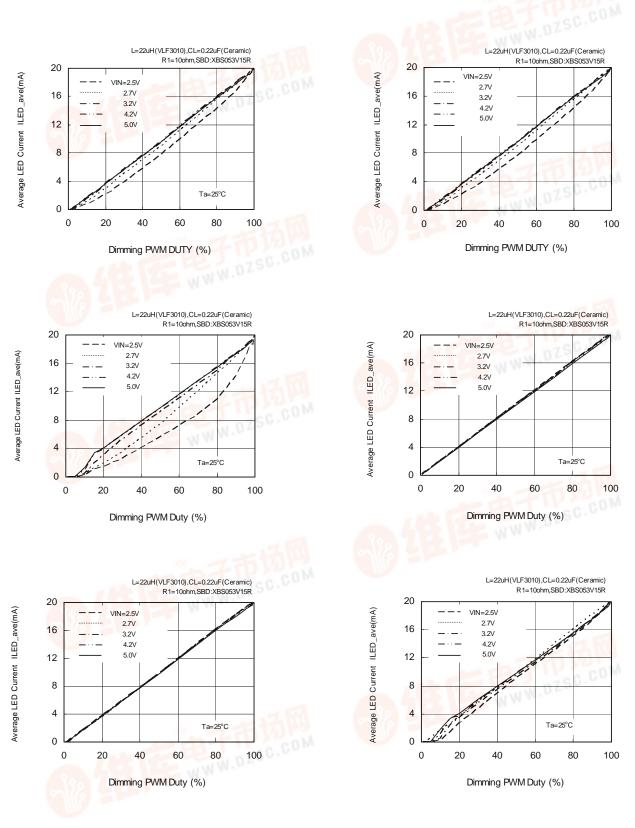


Note : Average LED Current denotes the average current which flows into LED when a PWM signal is input to the CE pin.

00

■ TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

(16) Average LED Current vs. Dimming PWM Duty

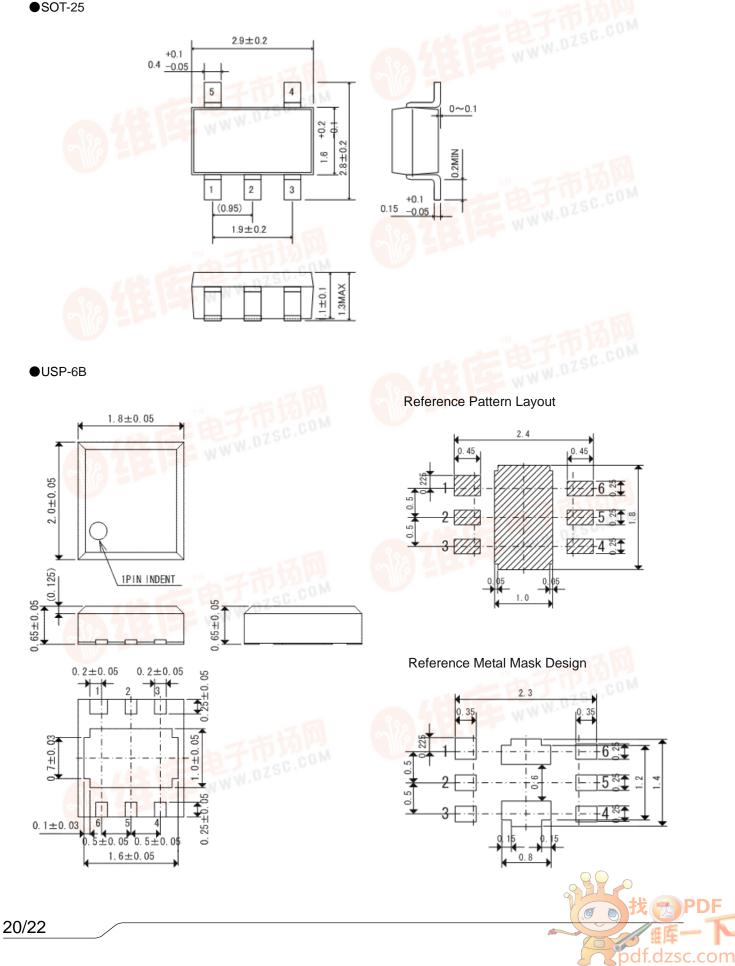


Note : Average LED Current denotes the average current which flows into LED when an PWM signal is input to the CE pin.

TOREXPDI 19/22

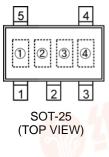
■ PACKAGING INFORMATION

●SOT-25



■ MARKING RULE

●SOT-25



① represents product series	
MARK	PRODUCT SERIES

* Character inversion used.

F

2 represents Lx overvoltage limit

MARK	Lx OVERVOLTAGE LIMIT	PRODUCT SERIES		
В	Available	XC9116B02AM x		
D	Not Available	XC9116D02AM x		
③ represents oscillation frequency				

XC9116 x 02AM x

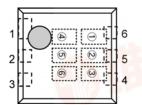
MARK	OSCILLATION FREQUENCY	PRODUCT SERIES
А	1MHz	XC9116 x 02AM x

represents production lot number

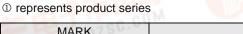
0 to 9 and A to Z, or inverted characters 0 to 9 and A to Z repeated.

(G, I, J, O, Q, and W excluded)

●USP-6B



USP-6B (TOP VIEW)



MARK	PRODUCT SERIES
K	XC9116 x 02AD x

2 represents Lx overvoltage limit

MARK	Lx OVERVOLTAGE LIMIT	PRODUCT SERIES
В	Available	XC9116B02AD x
D	Not Available	XC9116D02AD x

3 d represents FB voltage



MARK FB VOLTAGE (V) **PRODUCT SERIES** 3 4 0 2 0.2 XC9116 x 02AD x

S represents oscillation frequency

MARK	OSCILLATION FREQUENCY	PRODUCT SERIES
A	1MHz	XC9116 x 02AD x

© represents production lot number

0 to 9 and A to Z repeated (G, I, J, O, Q, and W excluded) * No character inversion used.



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