

# SPECIFICATION

DEVICE NAME : Power MOSFET

TYPE NAME : 2SK2834-01

SPEC. No. :

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Fuji Electric Co., Ltd.  
Matsumoto Factory

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	DATE	NAME	APPROVED	Fuji Electric Co., Ltd.	
DRAWN				DWG. NO.	
CHECKED					
					1/12

This specifies Fuji power MOSFET 2SK2834-01

2. Construction N-channel enhancement mode power MOSFET
3. Application for switching
4. Outview T0-3P Outview See to 5/12 page
5. Absolute maximum ratings at Tc=25°C (unless otherwise specified)

Description	Symbol	Characteristics	Unit	
Drain-source voltage	V <sub>DS</sub>	600	V	
Continuous Drain current	I <sub>D</sub>	± 9	A	
Pulsed drain current	I <sub>D PULSE</sub>	± 32	A	
Gate-source voltage	V <sub>GS</sub>	± 35	V	
Repetitive or non-repetitive	I <sub>AR</sub>	9	V	Tch ≤ 150 °C
Avalanche energy	E <sub>AS</sub>	162.3	mJ	See page 12/12 ※
Maximum power dissipation	P <sub>D</sub>	80	W	
Operating and storage temperature range	T <sub>ch</sub>	150	°C	
	T <sub>stg</sub>	-55 ~ +150	°C	

※ L=3.67mH, Vcc=60V

6. Electrical characteristics at Tc=25°C (unless otherwise specified)
- Static ratings

Description	Symbol	Conditions	Characteristics			Unit
			Min.	Typ.	Max.	
Drain-source breakdown voltage	B V <sub>DSS</sub>	I <sub>D</sub> = 1mA V <sub>GS</sub> = 0V	600			V
Gate threshold voltage	V <sub>GS(th)</sub>	I <sub>D</sub> = 1mA V <sub>DS</sub> = V <sub>GS</sub>	3.5	4.0	4.5	V
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> = 600V V <sub>GS</sub> = 0V	T <sub>ch</sub> = 25°C			μA
	I <sub>DSS</sub>		T <sub>ch</sub> = 125°C			mA
Gate-source leakage current	I <sub>GSS</sub>	V <sub>GS</sub> = ±35V V <sub>DS</sub> = 0V		10	100	nA
Drain-source on-state resistance	R <sub>DS(on)</sub>	I <sub>D</sub> = 4.5A V <sub>GS</sub> = 10V		1.0	1.2	Ω

Dynamic ratings

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Description	Symbol	Conditions	Characteristics			Unit
			Min.	Typ.	Max.	
Forward transconductance	$g_{fs}$	$I_D = 4.5A$ $V_{DS} = 25V$	2.5	5.0		S
Input capacitance	$C_{iss}$	$V_{DS} = 25V$ $V_{GS} = 0V$ $f = 1MHz$		900	1400	pF
Output capacitance	$C_{oss}$			150	230	pF
Reverse transfer capacitance	$C_{rss}$			70	110	pF
Turn-on time	$t_{d(on)}$	$V_{CC} = 300V$ $V_{GS} = 10V$ $I_D = 9A$ $R_{GS} = 10\Omega$		25	40	ns
	$t_r$			70	110	ns
Turn-off time	$t_{d(off)}$			60	90	ns
	$t_f$			35	60	ns

Reverse diode

Description	Symbol	Conditions	Characteristics			Unit
			Min.	Typ.	Max.	
Avalanche capability	$I_{AV}$	$L = 3.67mH, T_{ch} = 25^\circ C$ *See Fig.1 and 2	9			A
Diode forward on-voltage	$V_{SD}$	$I_F = 2 \times I_{DR}$ $V_{GS} = 0V, T_{ch} = 25^\circ C$		1.0	1.5	V
Reverse recovery time	$t_{rr}$	$I_F = I_{DR}$ $V_{GS} = 0V$ $-di_F/dt = 100A/\mu s$ $T_{ch} = 25^\circ C$		550		ns
Reverse recovery charge	$Q_{rr}$				7.0	

7. Thermal resistance

Description	Symbol	Conditions	Characteristics			Unit
			Min.	Typ.	Max.	
Thermal resistance	$R_{th_{ch-c}}$				1.56	$^\circ C/W$
	$R_{th_{ch-a}}$				35.0	$^\circ C/W$

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H04-004-03

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Fig.1 Test circuit

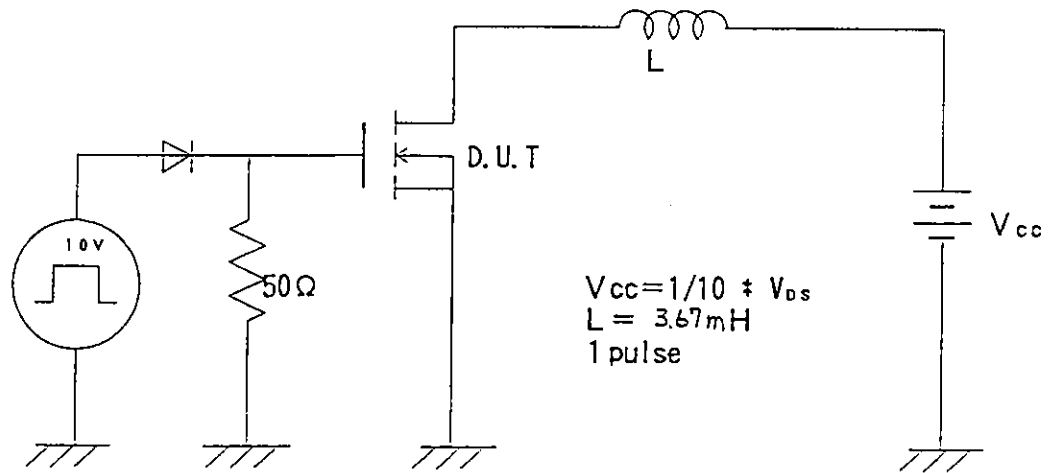
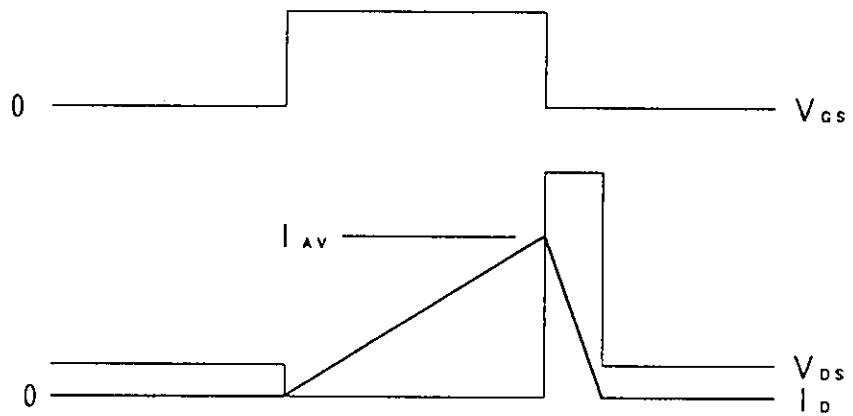
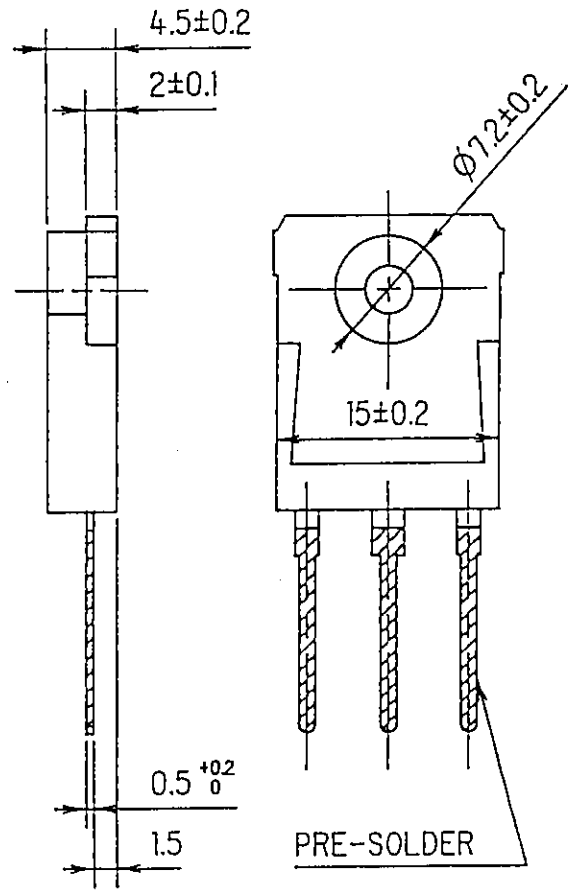
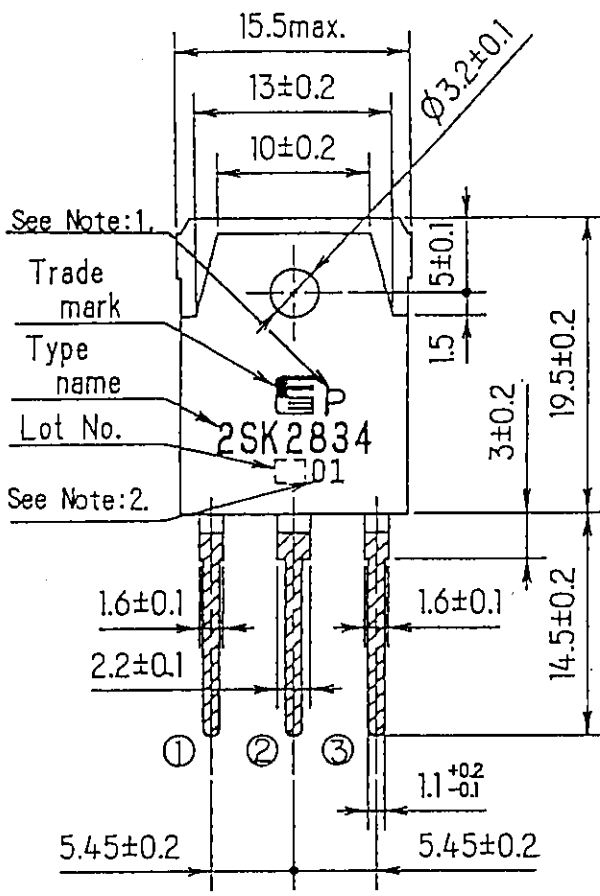


Fig.2 Operating waveforms

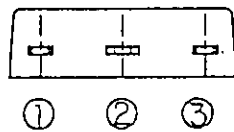


FUJI POWER MOS FET

TYPE : 2SK2834-01P



DIMENSIONS ARE IN MILLIMETERS.



CONNECTION

- ① GATE
- ② DRAIN
- ③ SOURCE

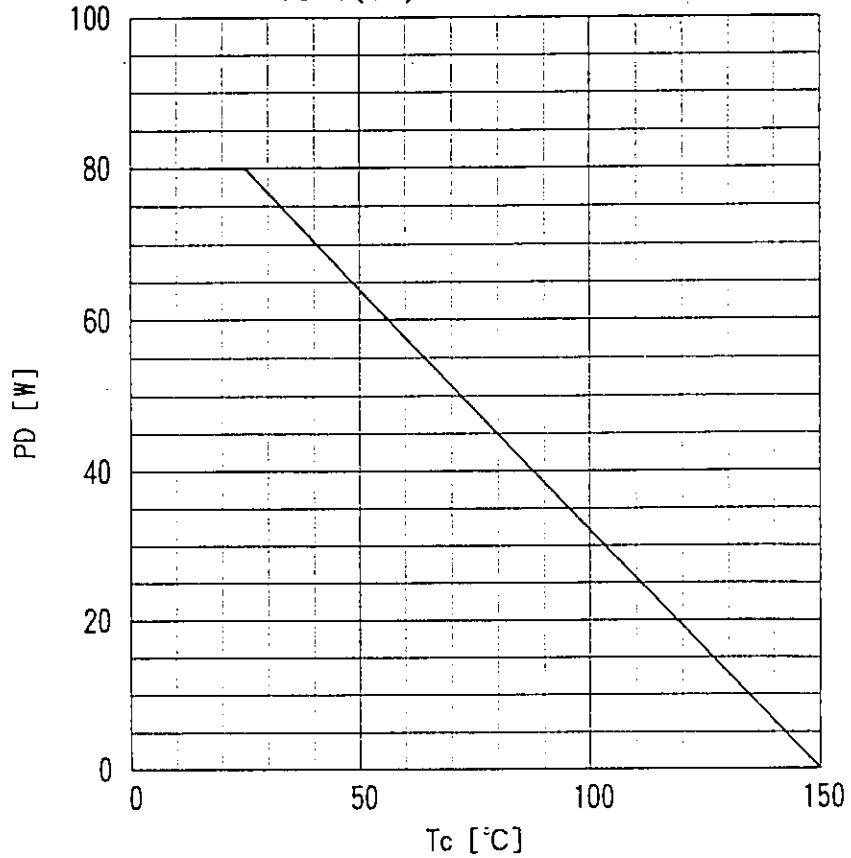
Note:1. Country of origin mark.  
No mark is Made in JAPAN  
「P」 is Made in PHILIPPINES.

2. Guaranteed mark of avalanche ruggedness.

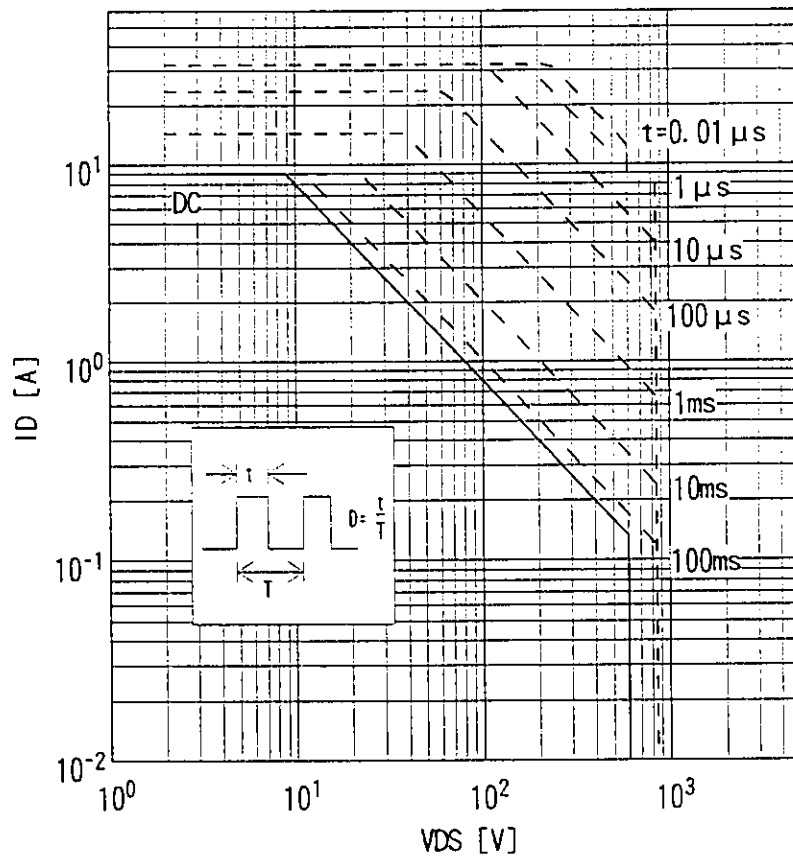
JEDEC : TO-247.  
EIAJ : SC-65

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Power Dissipation  
 $PD=f(T_c)$

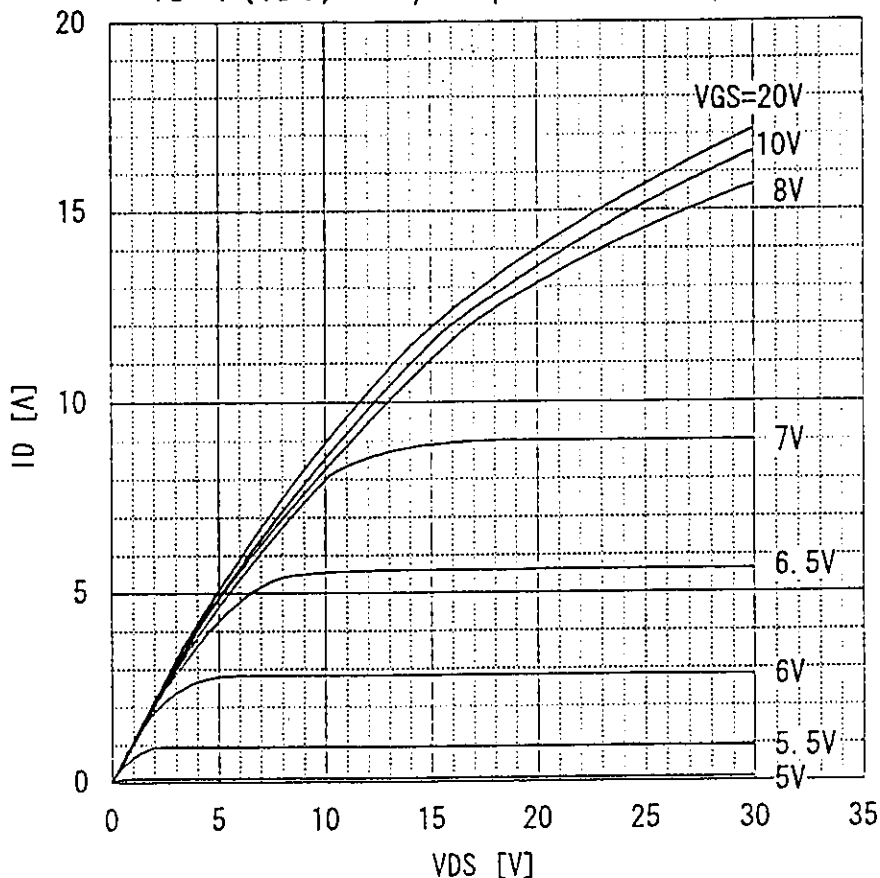


Safe operating area  
 $ID=f(V_{DS}): D=0.01, T_c=25^\circ C$

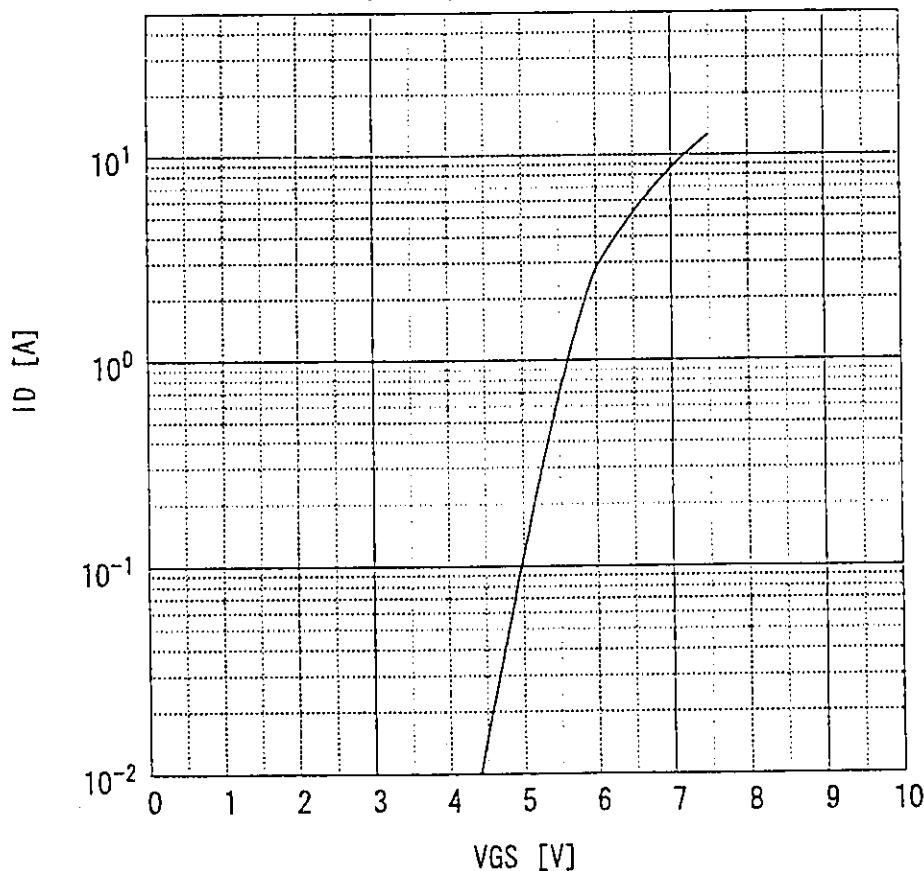


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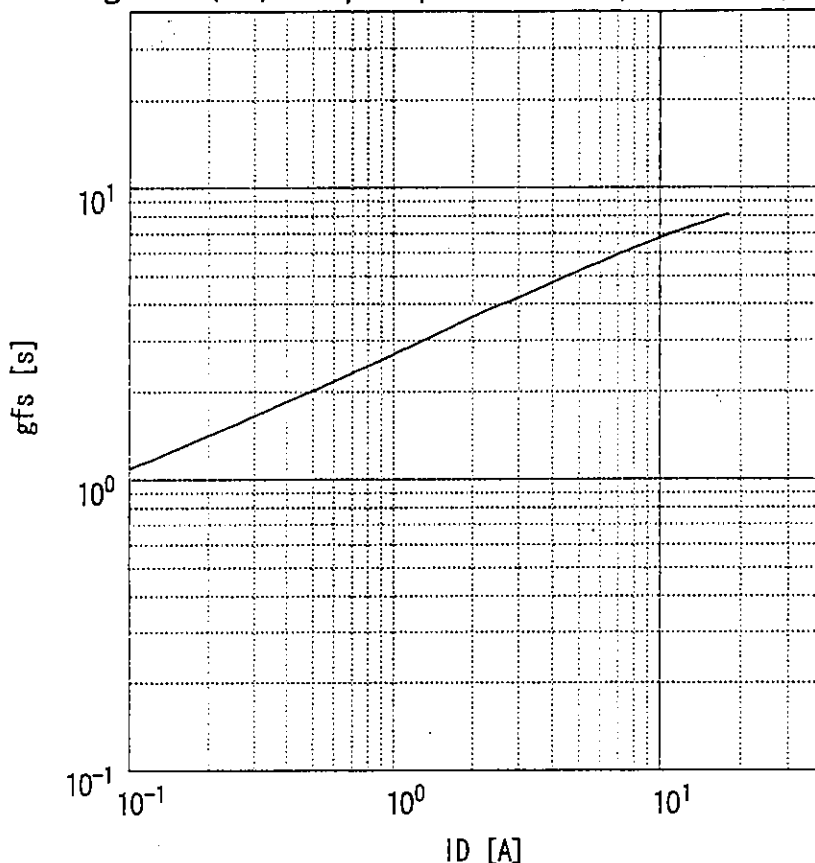
### Typical output characteristics $I_D=f(V_{DS})$ : 80 $\mu$ s pulse test, $T_c=25^\circ\text{C}$



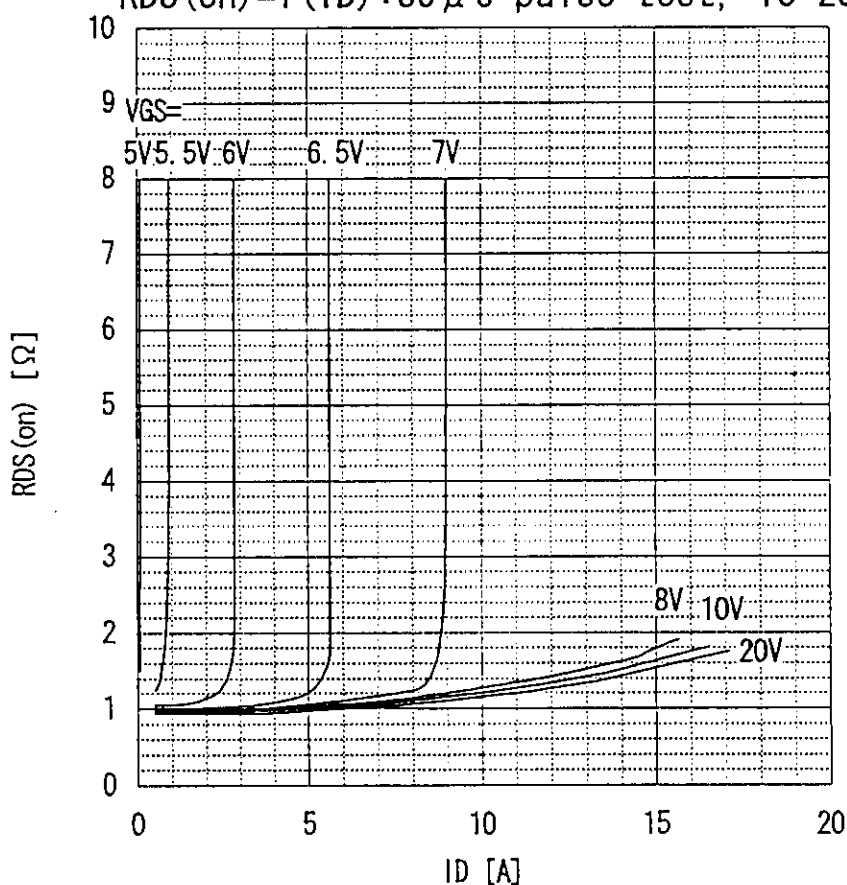
### Typical transfer characteristic $I_D=f(V_{GS})$ : 80 $\mu$ s pulse test, $V_{DS}=25\text{V}$ , $T_{ch}=25^\circ\text{C}$



Typical forward transconductance  
 $g_{fs}=f(I_D): 80\mu s$  pulse test,  $V_{DS}=25V$ ,  $T_{ch}=25^\circ C$

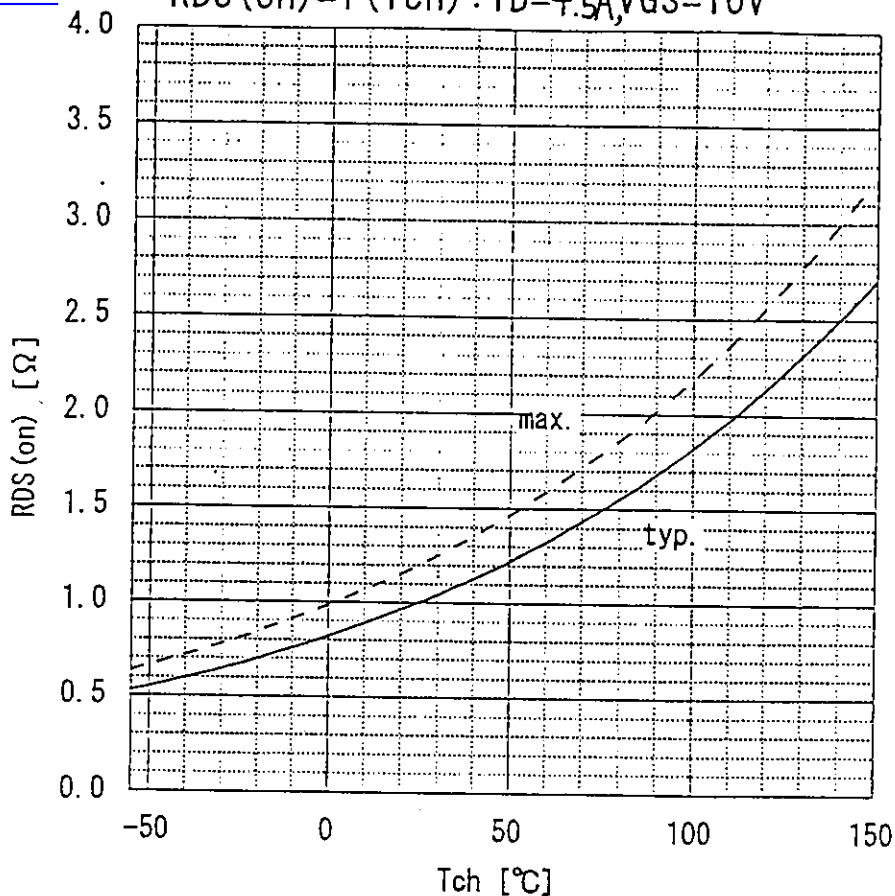


Typical drain-source on-state resistance  
 $R_{DS(on)}=f(I_D): 80\mu s$  pulse test,  $T_c=25^\circ C$

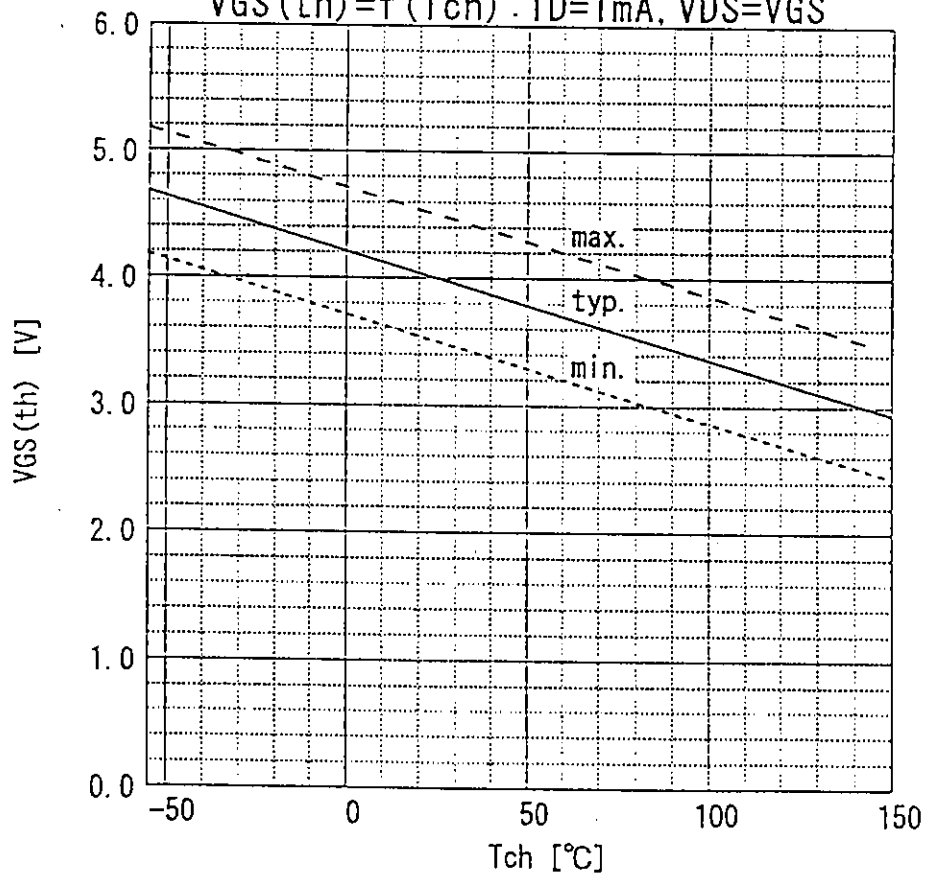




Drain-source on-state resistance  
 $R_{DS(on)} = f(T_{ch}) : I_D = 4.5A, V_{GS} = 10V$

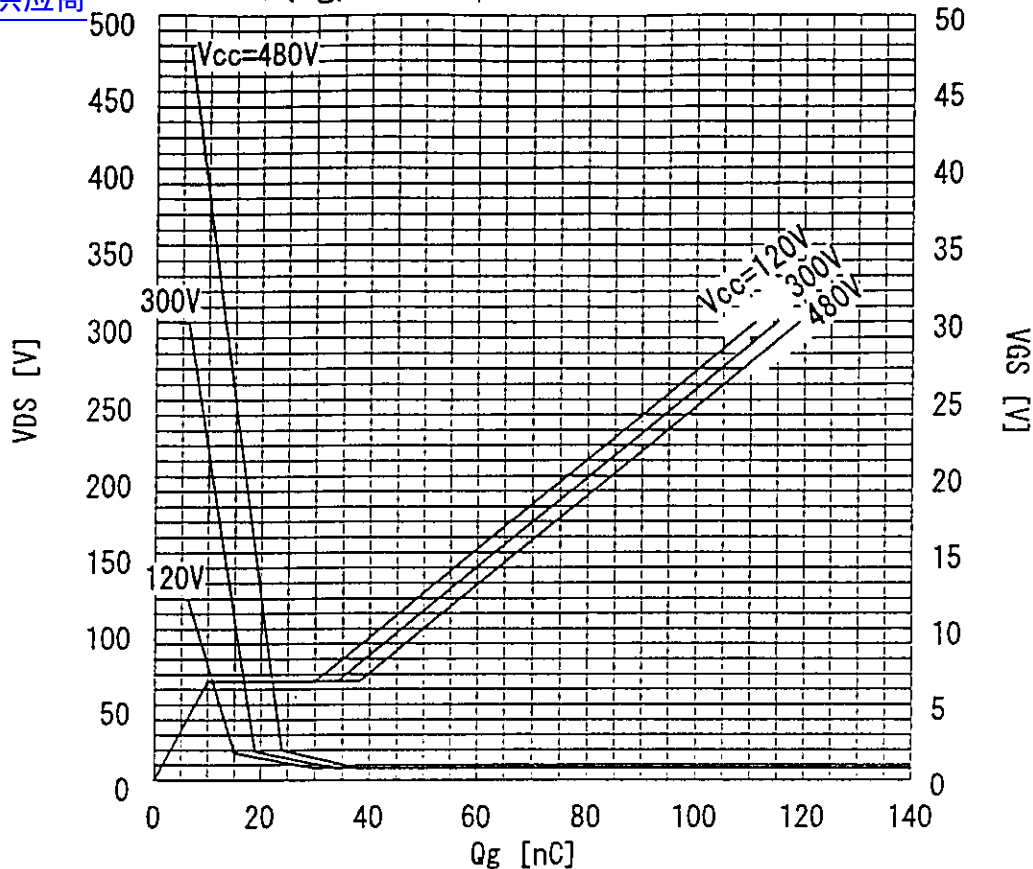


Gate threshold voltage  
 $V_{GS(th)} = f(T_{ch}) : I_D = 1mA, V_{DS} = V_{GS}$

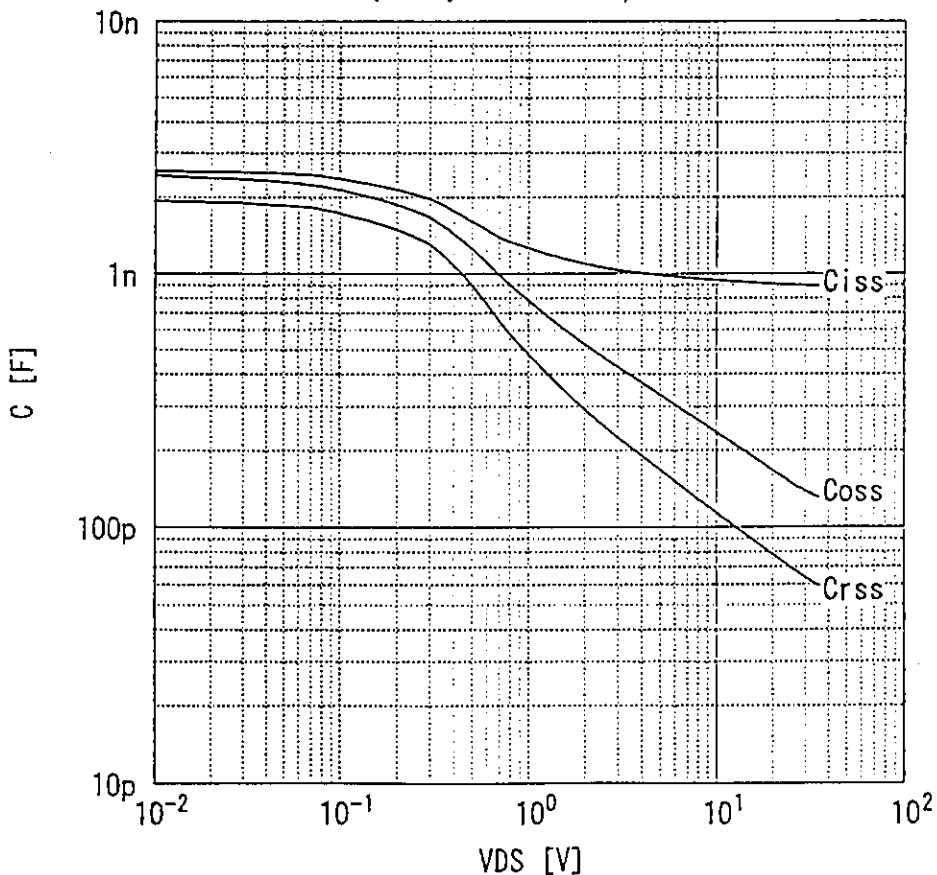


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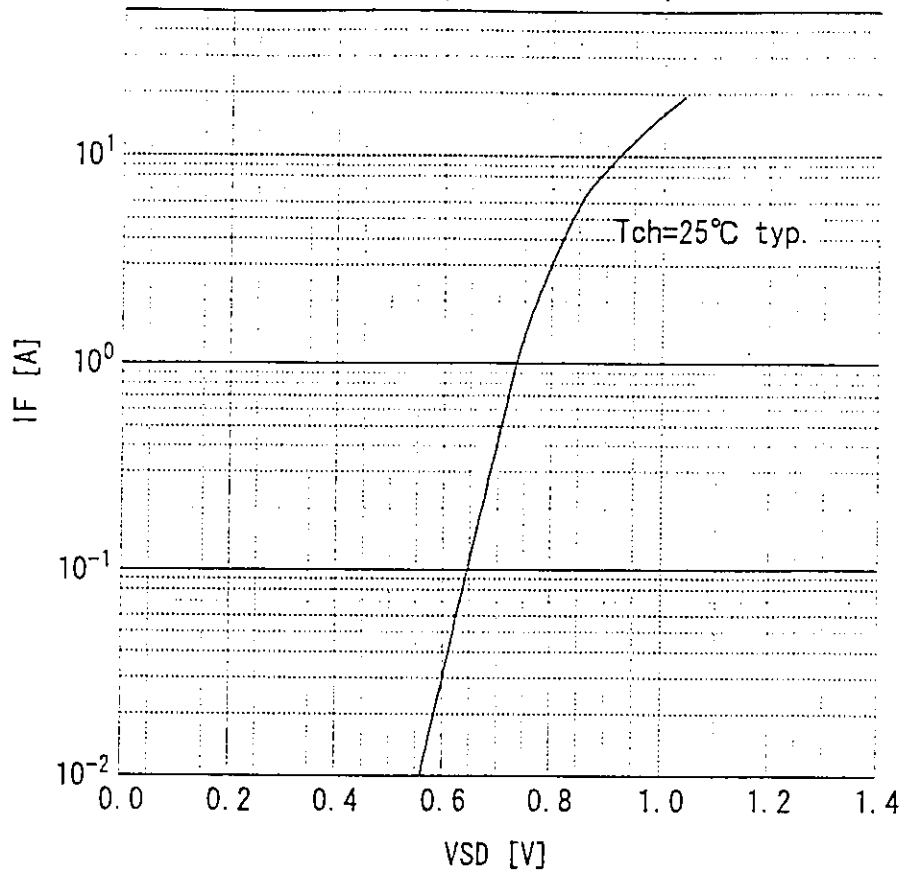
### Typical gate charge characteristic $V_{GS}=f(Q_g) : I_D=9A, T_c=25^\circ C$



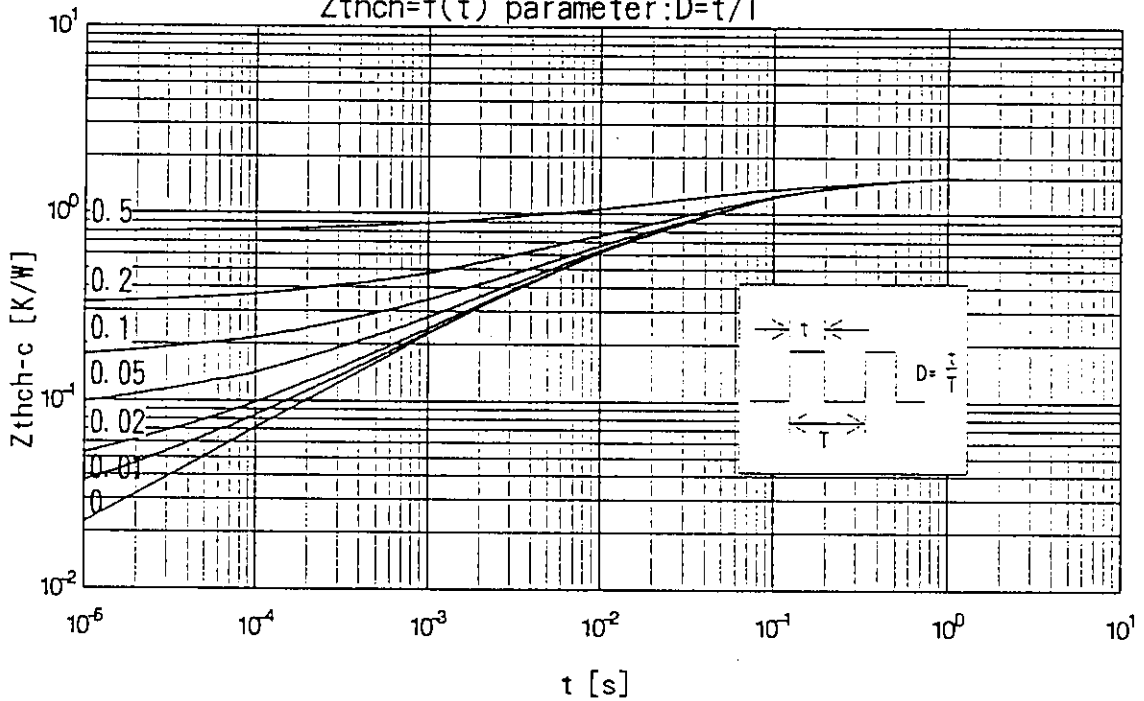
### Typical capacitances $C=f(V_{DS}) : V_{GS}=0V, f=1MHz$



Forward characteristic of reverse of diode  
 $I_F = f(V_{SD}) : 80 \mu s$  pulses test,  $V_{GS} = 0V$

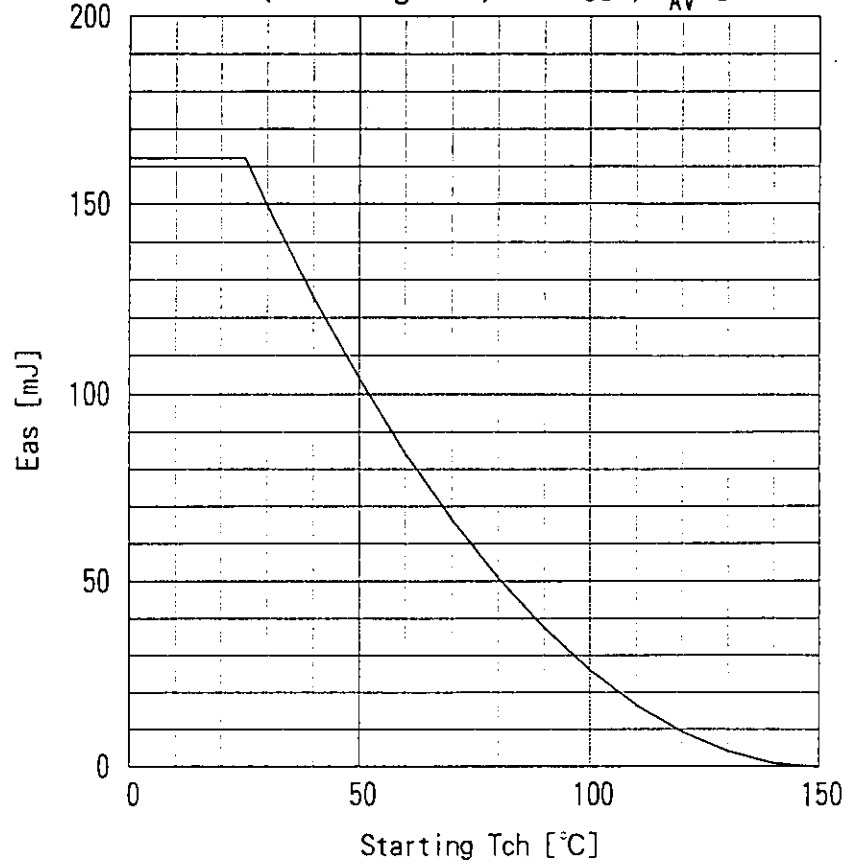


Transient thermal impedance  
 $Z_{thch} = f(t)$  parameter:  $D = t/T$



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Avalanche energy derating  
 $E_{as}=f(\text{starting } T_{ch}): V_{CC}=60V, I_{AV}=9A$



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