

Fig. 1 Typical output characteristics, chip level

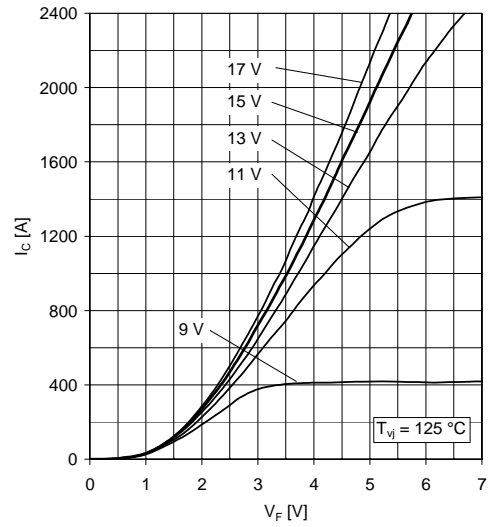


Fig. 2 Typical output characteristics, chip level

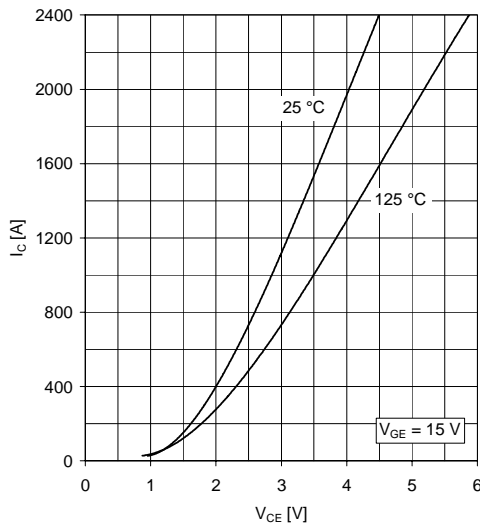


Fig. 3 Typical onstate characteristics, chip level

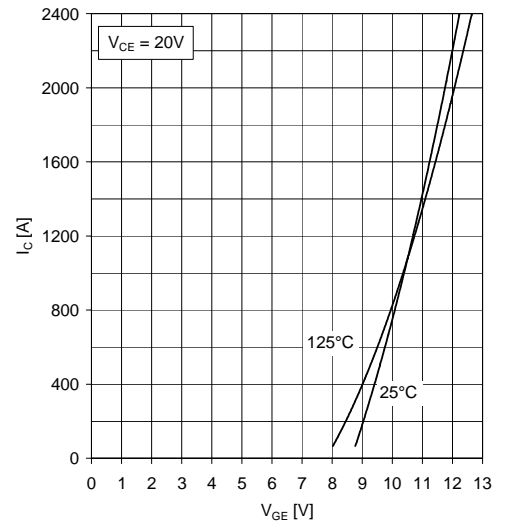


Fig. 4 Typical transfer characteristics, chip level

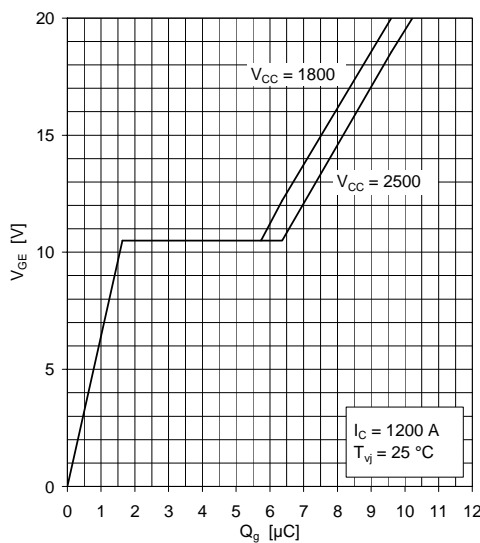


Fig. 5 Typical gate charge characteristics

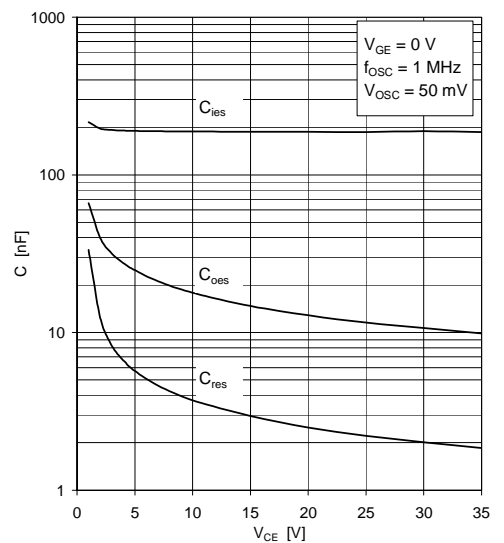


Fig. 6 Typical capacitances vs collector-emitter voltage