TOSHIBA 查询"2SK37460选成 Field Effect Transistor Silicon N Channel MOS Type (π-MOSV)

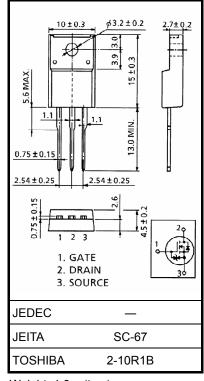
2SK3743

Switching Regulator Applications

- Low drain-source ON resistance: RDS (ON) = 0.29Ω (typ.)
- High forward transfer admittance: $|Y_{fs}| = 5.8 \text{ S}$ (typ.)
- Low leakage current: $I_{DSS} = 100 \ \mu A \ (max) \ (V_{DSS} = 450 \ V)$
- Enhancement-mode: $V_{th} = 3.0 \sim 5.0 \text{ V} (V_{DS} = 10 \text{ V}, \text{ID} = 1 \text{ mA})$

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V _{DSS}	450	V	
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)		V _{DGR}	450	V	
Gate-source voltage		V _{GSS}	±30	V	
Drain current	DC (Note 1)	۱ _D	13	А	
	Pulse (Note 1)	I _{DP}	52	A	
Drain power dissipat	ion (Tc = 25°C)	PD	40	W	
Single pulse avalanche energy (Note 2)		E _{AS}	350	mJ	
Avalanche current		I _{AR}	13	А	
Repetitive avalanche energy (Note 3)		E _{AR}	4.0	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55~150	°C	



Weight: 1.9 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

Thermal Characteristics

Characteristics	Symbol	Max	Unit	
Thermal resistance, channel to case	R _{th (ch-c)}	3.125	°C/W	
Thermal resistance, channel to ambient	R _{th (ch-a)}	62.5	°C/W	

Note 1: Please use device on condition that the channel temperature is below 150°C.

Note 2: $V_{DD} = 90 \text{ V}, \text{ T}_{ch} = 25^{\circ}\text{C}$ (initial), L = 3.46 mH, R_G = 25 Ω , I_{AR} = 13 A

Note 3: Repetitive rating; pulse width limited by maximum channel temperature.

This transistor is an electrostatic sensitive device. Please handle with caution.

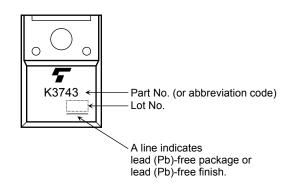
Etectrica Characteristics (Ta = 25°C)

Chara	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I _{GSS}	$V_{GS}=\pm 25~V,~V_{DS}=0~V$			±10	μA
Gate-source breakdown voltage		V (BR) GSS	$I_G=10~\mu A,~V_{DS}=0~V$	±30		_	V
Drain cut-off curr	ent	I _{DSS}	$V_{DS} = 450 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			100	μA
Drain-source bre	akdown voltage	V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	450	_	_	V
Gate threshold ve	oltage	V _{th}	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$	3.0		5.0	V
Drain-source ON	resistance	R _{DS (ON)}	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 6 \text{ A}$	_	0.29	0.4	Ω
Forward transfer	admittance	Y _{fs}	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 6 \text{ A}$	3.0	5.8	_	S
Input capacitance		C _{iss}	$V_{DS} = 25 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ f} = 1 \text{ MHz}$	_	1600	_	pF
Reverse transfer capacitance		C _{rss}		_	17	_	
Output capacitance		C _{oss}		_	220	_	
Switching time	Rise time	tr	$V_{GS}^{10 V} \downarrow I_D = 6 A$ $0 V \downarrow I_D = 6 A$ $R_L =$ 33.3Ω $V_{DD} \approx 200 V$ Duty $\leq 1\%$, $t_W = 10 \mu s$	_	28		
	Turn-on time	t _{on}			45		- ns
	Fall time	t _f			10	_	
	Turn-off time	t _{off}		_	56	_	
Total gate charge		Qg			34		nC
Gate-source charge		Q _{gs}	$V_{DD}\simeq 360~V,~V_{GS}=10~V,~I_{D}=13~A$		19	_	
Gate-drain charge		Q _{gd}			15		

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	—	_	_	13	А
Pulse drain reverse current (Note 1)	I _{DRP}	—	_	_	52	А
Forward voltage (diode)	V _{DSF}	I _{DR} = 13 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time	t _{rr}	I _{DR} = 13 A, V _{GS} = 0 V,	_	300	_	ns
Reverse recovery charge	Q _{rr}	dl _{DR} /dt = 100 A/μs	_	3.4	_	μC

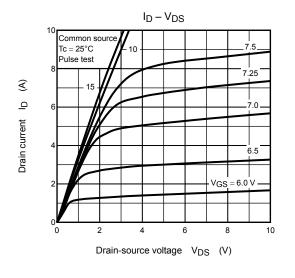
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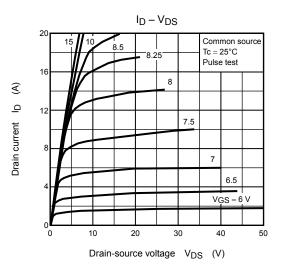


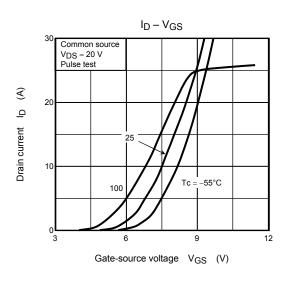
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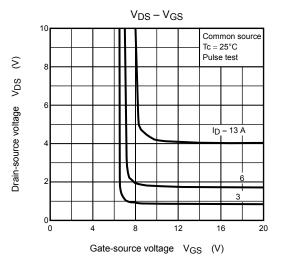
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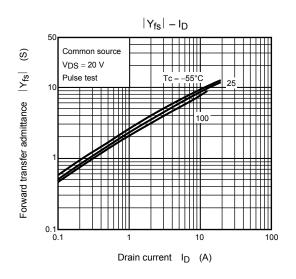
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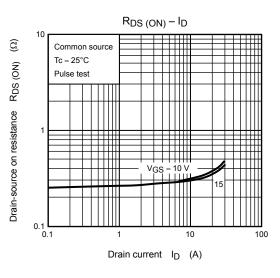








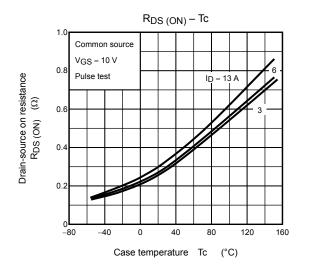


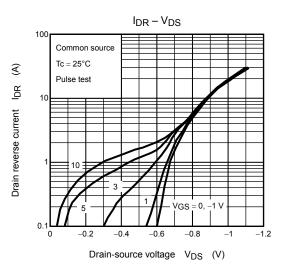


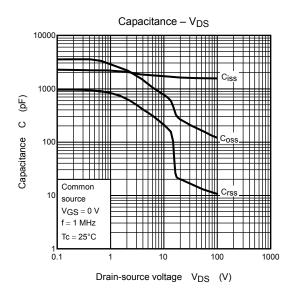
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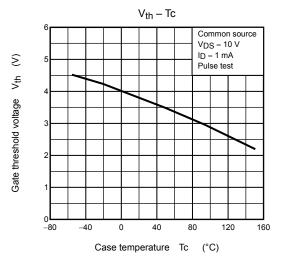
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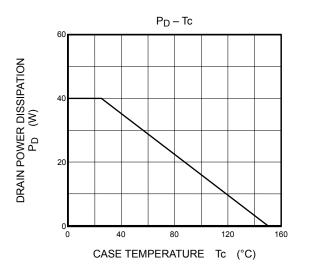
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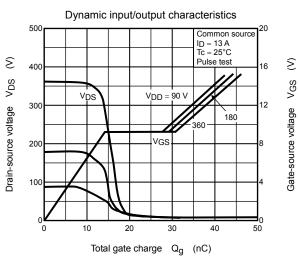








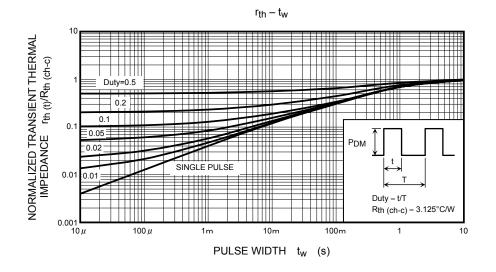




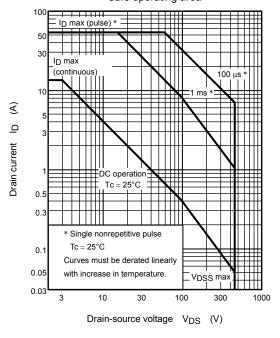
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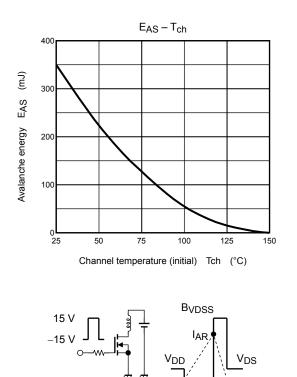
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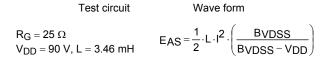
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Safe operating area







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