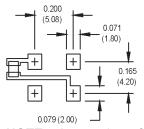


## SUGGESTED SOLDER PAD LAYOUT



NOTE: A capacitor of value 0.01 μF or greater between Vdd and Ground is recommended.

## **Pin Connections**

PIN	FUNCTION				
1	N/C or Tristate				
2	Ground				
3	Output				
4	+Vdd				

Ordering Inform	M2	1	2	т	C		00.0000 MHz	
	1.1.2	i	ĭ	i	Ĭ	ï	1	
Product Series -								
Temperature Range								
1: 0°C to +70°C		85°C						
3: -55°C to +105°C	4: -55°C to +	125°C	١ ١					
5: -10°C to +125°C	6: -20°C to +	70°C						
7: 0°C to +85°C								
Stability ———			_					
<b>3</b> : ±100 ppm								
<b>5</b> : ±35 ppm	<b>6</b> : ±25 ppm							
*8: ±20 ppm								
Output Type ———								
F: Fixed Q: Star	idby Function	T:	Trista	te		- 1		
Symmetry/Logic Com						- 1		
A or G: 40/60 @ 50°	% Vdd**					- 1		
C: 45/55 HCMOS	4:					- 1		
Package/Lead Configurations —————————								
N: Leadless Cerami	-							
Frequency (customer	specified) -							

\*Contact Factory for Availability
\*\* A and G codes are used interchangeably on the M2 Series

M2002Sxxx - Contact factory for datasheet

	PARAMETER	Symbol	Min.	Тур.	Max.	Units	Condition/Notes			
	Frequency Range	F	1.5		135	MHz	See Note 1			
	Operating Temperature	TA	(See orderi	ng inforn	nation)					
	Storage Temperature	Ts	-55	+125 °C						
	Frequency Stability	ÄF/F	(See ordering information)							
	Aging									
Electrical Specifications	1 <sup>st</sup> Year			±3		ppm				
	Thereafter (per year)			±2		ppm				
	Input Voltage	Vdd	3.0	3.3	3.6	V				
	Input Current	ldd			10	mA	1.500 to 20.000 MHz			
					20	mA	20.001 to 50.000 MHz			
					30	mA	50.001 to 67.000			
					55	mA	67.001 to 135.000 MHz			
	Standby Current				10	μΑ	"Q" Output Type			
	Output Type						HCMOS/TTL Compatible			
	Load		2 TTL or 15	pF		See Note 2				
	Symmetry (Duty Cycle)		(See ordering information)				½ Vdd			
	Logic "1" Level	Voh	90% Vdd			V	HCMOS Load			
			Vdd -0.5			V	TTL Load			
"	Logic "0" Level	Vol			10% Vdd	V	HCMOS Load			
					0.5	V	TTL Load			
	Output Current				±4	mA				
	Rise/Fall Time	Tr/Tf					See Note 3			
					6	ns	1.500 to 50.000 MHz			
					4	ns	50.001 to 80.000 MHz			
					2	ns	80.001 to 135.000 MHz			
	Standby/Tristate Function				ating: output					
			Input Logic "0"; output disables to high-Z							
	Start up Time				10	ms				
	Random Jitter	Rj		4	10	ps RMS	1-Sigma			
<del>-</del>	Mechanical Shock		Per MIL-STD-202, Method 213, Condition C (100 g's, 6 mS duration, ½ sinewave)							
ent	Vibration	Per MIL-STD-202, Method 201 & 204 (10 g's from 10-2000 Hz)								
Environmental	Hermeticity	Per MIL-STD-202, Method 112, (1x10 <sup>-8</sup> atm. cc/s of Helium)								
	Thermal Cycle		Per MIL-STD-883, Method 1010, Condition B (-55°C to +125°C, 15 min. dwell, 10 cycles)							
2	Solderability		EIAJ-STD-002							
Ш	Soldering Conditions	ring Conditions See solder profile, Figure 1								

- 1. Consult factory for availability of higher frequencies.
- 2. HCMOS Load See Load circuit diagram #2. Consult factory with nonstandard output load requirements.

  3. Rise/Fall times are measured between 0.5 V and 2.4 V with TTL load, and between 10% Vdd and 90% Vdd with HCMOS load.

MtronPTI reserves the right to make changes to the product(s) and service(s) described herein without notice. No liability is assumed as a result of their use or application.





