

# A\_D-1W & B\_LD-1W Series

# 1W, FIXED INPUT, ISOLATED & UNREGULATED DUAL/SINGLE OUTPUT DC-DC CONVERTER



ROHS S (E CTUS

#### **FEATURES**

High Efficiency up to 80%
1KVDC Isolation
DIP Package
Internal SMD Construction
Temperature Range: -40°C ~ +85°C
No Heat sink Required
No External Component Required
Industry Standard Pinout
RoHS Compliance

#### **APPLICATIONS**

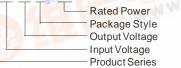
The A\_D-1W & B\_LD-1W Series are specially designed for applications where a group of polar power supplies are isolated from the input power supply in a distributed power supply system on a circuit board.

These products apply to:

- Where the voltage of the input power supply is fixed (voltage variation ≤ ±10%);
- Where isolation is necessary between input and output (isolation voltage ≤1000VDC);
- Where the regulation of the output voltage and the output ripple noise are not demanding.

Such as: purely digital circuits, ordinary low frequency analog circuits, and IGBT power device driving circuits.

## MODEL SELECTION A0505D-1W



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_	Input Voltage (VDC)			Output			
Part Number			Voltage	Current (mA)		Efficiency (%, Typ)	Certificate
	Nominal	Range	(VDC)	Max	Min		
B0303LD-1W	3.3	3.0-3.6	3.3	303	31	72	
B0305LD-1W	3.3	3.0-3.0	5	200	20	74	
A0505D-1W			±5	±100	±10	72	UL
A0509D-1W			±9	±56	±6	77	UL
A0512D-1W			±12	±42	±5	79	UL
A0515D-1W			±15	±33	±4	80	UL
B0505LD-W5	5	4.5-5.5	5	100	10	68	
B0505LD-1W			5	200	20	70	UL CE
B0509 LD-1W			9	111	12	78	UL CE
B0512 LD-1W			12	83	9	78	UL CE
B0515 LD-1W			15	67	7	80	UL CE
A1205D-1W			±5	±100	±10	72	UL
A1209D-1W			±9	±56	±6	78	UL
A1212D-1W			±12	±42	±5	79	UL
A1215D-1W			±15	±33	±4	78	UL
B1203 LD-1W	12	10.8-13.2	3.3	303	31	73	
B1205 LD-1W		- Aller	5	200	20	71	UL CE
B1209 LD-1W			9	111	12	76	UL CE
B1212 LD-1W			12	83	9	78	UL CE
B1215 LD-1W			15	67	7	79	UL CE
A1505D-1W	45	40 5 40 5	±5	±100	±10	72	
B1515LD-1W	15	13.5-16.5	15	67	7	75	
A2405D-1W			±5	±100	±10	73	UL
A2409D-1W			±9	±56	±6	79	UL
A2412D-1W			±12	±42	±5	80	UL
A2415D-1W			±15	±33	±4	80	UL
B2405 LD-1W	24	21.6-26.4	5	200	20	73	UL CE
B2409 LD-1W			9	111	12	78	UL CE
B2412 LD-1W			12	83	9	78	UL CE
B2415 LD-1W			15	67	7	79	UL CE
B2424LD-1W			24	42	4	78	

Item	Test conditions	Min	Тур	Max	Units
Operating Temp.		-40	50%	85	°C
Storage Temp. Range	Maria Maria	-55		125	
Storage humidity	B. STELLEY			95	%
Cooling	/A	Fre	e air c	ion	
Temp. rise at full load			15	25	°C
Lead temperature	1.5mm from case for 10 seconds			300	
Isolation voltage	Tested for 1 minute and 1 mA max	1000			VDC
Isolation resistance	Test at 500VDC	1000			ΜΩ
Short circuit protection*				1	s
Case material		PI	astic (l	0)	
MTBF		3500			K hours
Weight			2.1		G

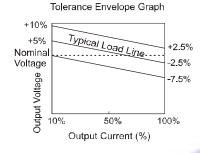
<b>OUTPUT SPEC</b>	CIFICATIONS						
Item	Test conditions	Min	Тур	Max	Units		
<b>学前世級%95D-1</b>	₩№05D-1W"供应商				1	W	
Line regulation	For Vin change of 1%	(3.3V output)			±1.5	%	
Line regulation		(Other output)			±1.2		
	10% to 100% load	(3.3V output)		12	20		
		(5V output)		10.5	15		
Load regulation		(9V output)		8.3	15		
		(12V output)		6.8	15		
		(15V output)		6.3	15		
Output voltage accur	acy		See tolerance envelope graph			graph	
Temperature drift	100% full load				0.03	%/°C	
	20MHz Bandwidth	(AXXXXD-1W)		50	75		
Ripple & Noise*		(BXXXXD-1W)		75	100	mVp-p	
	Banawiatii	(A/BXX24D-1W)		100	150		
Switching frequency	Full load, nominal input			100		KHz	

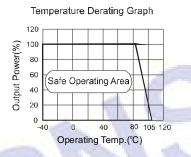
\*Test ripple and noise by "parallel cable" method. See detailed operation instructions at Testing of Power Converter section, application notes.

#### Note

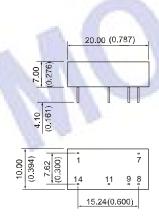
- 1.All specifications measured at T<sub>A</sub>=25°C, humidity<75%, nominal input voltage and rated output load unless otherwise specified.</p>
- 2. Dual output models unbalanced load: ±5%

#### **TYPICAL CHARACTERISTICS**





## **OUTLINE DIMENSIONS & PIN CONNECTIONS**

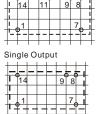


Note: Unit:mm(inch) Pin section:0.50\*0.30mm(0.020\*0.012inch) Pin tolerances:±0.10mm(±0.004inch) General tolerances:±0.25mm(±0.010inch)

#### First Angle Projection ← ⊕

RECOMMENDED FOOTPRINT Top view, grid:2.54mm(0.1inch) diameter:1.00mm(0.039inch)

**Dual Output** 



## FOOTPRINT DETAILS Pin Single D

Single	Dual
GND	GND
NC	NC
0V	0V
+Vo	+Vo
No Pin	-Vo
Vin	Vin
	GND NC 0V +Vo No Pin

## **APPLICATION NOTE**

#### Requirement on output load

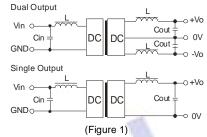
To ensure this module can operate efficiently and reliably, During operation, the minimum output load is **not less than 10%** of the full load, and that **this product should never be operated under no load!** If the actual output power is very small, please connect a resistor with proper resistance at the output end in parallel to increase the load, or use our company's products with a lower rated output power (A\_D -W25/B\_LD-W25 series)

#### **Overload Protection**

Under normal operating conditions, the output circuit of these products has no protection against overload. The simplest method is to connect a self-recovery fuse in series at the input end or add a circuit breaker to the circuit.

#### Recommended circuit

If you want to further decrease the input/output ripple, an "LC" filtering network may be connected to the input and output ends of the DC/DC converter, see (Figure 1).



It should also be noted that the inductance and the frequency of the "LC" filtering network should be staggered with the DC/DC frequency to avoid mutual interference. However, the capacitance of the output filter capacitor must be proper. If the capacitance is too big, a startup problem might arise. For every channel of output, provided the safe and reliable operation is ensured, the recommended capacitance of its filter capacitor sees (Table 1).

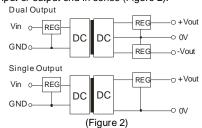
EXTERNAL CAPACITOR TABLE (TABLE 1)

Vin (VDC)	Cin (uF)	Single Vout (VDC)	Cout (uF)	Dual Vout (VDC)	Cout (uF)
5	4.7	5	10	±5	4.7
12	2.2	9	4.7	±9	2.2
15	2.2	12	2.2	±12	1
24	1	15	1	±15	0.47

It's not recommended to connect any external capacitor in the application field with less than 0.5 watt output.

# Output Voltage Regulation and Over-voltage Protection Circuit

The simplest device for output voltage regulation, over-voltage and over-current protection is a linear voltage regulator with overheat protection that is connected to the input or output end in series (Figure 2).



No parallel connection or plug and play.