查询"2SK344f也连点离 Field Effect Transistor Silicon N Channel MOS Type (U-MOSII)

2SK3441

DC-DC Converter Relay Drive and Motor Drive Applications

• Low drain-source ON resistance: $RDS(ON) = 4.5 \text{ m}\Omega \text{ (typ.)}$

• High forward transfer admittance: $|Y_{fs}| = 80 \text{ S (typ.)}$

• Low leakage current: $IDSS = 100 \mu A (max) (VDS = 60 V)$

• Enhancement-mode: $V_{th} = 1.3 \text{ to } 2.5 \text{ V (V}_{DS} = 10 \text{ V, I}_{D} = 1 \text{ mA)}$

Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	60	V	
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)		V_{DGR}	60	V	
Gate-source voltage		V_{GSS}	±20	V	
Drain current	DC (Note 1)	I _D	75	A	
	Pulse ($t \le 1 \text{ ms}$) (Note 1)	I _{DP}	300		
Drain power dissipation (Tc = 25°C)		P _D	125	W	
Single pulse avalanche energy (Note 2)		E _{AS}	468	mJ	
Avalanche current		I _{AR}	75	Α	
Repetitive avalanche energy (Note 3)		E _{AR}	12.5	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55 to 150	°C	

Thermal Characteristics

Characteristics	Symbol	Max	Unit	
Thermal resistance, channel to case	R _{th (ch-c)}	1.00	°C/W	

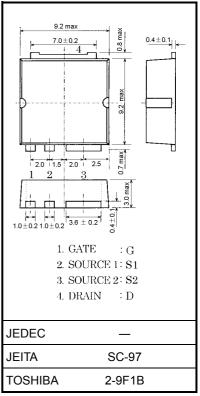
Note 1: Please use devices on condition that the channel temperature is below 150°C.

Note 2: V_{DD} = 25 V, T_{ch} = 25°C (initial), L = 113 μH , R_G = 25 Ω , I_{AR} = 75 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic sensitive device. Please handle with caution.

Unit: mm

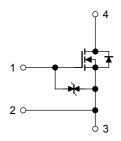


Weight: 0.74 g (typ.)

Circuit Configuration

Notice:

Please use the S1 pin for gate input signal return. Make sure that the main current flows into S2 pin.





Electrical Character istics (Note 4) (Ta = 25°C)

Cha	aracteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rent	I _{GSS}	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μА
Drain cut-off curre	ent	I _{DSS}	V _{DS} = 60 V, V _{GS} = 0 V	_	_	100	μА
Drain-source breakdown voltage		V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	60	_	_	V
		V _{(BR) DSX}	$I_D = 10 \text{ mA}, V_{GS} = -20 \text{ V}$	40	_	_	v
Gate threshold voltage		V _{th}	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$	1.3	_	2.5	V
Drain-source ON resistance		R _{DS (ON)}	V _{GS} = 10 V, I _D = 38 A	_	4.5	5.8	- mΩ
			V _{GS} = 4 V, I _D = 38 A		5.8	10	
Forward transfer	admittance	Y _{fs}	V _{DS} = 10 V, I _D = 38 A	40	80	_	S
Input capacitance	;	C _{iss}		_	9300	_	
Reverse transfer capacitance		C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz		910		pF
Output capacitance		Coss			1435	_	
Switching time	Rise time	t _r	V _{GS} 10 V	_	18	_	- ns
	Turn-on time	t _{on}		_	40	_	
	Fall time	t _f			42		
	Turn-off time	t _{off}	Duty ≦ 1%, t _W = 10 μs	1	250		
Total gate charge (gate-source plus gate-drain)		Qg			210		nC
Gate-source charge		Q _{gs}	$V_{DD} \simeq 48 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 75 \text{ A}$		145	_	
Gate-drain ("miller") charge		Q _{gd}			65		

Note 4: Please connect the S1 pin and S2 pin, and then ground the connected pin. (However, while switching times are measured, please don't connect and ground it.)

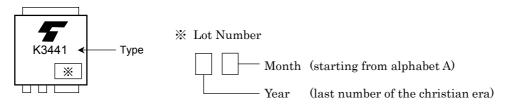
Source-Drain Ratings and Characteristics (Note 5) (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1, Note 5)	I _{DR} 1	_	_	_	75	Α
Pulse drain reverse current (Note 1, Note 5)	I _{DRP} 1	_	_	_	300	Α
Continuous drain reverse current (Note 1, Note 5)	I _{DR} 2	_	_	_	1	Α
Pulse drain reverse current (Note 1, Note 5)	I _{DRP} 2	_	_	_	4	Α
Forward voltage (diode)	V _{DS2F}	I _{DR} 1 = 75 A, V _{GS} = 0 V	_	_	-1.5	V
Reverse recovery time	t _{rr}	$I_{DR} = 75 \text{ A}, V_{GS} = 0 \text{ V},$		60		ns
Reverse recovery charge	Q _{rr}	dI _{DR} /dt = 50 A/μs	_	50	_	nC

Note 5: drain, flowing current value between the S2 pin, open the S1 pin drain, flowing current value between the S1 pin, open the S2 pin

Unless otherwise specified, please connect the S1 and S2 pins, and then ground the connected pin.

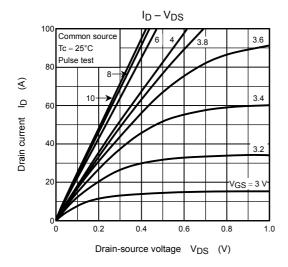
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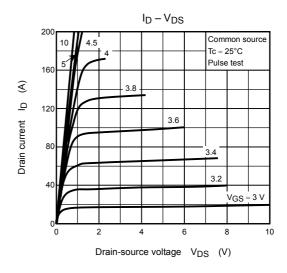


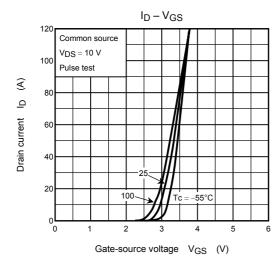
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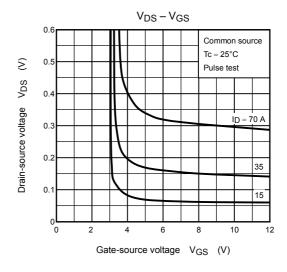
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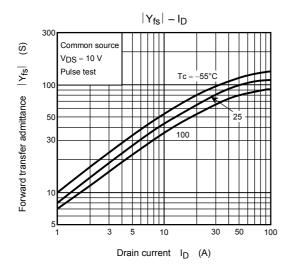
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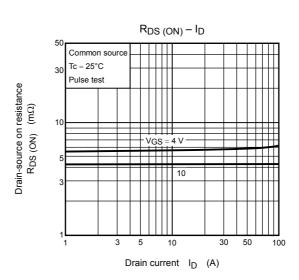






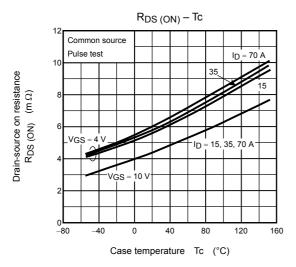


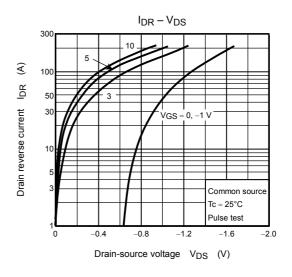


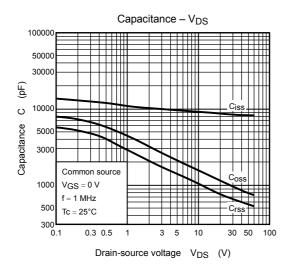


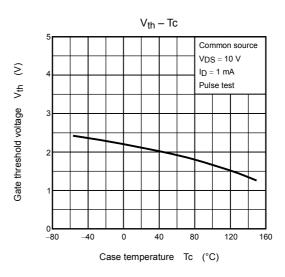
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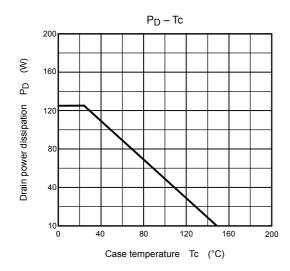
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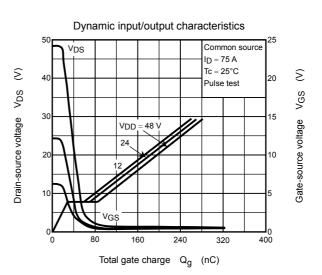






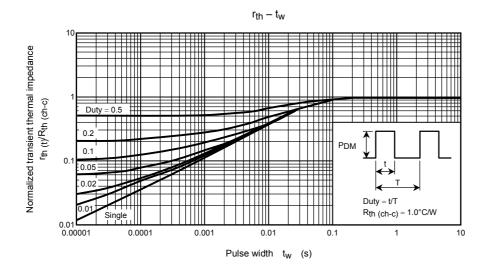


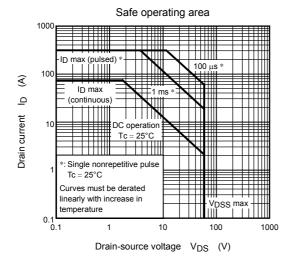


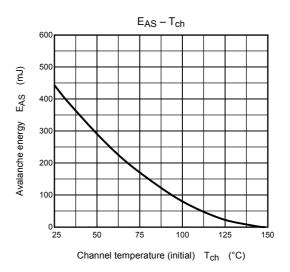


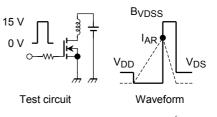
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$$\begin{aligned} R_G &= 25~\Omega \\ V_{DD} &= 25~V,~L = 236~\mu H \end{aligned} \qquad E_{AS} &= \frac{1}{2} \cdot L \cdot I^2 \cdot \left(\frac{BVDSS}{BVDSS} - V_{DD} \right) \end{aligned}$$

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