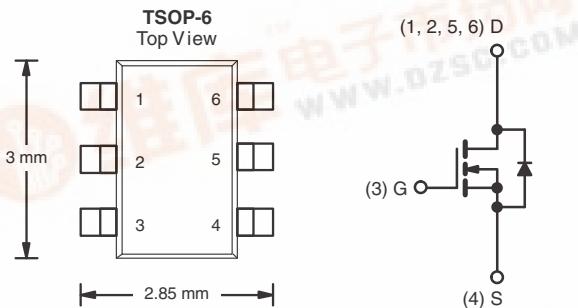


Automotive N-Channel 30 V (D-S) 175 °C MOSFET

PRODUCT SUMMARY	
V _{DS} (V)	30
R _{DS(on)} (Ω) at V _{GS} = 10 V	0.035
R _{DS(on)} (Ω) at V _{GS} = 4.5 V	0.052
I _D (A)	8
Configuration	Single



FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET
- Compliant to RoHS Directive 2002/95/EC
- AEC-Q101 Qualified^d
- Find out more about Vishay's Automotive Grade Product Requirements at: www.vishay.com/applications



ORDERING INFORMATION

Package	TSOP-6
Lead (Pb)-free and Halogen-free	SQ3456EV-T1-GE3

ABSOLUTE MAXIMUM RATINGS (T_C = 25 °C, unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	V _{DS}	30	V
Gate-Source Voltage	V _{GS}	± 20	
Continuous Drain Current ^a	T _C = 25 °C	I _D	
	T _C = 125 °C	4.6	
Continuous Source Current (Diode Conduction) ^a	I _S	5	A
Pulsed Drain Current ^b	I _{DM}	32	
Single Pulse Avalanche Current	I _{AS}	13	
Single Pulse Avalanche Energy	E _{AS}	8.5	mJ
Maximum Power Dissipation ^b	T _C = 25 °C	P _D	W
	T _C = 125 °C	1.3	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 to + 175	°C

THERMAL RESISTANCE RATINGS

PARAMETER	SYMBOL	LIMIT	UNIT
Junction-to-Ambient	R _{thJA}	110	°C/W
Junction-to-Foot (Drain)	R _{thJF}	38	

Notes

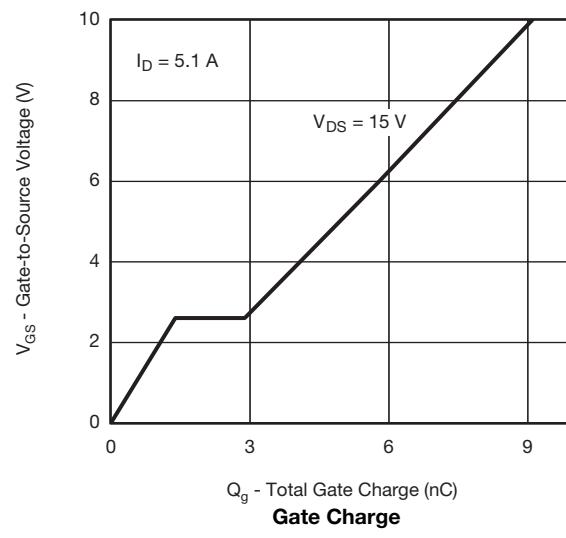
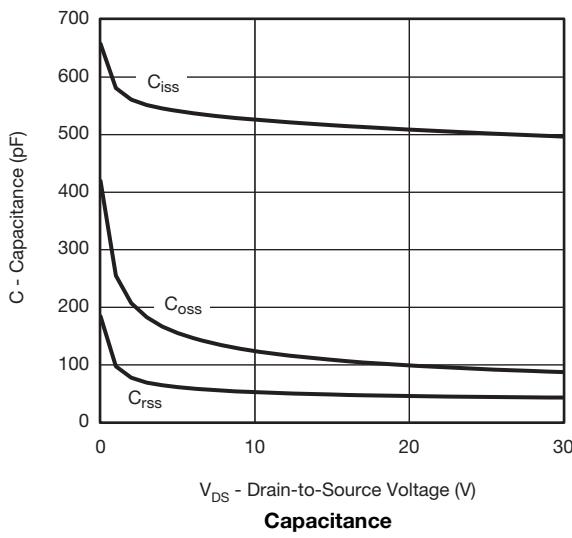
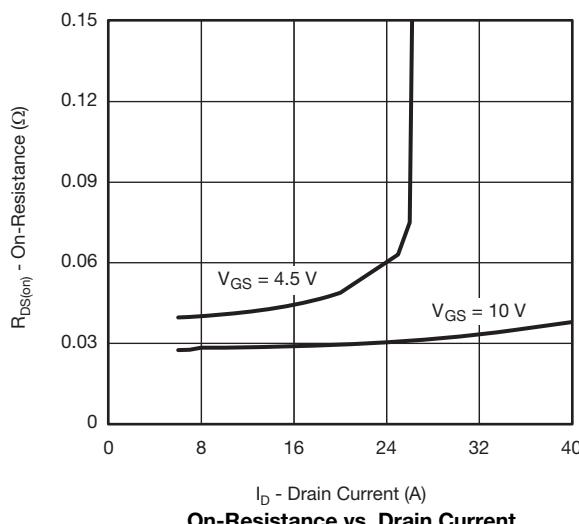
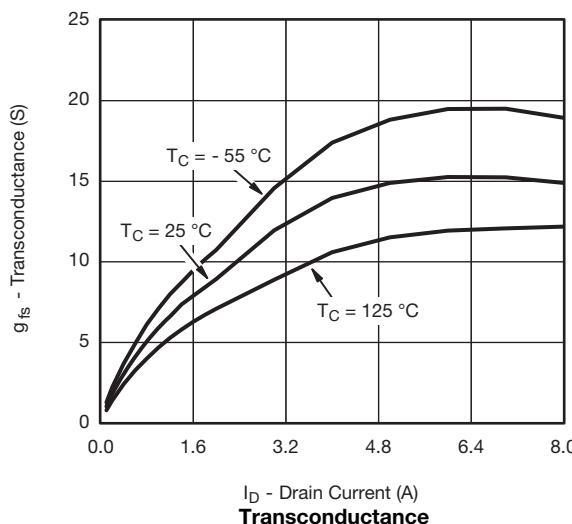
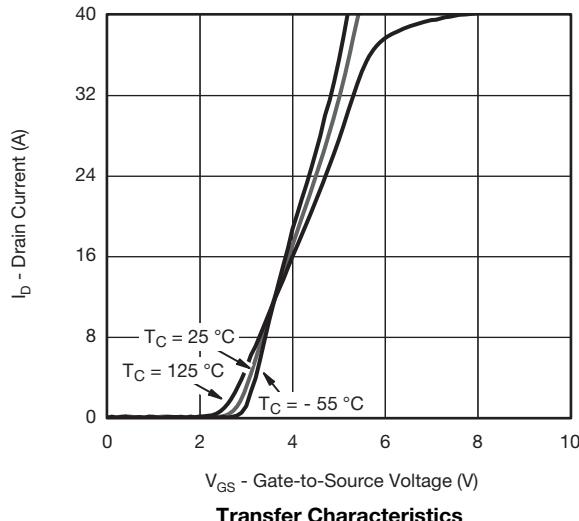
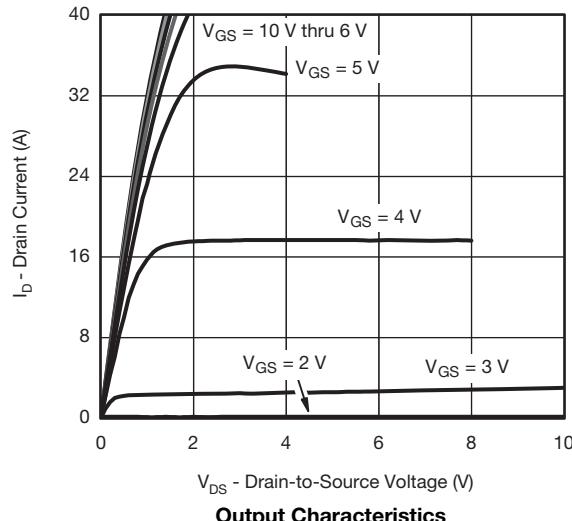
- Package limited.
- Pulse test; pulse width ≤ 300 µs, duty cycle ≤ 2 %.
- When mounted on 1" square PCB (FR-4 material).
- Parametric verification ongoing.

SPECIFICATIONS ($T_C = 25^\circ\text{C}$, unless otherwise noted)								
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT	
Static								
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$		30	-	-	V	
Gate-Source Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 250 \mu\text{A}$		1.5	2.0	2.5		
Gate-Source Leakage	I_{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$		-	-	± 100	nA	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{GS} = 0 \text{ V}$	$V_{DS} = 30 \text{ V}$	-	-	1	μA	
		$V_{GS} = 0 \text{ V}$	$V_{DS} = 30 \text{ V}, T_J = 125^\circ\text{C}$	-	-	50		
		$V_{GS} = 0 \text{ V}$	$V_{DS} = 30 \text{ V}, T_J = 175^\circ\text{C}$	-	-	150		
On-State Drain Current ^a	$I_{D(\text{on})}$	$V_{GS} = 10 \text{ V}$	$V_{DS} \geq 5 \text{ V}$	10	-	-	A	
Drain-Source On-State Resistance ^a	$R_{DS(\text{on})}$	$V_{GS} = 10 \text{ V}$	$I_D = 6 \text{ A}$	-	0.028	0.035	Ω	
		$V_{GS} = 4.5 \text{ V}$	$I_D = 4.9 \text{ A}$	-	0.038	0.052		
Forward Transconductance ^b	g_{fs}	$V_{DS} = 15 \text{ V}, I_D = 5 \text{ A}$		-	14	-	S	
Dynamic^b								
Input Capacitance	C_{iss}	$V_{GS} = 0 \text{ V}$	$V_{DS} = 15 \text{ V}, f = 1 \text{ MHz}$	-	514	640	pF	
Output Capacitance	C_{oss}			-	105	130		
Reverse Transfer Capacitance	C_{rss}			-	47	60		
Total Gate Charge ^c	Q_g	$V_{GS} = 10 \text{ V}$	$V_{DS} = 15 \text{ V}, I_D = 5.1 \text{ A}$	-	9.1	16	nC	
Gate-Source Charge ^c	Q_{gs}			-	1.4	2.1		
Gate-Drain Charge ^c	Q_{gd}			-	1.5	2.2		
Turn-On Delay Time ^c	$t_{d(\text{on})}$			-	6	9	ns	
Rise Time ^c	t_r			-	8	12		
Turn-Off Delay Time ^c	$t_{d(\text{off})}$			-	10	15		
Fall Time ^c	t_f			-	7	11		
Source-Drain Diode Ratings and Characteristics^b								
Pulsed Current ^a	I_{SM}			-	-	32	A	
Forward Voltage	V_{SD}	$I_F = 1.7 \text{ A}, V_{GS} = 0 \text{ V}$		-	0.8	1.1	V	

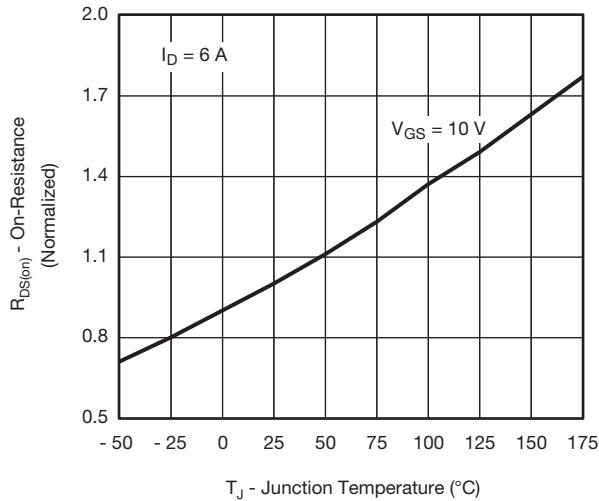
Notes

- a. Pulse test; pulse width $\leq 300 \mu\text{s}$, duty cycle $\leq 2 \%$.
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

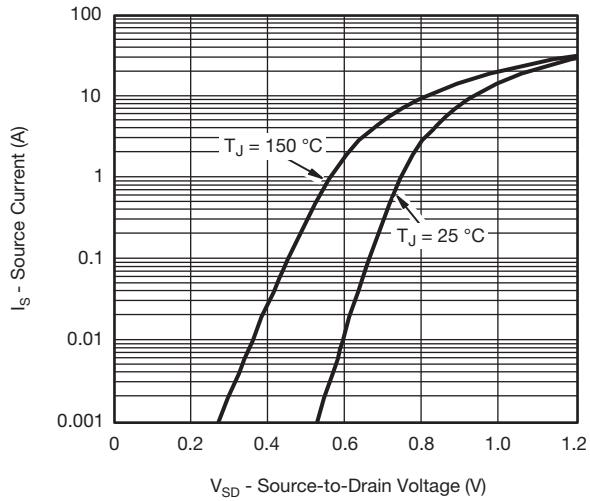
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$, unless otherwise noted)


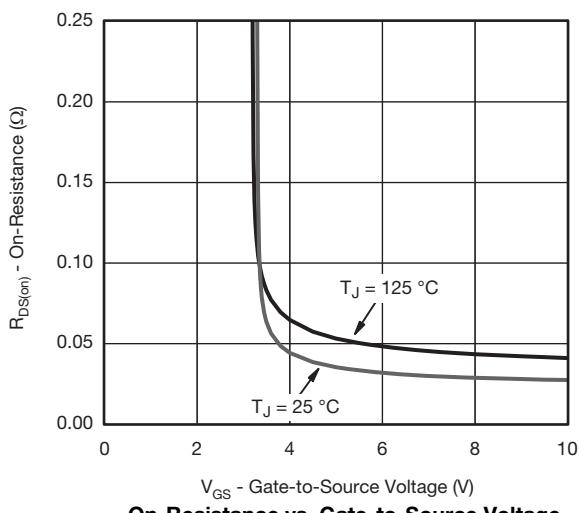
TYPICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$, unless otherwise noted)



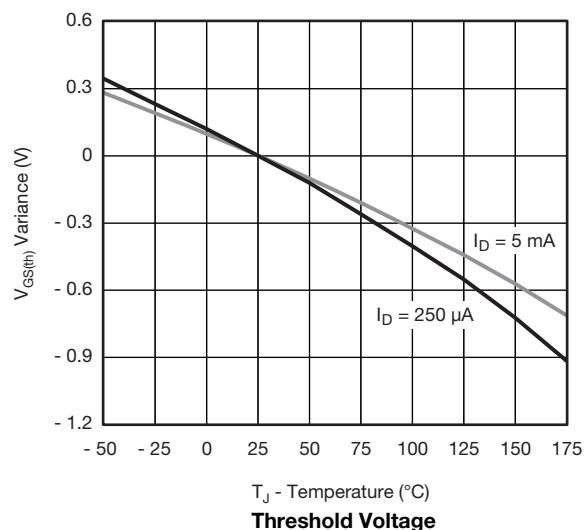
On-Resistance vs. Junction Temperature



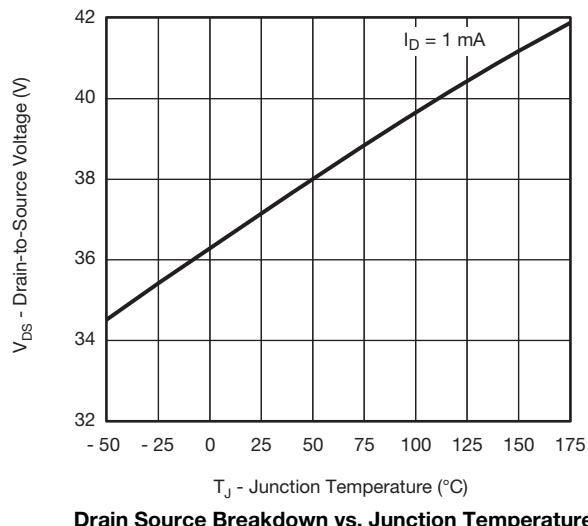
Source-Drain Diode Forward Voltage



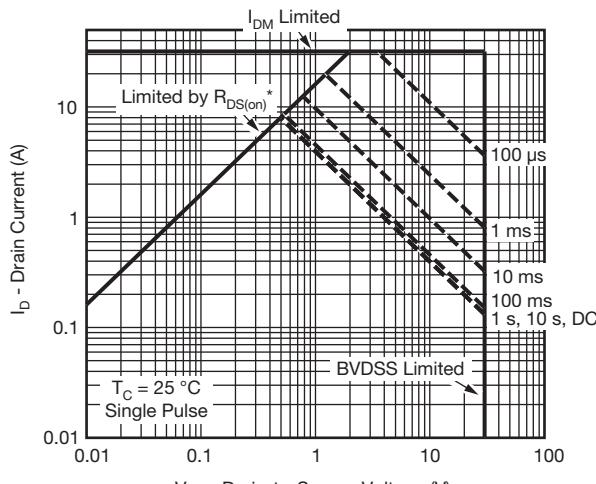
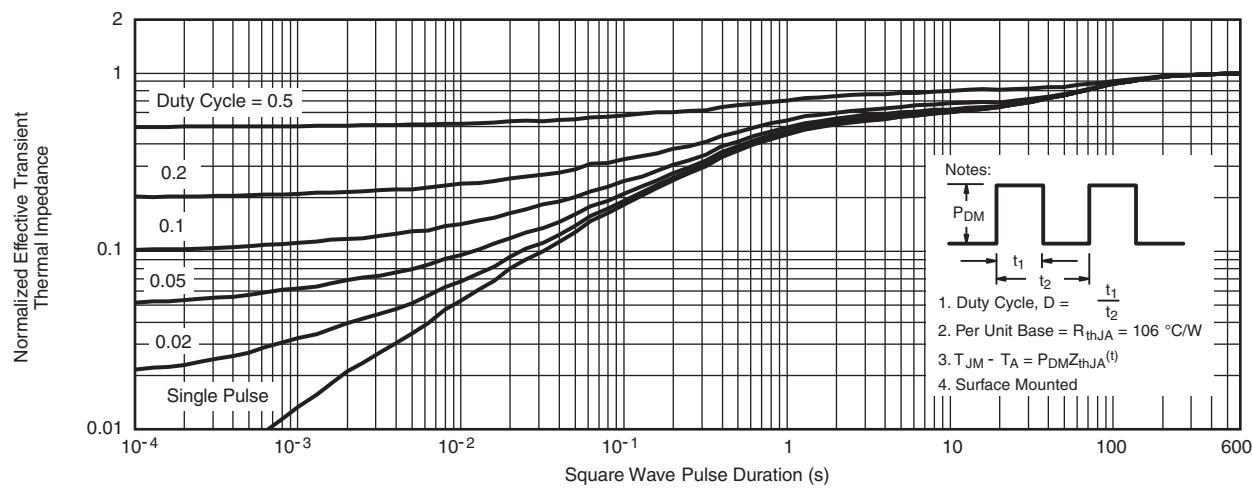
On-Resistance vs. Gate-to-Source Voltage

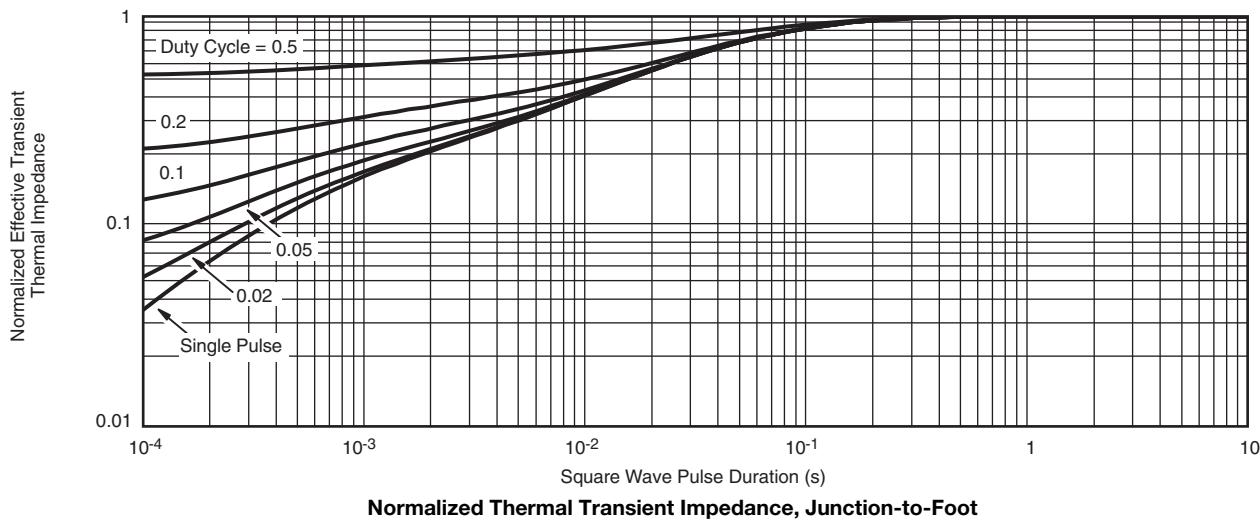


Threshold Voltage



Drain Source Breakdown vs. Junction Temperature

THERMAL RATINGS ($T_A = 25^\circ\text{C}$, unless otherwise noted)

Safe Operating Area


THERMAL RATINGS ($T_A = 25^\circ\text{C}$, unless otherwise noted)**Note**

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction-to-Ambient (25°C)
 - Normalized Transient Thermal Impedance Junction-to-Foot (25°C)
- are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

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