## **Four Channel ESD Array**

This integrated transient voltage suppressor device (TVS) is designed for applications requiring transient overvoltage protection. It is intended for use in sensitive portable equipment and other applications. Its integrated design provides very effective and reliable protection for four (4) separate lines using only one package. These devices are ideal for situations where board space is a premium.

### **Features**

- Unidirectional, Quad ESD Protection
- Ultra-small Flip-Chip Packaging (0.95 mm x 1.33 mm)
- Compliance with IEC61000-4-2 (Level 4) Requirements
- Maximum Leakage Current of 100 nA at 3.3 V
- Pb-Free Package is Available\*

#### **Benefits**

- Protects Four Data Lines from ESD while Reducing Component Count
- Small Package Saves on PCB Real Estate
- Provides Protection for ESD Industry Standards, IEC 61000, HBM and MM
- Low Leakage Capability Minimizes Power Loss in the System

## **Applications**

- ESD Protection for Portable Equipment
- Cell Phones
- MP3 Players
- PDAs

#### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
ESD Discharge IEC61000-4-2,  - Air Discharge  - Contact Discharge  Human Body Model  Machine Model	V <sub>PP</sub>	30 30 16 1.6	kV
Junction Temperature	TJ	150	°C
Operating Ambient Temperature Range	T <sub>A</sub>	-40 to +85	°C
Storage Temperature Range	T <sub>STG</sub>	-55 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

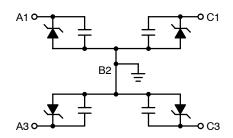
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## ON Semiconductor®

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## **CIRCUIT DESCRIPTION**





## 5-PIN FLIP-CHIP CSP PLASTIC CASE 766AB



**MARKING** 

E = Specific Device Code

M = Date Code

= Pb-Free Package

(Note: Microdot may be in either location)

	TOP VIEW Bumps Down)		OTTOM VIEW (Bumps Up)
	1 2 3		3 2 1
Α		Α	$\bigcirc$
В	(_)	В	
С		С	0 0

## **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
NUP4103FCT1	Flip-Chip	3000/Tape & Reel
NUP4103FCT1G	Flip-Chip (Pb-Free)	3000/Tape & Reel

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

<sup>\*</sup>For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

## NUP4103FC

ESTRICAL CHARACTERISTICS J = 25°C unless otherwise specified)

	1-1					
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Reverse Stand-Off Voltage	$V_{RWM}$	I <sub>RWM</sub> = 10 μA (Note 1)			5.5	V
Breakdown Voltage	$V_{BR}$	I <sub>T</sub> = 1.0 mA (Note 2)	6.0	7.0	8.0	V
Leakage Current	I <sub>R</sub>	V <sub>RM</sub> = 3.3 V per line			100	nA
Junction Capacitance	CJ	V <sub>R</sub> = 2.5 V, f = 1 MHz		30		pF

<sup>1.</sup> TVS devices are normally selected according to the working peak reverse voltage (V<sub>RWM</sub>) which should be equal or greater than the DC or continuous peak operating voltage level.

2. V<sub>BR</sub> is measured at pulse test current I<sub>T</sub>.

## **TYPICAL PERFORMANCE CURVES**

(T<sub>J</sub> = 25°C unless otherwise specified)

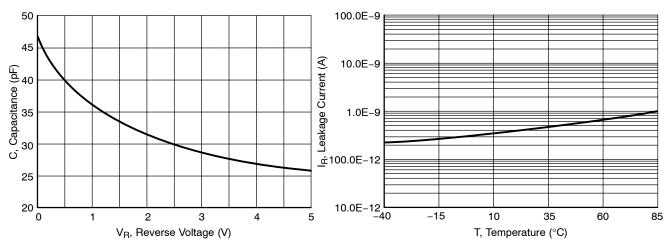


Figure 1. Reverse Voltage vs Junction Capacitance

Figure 2. Reverse Leakage Current vs Junction Temperature

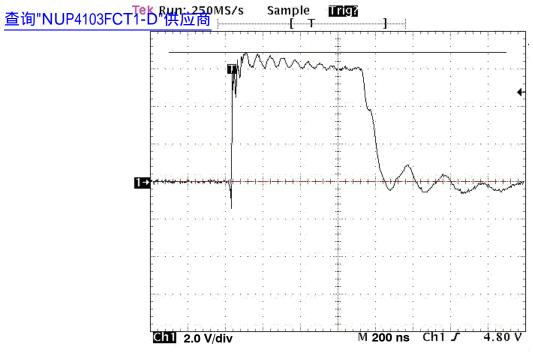


Figure 3. ESD Response for Human Body Model (+8 kV)

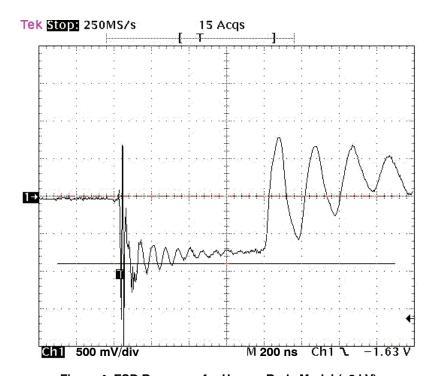


Figure 4. ESD Response for Human Body Model (-8 kV)

# 查询"NUP4103**P(n)ted © lyth)应用** and Recommendations

Parameter	500 μm Pitch 300 μm Solder Ball
PCB Pad Size	250 μm +25 / -0
Pad Shape	Round
Pad Type	NSMD
Solder Mask Opening	350 μm ±25
Solder Stencil Thickness	125 μm
Stencil Aperture	250 x 250 μm sq.
Solder Flux Ratio	50/50
Solder Paste Type	No Clean Type 3 or Finer
Trace Finish	OSP Cu
Trace Width	150 μm Max

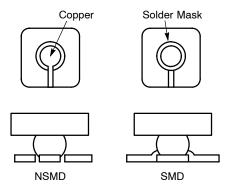


Figure 5. Solder Mask versus Non-Solder Mask Definition

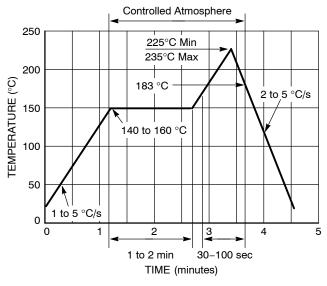


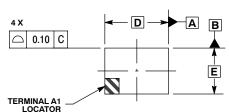
Figure 6. Solder Reflow Profile

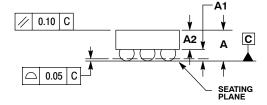
## NUP4103FC

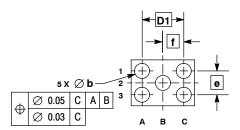
## 查询"NUP4103FCT1-D"供应商

### PACKAGE DIMENSIONS

## 5-PIN FLIP-CHIP CSP CASE 766AB-01 **ISSUE O**







#### NOTES:

- DIMENSIONING AND TOLERANCING PER
- COPLANAITY APPLIES TO SPHERICAL CROWNS OF SOLDER BALLS.

	MILLIMETERS		
DIM	MIN	MAX	
Α		0.680	
A1	0.210	0.270	
A2	0.380	0.430	
D	1.330 BSC		
E	0.960 BSC		
b	0.290	0.340	
e	0.500 BSC		
f	0.433 BSC		
D1	0.866 BSC		

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