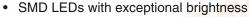


Power Mini SMD LED

FEATURES

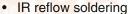






- Compatible with automatic placement
- equipment





- Available in 8 mm tape
- Low profile package
- · Non-diffused lens: excellent for coupling to light pipes and backlighting
- Low power consumption
- · Luminous intensity ratio in one packing unit $I_{Vmax}/I_{Vmin.} \le 2.0$, optional ≤ 1.6
- Lead (Pb)-free device
- ESD withstand voltage: up to 2 kV according to JESD22-A114-B
- Preconditioning: according to JEDEC level 2a



DESCRIPTION

The new MiniLED series have been designed in a small white SMT package. The feature of the device is the very small package 2.3 mm x 1.3 mm x 1.4 mm. The MiniLED is an obvious solution for small-scale, high-power products that are expected to work reliability in an arduous environment. This is often the case in automotive and industrial application.

PRODUCT GROUP AND PACKAGE DATA

 Product group: LED Package: SMD MiniLED

Product series: power

Angle of half intensity: ± 60°

APPLICATIONS

- · Automotive: backlighting in dashboards and switches
- · Telecommunication: indicator and backlighting in telephone and fax
- Indicator and backlight for audio and video equipment
- Indicator and backlight in office equipment
- Flat backlight for LCDs, switches and symbols

PARTS TABLE		
PART	COLOR, LUMINOUS INTENSITY	TECHNOLOGY
VLMP232M2N2-GS08	Pure green, I _V = (22.4 to 45) mcd	AllnGaP on GaAs
VLMP232N1P1-GS08	Pure green, I _V = (28 to 56) mcd	AllnGaP on GaAs
VLMP232M2P1-GS08	Pure green, I _V = (22.4 to 56) mcd	AllnGaP on GaAs



ABSOLUTE MAXIMUM RATINGS 1) VLMP232				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Reverse voltage ²⁾		V _R	5	V
DC Forward current	T _{amb} ≤ 80 °C	I _F	40	mA
Surge forward current	t _p ≤ 10 μs	I _{FSM}	0.1	Α
Power dissipation		P _V	110	mW
Junction temperature		T _j	125	°C
Operating temperature range		T _{amb}	- 40 to + 100	°C
Storage temperature range		T _{stg}	- 40 to + 100	°C
Thermal resistance junction/ ambient	mounted on PC board (pad size > 5 mm²)	R _{thJA}	580	K/W

Note:

²⁾ Driving the LED in reverse direction is suitable for a short term application

PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
		VLMP232M2N2	I _V	22.4		45	mcd
Luminous intensity 2)	$I_F = 30 \text{ mA}$	VLMP232N1P1	I _V	28		56	mcd
		VLMP232M2P1	I _V	22.4		56	mcd
Dominant wavelength	I _F = 30 mA		λ_{d}	555	558	565	nm
Peak wavelength	I _F = 30 mA		λ_{p}		555		nm
Angle of half intensity	I _F = 30 mA		φ		± 60		deg
Forward voltage	I _F = 30 mA		V_{F}		2.2	2.6	V
Reverse voltage	I _R = 10 μA		V_{R}	5			V
Junction capacitance	V _R = 0, f = 1 MHz		C _j		15		pF

Note:

LUMINOUS INTENSITY CLASSIFICATION				
GROUP	LIGHT INTENSITY (mcd)			
STANDARD	OPTIONAL MIN. MAX.			
М	1	18	22.4	
	2	22.4	28	
N	1	28	35.5	
	2	35.5	45	
Р	1	45	56	
	2	56	71	

Note:

Luminous intensity is tested at a current pulse duration of 25 ms and an accuracy of ± 11 %.

The above type numbers represent the order groups which include only a few brightness groups. Only one group will be shipped on each reel (there will be no mixing of two groups on each reel).

In order to ensure availability, single brightness groups will not be

In a similar manner for colors where wavelength groups are measured and binned, single wavelength groups will be shipped on

In order to ensure availability, single wavelength groups will not be orderable.

COLOR CLASSIFICATION			
	DOMINANT WAVELENGTH (nm)		
GROUP	PURE GREEN		
	MIN.	MAX.	
0	555	559	
1	558	561	
2	560	563	
3	562	565	

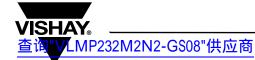
Note:

Wavelengths are tested at a current pulse duration of 25 ms and an accuracy of ± 1 nm.

CROSSING TABLE		
VISHAY	OSRAM	
VLMP232M2P1	LPM675-M2P1	

¹⁾ T_{amb} = 25 °C, unless otherwise specified

 $^{^{1)}}$ T_{amb} = 25 °C, unless otherwise specified $^{2)}$ In one packing unit $I_{Vmax}/I_{Vmin.} \le 2.0$



TYPICAL CHARACTERISTICS

T_{amb} = 25 °C, unless otherwise specified

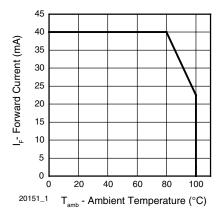


Figure 1. Forward Current vs. Ambient Temperature

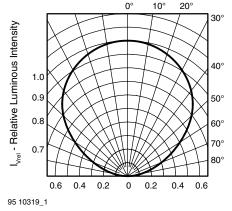


Figure 2. Rel. Luminous Intensity vs. Angular Displacement

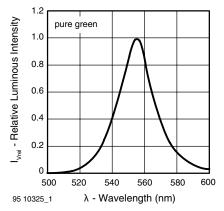


Figure 3. Relative Intensity vs. Wavelength

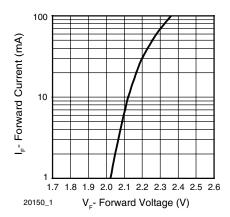


Figure 4. Forward Current vs. Forward Voltage

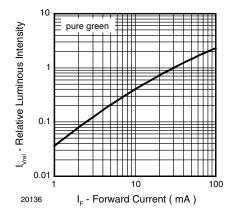


Figure 5. Relative Luminous Intensity vs.
Forward Current

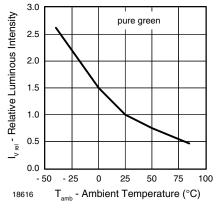


Figure 6. Rel. Luminous Intensity vs. Ambient Temperature

Vishay/Semaconchuctores"供应商



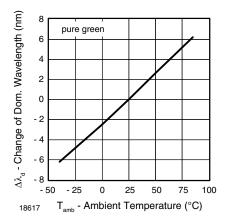


Figure 7. Change of Dominant Wavelength vs.

Ambient Temperature

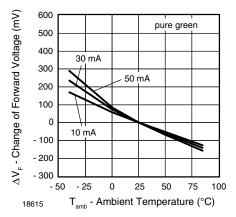
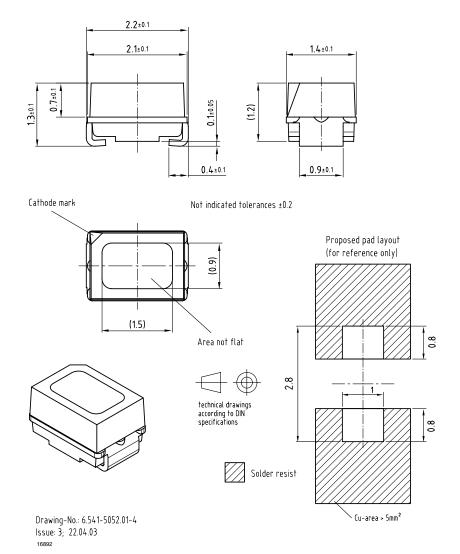
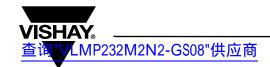


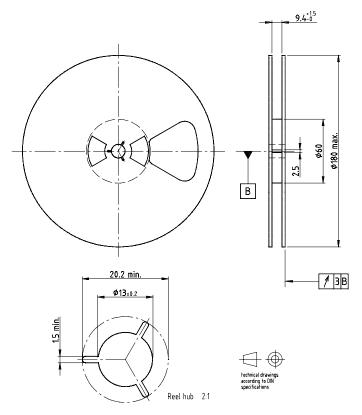
Figure 8. Change of Forward Voltage vs.
Ambient Temperature

PACKAGE DIMENSIONS in millimeters



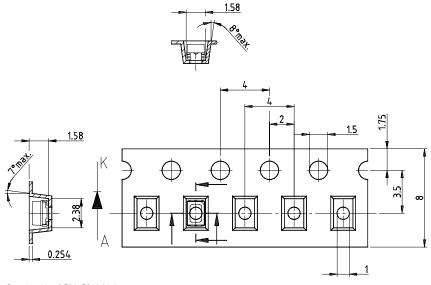


REEL DIMENSIONS in millimeters



Drawing-No.: 9.800-5051.V5-4 Issue: 1; 25.07.02

TAPE DIMENSIONS in millimeters

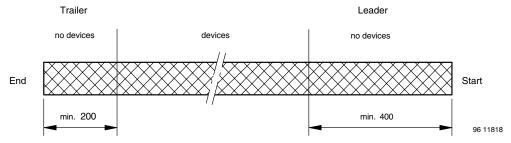


Drawing-No.: 9.700-5266.01-4 Issue: 1; 05.06.02

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LEADER AND TRAILER Dimensions in millimeters



GS08 = 3000 pcs

COVER TAPE PEEL STRENGTH

According to DIN EN 60286-3 0.1 to 1.3 N 300 ± 10 mm/min 165° to 180° peel angle

LABEL

Standard bar code labels for finished goods

The standard bar code labels are product labels and used for identification of goods. The finished goods are packed in final packing area. The standard packing units are labeled with standard bar code labels before transported as finished goods to warehouses. The labels are on each packing unit and contain Vishay Semiconductor GmbH specific data.

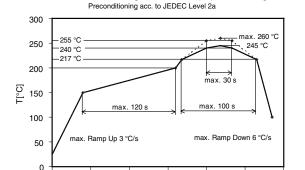
PLAIN WRITING	ABBREVIATION	LENGTH
Item-description	-	18
Item-number	INO	8
Selection-code	SEL	3
LOT-/serial-number	BATCH	10
Data-code	COD	3 (YWW)
Plant-code	PTC	2
Quantity	QTY	8
Accepted by:	ACC	-
Packed by:	PCK	-
Mixed code indicator	MIXED CODE	-
Origin	xxxxxxx ⁺	Company logo
LONG BAR CODE TOP	TYPE	LENGTH
Item-number	N	8
Plant-code	N	2
Sequence-number	X	3
Quantity	N	8
Total length	-	21
SHORT BAR CODE BOTTOM	TYPE	LENGTH
Selection-code	X	3
Data-code	N	3
Batch-number	X	10
Filter	-	1
Total length	-	17

Vishay Semiconductors

SOLDERING PROFILE

0

19470-3



IR Reflow Soldering Profile for lead (Pb)-free soldering

Figure 9. Vishay Lead (Pb)-free Reflow Soldering Profile (acc. to J-STD-020C)

150

t [s]

200

100

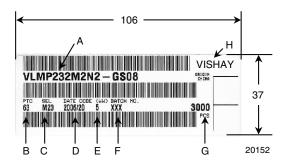
250

max. 2 cycles allowed

300

BAR CODE PRODUCT LABEL EXAMPLE:

50



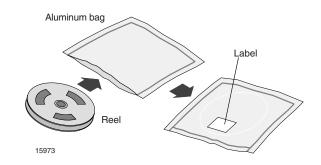
- A) Type of component
- B) Manufacturing plant
- C) SEL selection code (bin):

e.g.: M2 = code for luminous intensity group 3 = code for color group

- D) Date code year/week
- E) Day code (e.g. 5: Friday)
- F) Batch no.
- G) Total quantity
- H) Company code

DRY PACKING

The reel is packed in an anti-humidity bag to protect the devices from absorbing moisture during transportation and storage.



FINAL PACKING

The sealed reel is packed into a cardboard box. A secondary cardboard box is used for shipping purposes.

Violing WS WIP 1820 Mather 1898 "供应商

RECOMMENDED METHOD OF STORAGE

Dry box storage is recommended as soon as the aluminum bag has been opened to prevent moisture absorption. The following conditions should be observed, if dry boxes are not available:

- Storage temperature 10 °C to 30 °C
- Storage humidity ≤ 60 % RH max.

After more than 672 h under these conditions moisture content will be too high for reflow soldering.

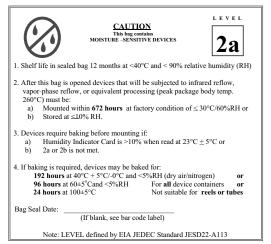
In case of moisture absorption, the devices will recover to the former condition by drying under the following condition:

192 h at $40 \,^{\circ}\text{C} + 5 \,^{\circ}\text{C/-} \, 0 \,^{\circ}\text{C}$ and $< 5 \,^{\circ}\text{KH}$ (dry air/nitrogen) or

96 h at 60 $^{\circ}$ C + 5 $^{\circ}$ C and < 5 $^{\circ}$ RH for all device containers or

24 h at 100 °C + 5 °C not suitable for reel or tubes.

An EIA JEDEC standard JESD22-A112 level 2a label is included on all dry bags.



Example of JESD22-A112 level 2a label



ESD PRECAUTION

Proper storage and handling procedures should be followed to prevent ESD damage to the devices especially when they are removed from the antistatic shielding bag. Electro-static sensitive devices warning labels are on the packaging.

VISHAY SEMICONDUCTORS STANDARD BAR CODE LABELS

The Vishay Semiconductors standard bar code labels are printed at final packing areas. The labels are on each packing unit and contain Vishay Semiconductors specific data.



Vishay Semiconductors

OZONE DEPLETING SUBSTANCES POLICY STATEMENT

It is the policy of Vishay Semiconductor GmbH to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively.
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA.
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design and may do so without further notice.

Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use Vishay Semiconductors products for any unintended or unauthorized application, the buyer shall indemnify Vishay Semiconductors against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

Vishay Semiconductor GmbH, P.O.B. 3535, D-74025 Heilbronn, Germany



Vishay

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