

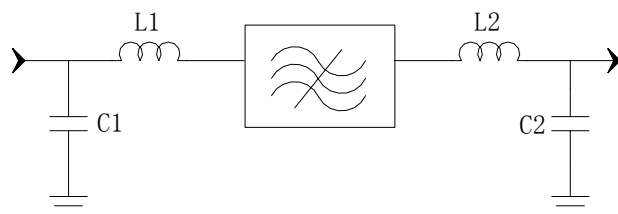
Specifications

Parameter	Unit	Minimum	Typical	Maximum
Center Frequency	MHz	139.8	140	140.2
Insertion Loss	dB	-	11.1	13
1 dB Bandwidth	MHz	5	5.1	-
3 dB Bandwidth	MHz	-	5.98	-
35 dB Bandwidth	MHz	-	8.9	9.2
Passband Variation	dB	-	0.4	1
Group delay Variation(80% of 3dB)	nsec	-	70	100
Ultimate Rejection	dB	40	45	-
Absolute Delay	usec	-	0.869	-
Material Temperature coefficient	KHz/°C	-2.52		
Operating temperature	°C	-35~75		
Subatrate Material		112LT		
Ambient Temperature	°C	25		
Package Size	SMP-53 (13.3 x 6.5 mm Nominal Footprint)			

Notes:

1. All specifications are based on the test circuit shown
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. This is the optimum impedance in order to achieve the performance show

Matching Configuration



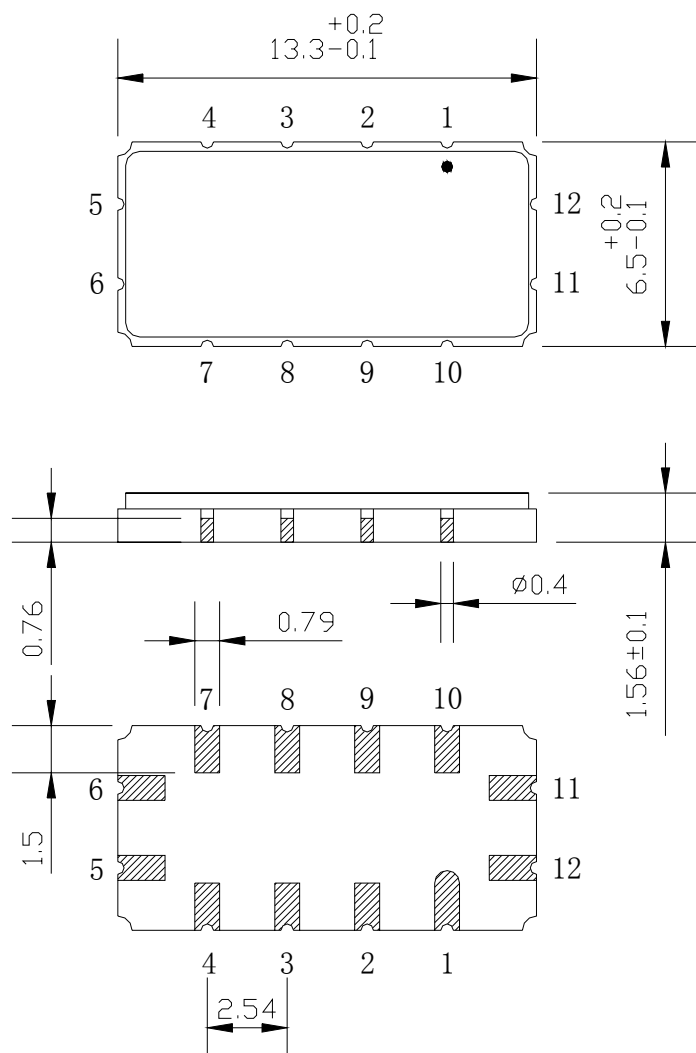
L1=L2=82nH
C1=33pF C2=22pF
Source /Load Impedance=50 ohm
 Notes - Component values may change depending
 on board layout.



SIPAT Co., Ltd.
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 Chongqing, China, 400060

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Package Dimension

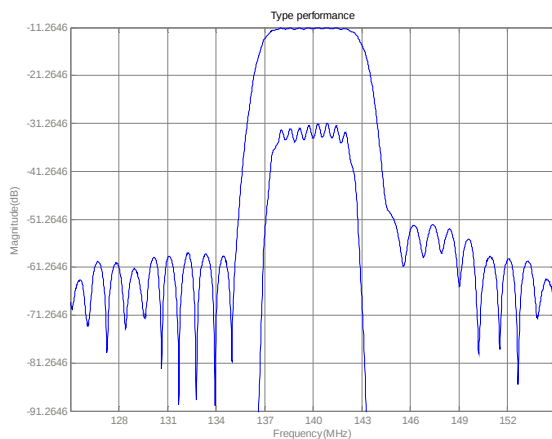


Input:11
Output:5

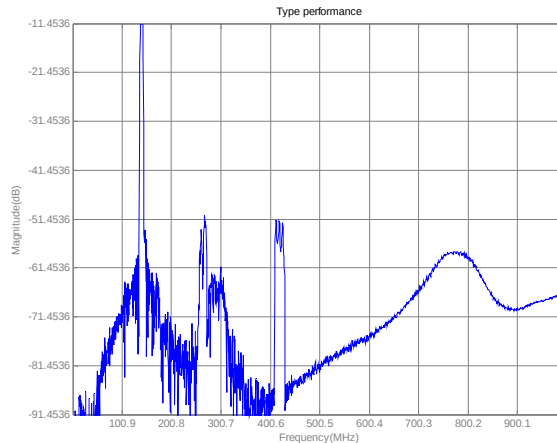
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Typical Performance

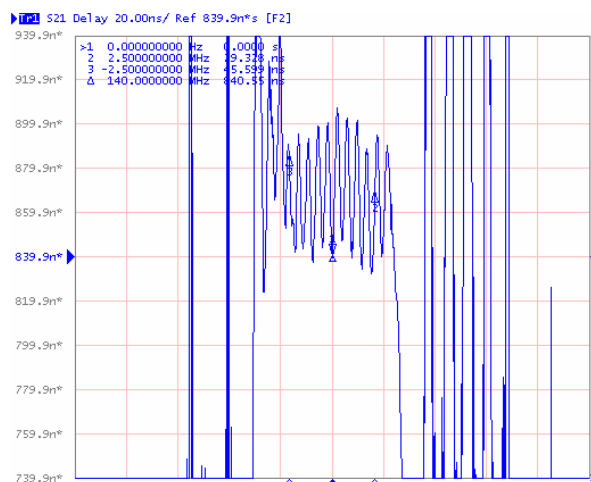
Frequency Respond



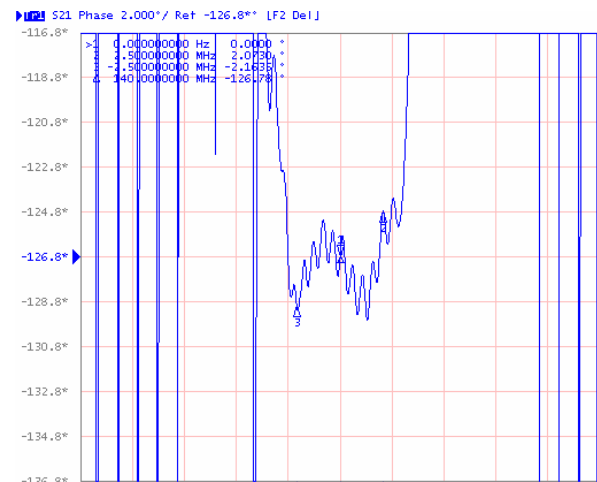
Wideband Respond



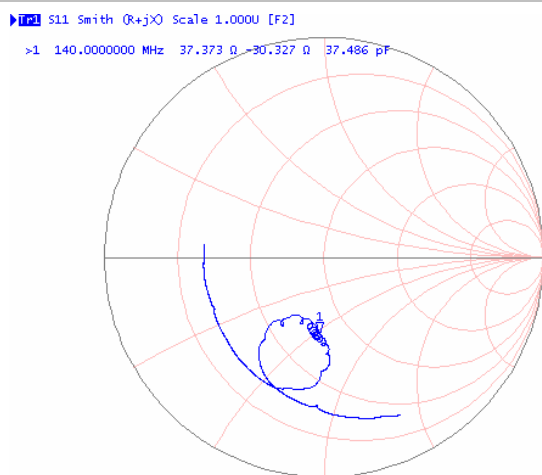
$F_0 \pm 2.5\text{MHz}$ Group delay Variation



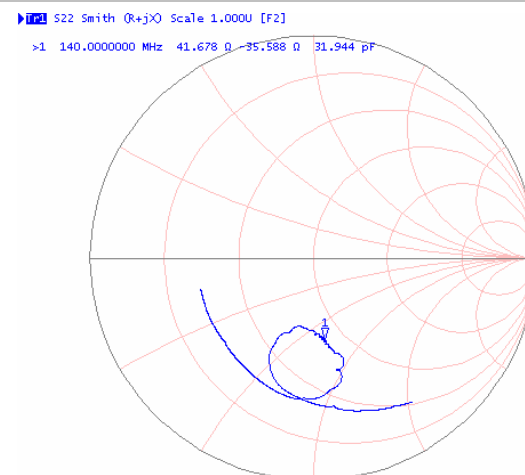
$F_0 \pm 2.5\text{MHz}$ Phase Linearity



Simth Chart S11



Simth Chart S22



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Marking

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