



CSD75301W1015

SLPS212B-AUGUST 2009-REVISED NOVEMBER 2009

P-Channel NexFET™ Power MOSFET

Check for Samples: CSD75301W1015

FEATURES

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- **Dual P-Ch MOSFETs**
- **Common Source Configuration**
- Small Footprint 1mm × 1.5mm
- Low Profile 0.62mm
- Ultra Low Qg and Qgd
- Pb Free / RoHS Compliant WWW.DZSC.COM
- **Halogen Free**

APPLICATIONS

- **Battery Management**
- Load Switch
- **Battery Protection**

DESCRIPTION

The device has been designed to deliver the lowest on resistance and gate charge in the smallest outline possible with excellent thermal characteristics in an ultra low profile.

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G1

PRODUCT SUMMARY

(Per MOSFET unless otherwise stated)						
V _{DS}	0.00	V				
Qg	Gate Charge Total (4.5V) 1.5					
Q _{gd}	Gate Charge Gate to Drain	0.3	nC			
		$V_{GS} = -1.8V$	150	mΩ		
R _{DS(on)}	Drain to Source On Resistance	$V_{GS} = -2.5V$	105	mΩ		
		V _{GS} = -4.5V 80		mΩ		
V _{GS(th)}	Voltage threshold	-0.7		V		

ORDERING INFORMATION

Device	Package	Media	Qty	Ship	
CSD75301W1015	1 × 1.5 Wafer Level Package	7-inch reel	3000	Tape and Reel	

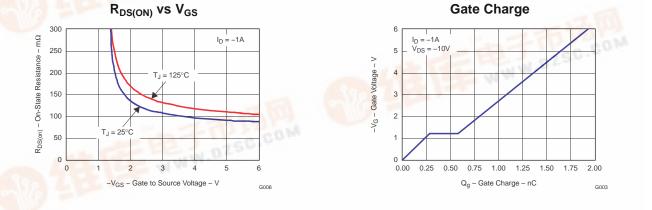
ABSOLUTE MAXIMUM RATINGS

$T_A = 2$	5°C unless otherwise stated	VALUE	UNIT
V _{DS}	Drain to Source Voltage	-20	V
V _{GS}	Gate to Source Voltage	±8	V
I _D	Continuous Drain Current, $T_C = 25^{\circ}C^{(1)}$ (2)	-1.2	А
I _{DM}	Pulsed Drain Current, $T_A = 25^{\circ}C^{(1)}$ ⁽²⁾ ⁽³⁾	-17.5	А
PD	Power Dissipation ^{(1) (2)}	0.8	W
T _J , T _{STG}	Operating Junction and Storage Temperature Range	-55 to 150	°C

(1) Per device, both devices in conduction.

(2) $R_{\theta,JA} = 74^{\circ}C/W$ on $1in^2$ Cu (2 oz.) on 0.060" thick FR4 PCB.

(3) Pulse width ≤300µs, duty cycle ≤2%



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PRODUCTION DATA information is current as of publication date. Products conform to specifications per the terms of the Texas Instruments standard warranty. Production processing does not processarily include testing of all parameters.

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ELECTRICAL CHARACTERISTICS

(T_A = 25°C unless otherwise stated) (Per MOSFET unless otherwise stated)

	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Static Cl	naracteristics					
BV _{DSS}	Drain to Source Voltage	$V_{GS} = 0V, I_D = -250\mu A$	-20			V
I _{DSS}	Drain to Source Leakage Current	$V_{GS} = 0V, V_{DS} = -16V$			-1	μA
I _{GSS}	Gate to Source Leakage Current	$V_{DS} = 0V, V_{GS} = -8V$			-100	nA
V _{GS(th)}	Gate to Source Threshold Voltage	$V_{DS} = V_{GS}, I_D = -250 \mu A$	-0.4	-0.7	-1.0	V
. ,		$V_{GS} = -1.8V, I_D = -1A$		150	190	mΩ
R _{DS(on)}	Drain to Source On Resistance	$V_{GS} = -2.5V, I_D = -1A$		105	135	mΩ
		$V_{GS} = -4.5V, I_D = -1A$		80	100	mΩ
9 _{fs}	Transconductance	$V_{DS} = -10V, I_D = -1A$		5.2		S
Dynamic	Characteristics				Ļ	
CISS	Input Capacitance			150	195	pF
C _{OSS}	Output Capacitance	$V_{GS} = 0V, V_{DS} = -10V, f = 1MHz$		67	87	pF
C _{RSS}	Reverse Transfer Capacitance			24	31	pF
Qg	Gate Charge Total (-4.5V)			1.5	2.1	nC
Q _{gd}	Gate Charge Gate to Drain			0.3		nC
Q _{gs}	Gate Charge Gate to Source	$V_{DS} = -10V, I_D = -1A$		0.28		nC
Q _{g(th)}	Gate Charge at Vth			0.12		nC
Q _{OSS}	Output Charge	$V_{DS} = -9.5V, V_{GS} = 0V$		1.1		nC
t _{d(on)}	Turn On Delay Time			3		ns
t _r	Rise Time	$V_{DS} = -10V, V_{GS} = -4.5V, I_{D} = -1A$		1.7		ns
t _{d(off)}	Turn Off Delay Time	$R_{G} = 30\Omega$		38		ns
t _f	Fall Time			16		ns
Diode Cl	haracteristics	+				
V _{SD}	Diode Forward Voltage	$I_{\rm S}$ = -1A, $V_{\rm GS}$ = 0V		-0.81	-1	V
Q _{rr}	Reverse Recovery Charge	$V_{dd} = -9.5V, I_F = -1A, di/dt = 200A/\mu s$		2		nC
t _{rr}	Reverse Recovery Time	$V_{dd} = -9.5V$, $I_F = -1A$, di/dt = 200A/µs		7.5		ns

THERMAL CHARACTERISTICS

 $(T_A = 25^{\circ}C \text{ unless otherwise stated})$

	PARAMETER	MIN	TYP	MAX	UNIT
$R_{\theta JC}$	Thermal Resistance Junction to Ambient ⁽¹⁾ ⁽²⁾			136	°C/W
R $_{\theta JA}$	Thermal Resistance Junction to Ambient ^{(2) (3)}			93	°C/W

(1) Device mounted on FR4 material with Minimum Cu mounting area.

Measured with both devices biased in a parallel condition. Device mounted on FR4 material with 1in² of 2 oz Cu. (2) (3)

STRUMENTS

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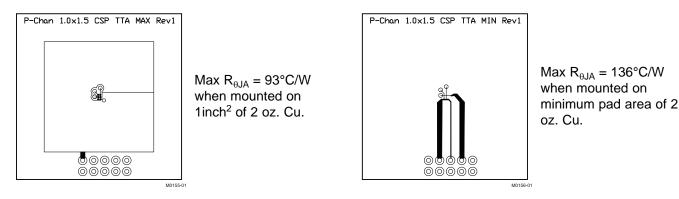
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TYPICAL MOSFET CHARACTERISTICS

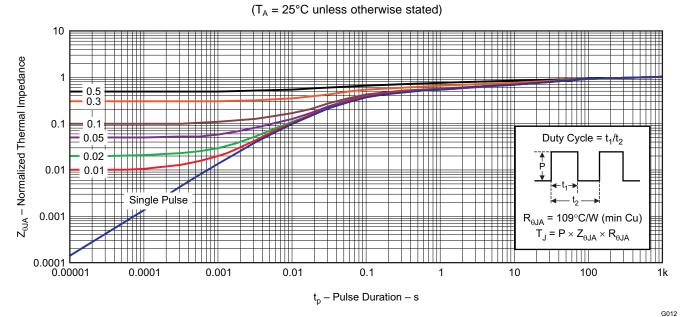


Figure 1. Transient Thermal Impedance

6

5

4

3

2

1

0 0.00

-V_G - Gate Voltage - V

 $I_D = -1A$

0.25

0.50

0.75

 $V_{DS} =$

-10V

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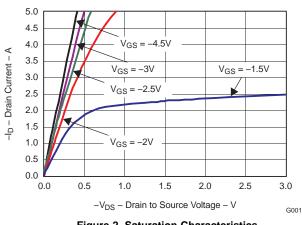
STRUMENTS

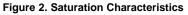
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TYPICAL MOSFET CHARACTERISTICS (continued)

 $(T_A = 25^{\circ}C \text{ unless otherwise stated})$





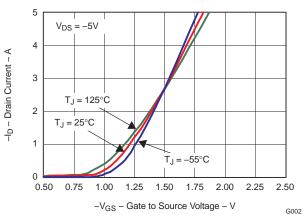
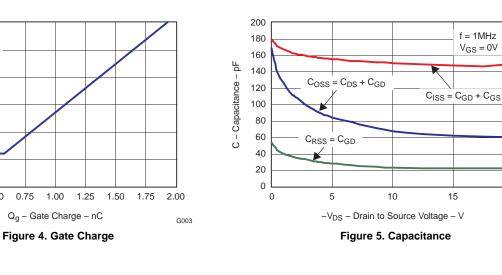
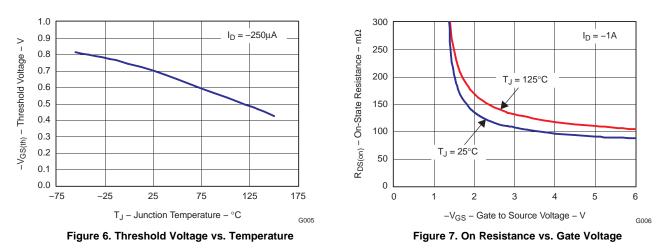


Figure 3. Transfer Characteristics







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TYPICAL MOSFET CHARACTERISTICS (continued)

 $(T_A = 25^{\circ}C \text{ unless otherwise stated})$

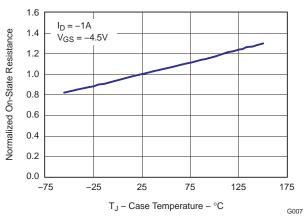


Figure 8. On Resistance vs. Temperature

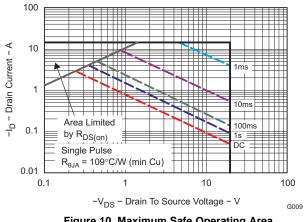


Figure 10. Maximum Safe Operating Area

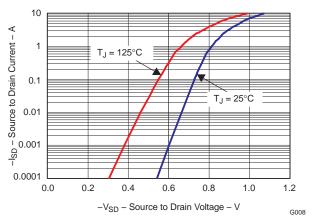


Figure 9. Typical Diode Forward Voltage

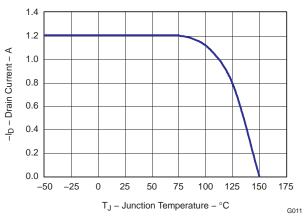
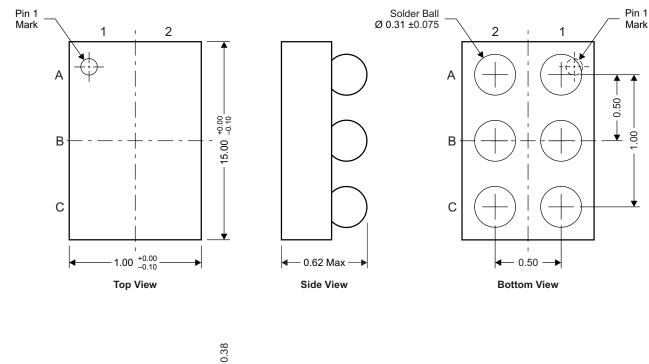


Figure 11. Maximum Drain Current vs. Temperature

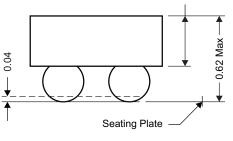
TEXAS INSTRUMENTS

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MECHANICAL DATA



CSD75301W1015 Package Dimensions



Front View

M0157-01

NOTE: All dimensions are in mm (unless othersse specified)

Pinout						
POSITION	DESIGNATION					
B1, B2	Source					
C1	Gate1					
C2	Drain1					
A2	Gate2					
A1	Drain2					



2° Max

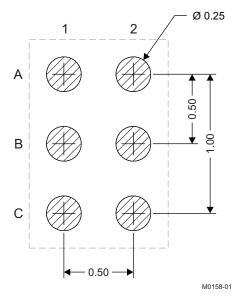
M0159-01

1.65 ±0.05 -

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Land Pattern Recommendation



NOTE: All dimensions are in mm (unless othersse specified)

Ø 1.50 ±0.10 ◀ 4.00 ±0.10 → 2.00 ±0.05 .75 ±0.10 +0.30 -0.10 8.00 • 0 Ð Ð \oplus \oplus 3.50 ±0.05 Ø 0.60 +0.05 -0.10 ← 4.00 ±0.10 → 0.86 ±0.05 0.254 ±0.02 2° Max 1.19 ±0.05

Tape and Reel Information

NOTE: All dimensions are in mm (unless othersse specified)

TEXAS INSTRUMENTS

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SLAS242B	AUGI	15T-2009	REXISED	NOVEMBER 2009
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REVISION HISTORY

Changes from Original (August 2009) to Revision A				
Changed location of the Pin 1 indicator dot in the pin out illustration.	1			
Changes from Revision A (November 2009) to Revision B	Page			
Deleted the Package Marking Information section				



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PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	Eco Plan ⁽²⁾	Lead/ Ball Finish	MSL Pe
CSD75301W1015	ACTIVE	DSBGA	YZC	6	3000	Green (RoHS & no Sb/Br)	Call TI	Level-1-260

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new **PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

⁽²⁾ Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www. information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for **Pb-Free (RoHS Exempt):** This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retard in homogeneous material)

⁽³⁾ MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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