

Preferred Devices

Surface Mount Schottky Power Rectifier

Plastic SOD-123 Package

The Schottky Power Rectifier employs the Schottky Barrier principle with a barrier metal that produces optimal forward voltage drop—reverse current tradeoff. Ideally suited for low voltage, high frequency rectification, or as free wheeling and polarity protection diodes in surface mount applications where compact size and weight are critical to the system. This package provides an alternative to the leadless 34 MELF style package. These state—of—the—art devices have the following features:

Features

- Guardring for Stress Protection
- Very Low Forward Voltage (0.38 V Max @ 0.5 A, 25°C)
- 125°C Operating Junction Temperature
- Epoxy Meets UL 94 V-0 @ 0.125 in
- Package Designed for Optimal Automated Board Assembly
- Pb-Free Packages are Available

Mechanical Characteristics

- Reel Options: MBR0520LT1 = 3,000 per 7'' reel/8 mm tapeMBR0520LT3 = 10,000 per 13'' reel/8 mm tape
- Polarity Designator: Cathode Band
- Weight: 11.7 mg (approximately)
- Case: Epoxy, Molded
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead and Mounting Surface Temperature for Soldering Purposes: 260°C Max. for 10 Seconds



ON Semiconductor®

http://onsemi.com

SCHOTTKY BARRIER RECTIFIER 0.5 AMPERES, 20 VOLTS



SOD-123 CASE 425 STYLE 1

MARKING DIAGRAM



B2 = Device Code
M = Date Code
Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping [†]
MBR0520LT1	SOD-123	3000/Tape & Reel
MBR0520LT1G	SOD-123 (Pb-Free)	3000/Tape & Reel
MBR0520LT3	SOD-123	10,000/Tape & Reel
MBR0520LT3G	SOD-123 (Pb-Free)	10,000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

Preferred devices are recommended choices for future use and best overall value.

MBR0520LT1, MBR0520LT3

曾柳柳柳RATING\$1-D"供应商

Rating	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	20	V
Average Rectified Forward Current (Rated V_R , $T_L = 90^{\circ}C$)	I _{F(AV)}	0.5	А
Non-Repetitive Peak Surge Current (Surge Applied at Rated Load Conditions Halfwave, Single Phase, 60 Hz)	I _{FSM}	5.5	А
Storage Temperature Range	T _{stg}	-65 to +150	°C
Operating Junction Temperature	T_J	-65 to +125	°C
Voltage Rate of Change (Rated V _R)	dv/dt	1000	V/μs
ESD Ratings: Machine Model = C Human Body Model = 3B		> 400 > 8000	V

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

THERMAL CHARACTERISTICS

Rating	Symbol	Value	Unit
Thermal Resistance; Junction-to-Ambient (Note 1)	$R_{ heta JA}$	206	°C/W
Thermal Resistance; Junction-to-Lead	$R_{ heta JL}$	150	°C/W

ELECTRICAL CHARACTERISTICS

Maximum Instantaneous Forward Voltage (Note 2)	VF	T _J = 25°C	T _J = 100°C	V
$(i_F = 0.1 \text{ Amps})$ $(i_F = 0.5 \text{ Amps})$		0.300 0.385	0.220 0.330	
Maximum Instantaneous Reverse Current (Note 2)	I _R	T _J = 25°C	T _J = 100°C	mA
(V _R = 10 V) (Rated DC Voltage = 20 V)		75 μΑ 250 μΑ	5 mA 8 mA	

 ^{1. 1} inch square pad size (1 x 0.5 inch for each lead) on FR4 board.
 2. Pulse Test: Pulse Width = 300 μs, Duty Cycle ≤ 2%.

MBR0520LT1, MBR0520LT3

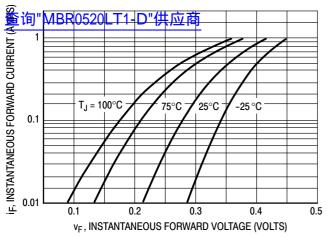


Figure 1. Typical Forward Voltage

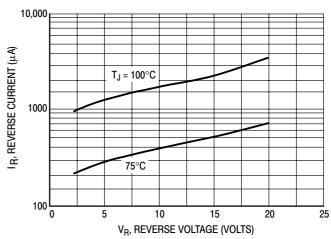


Figure 2. Typical Reverse Current

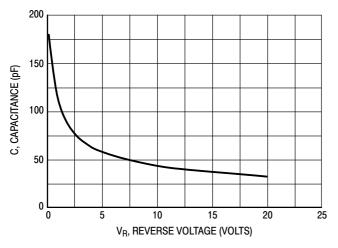


Figure 3. Typical Capacitance

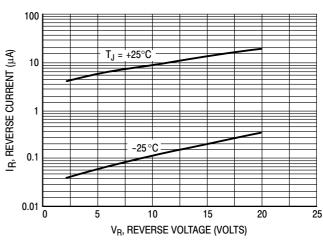


Figure 4. Typical Reverse Current

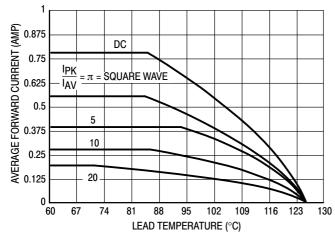


Figure 5. Current Derating (Lead)

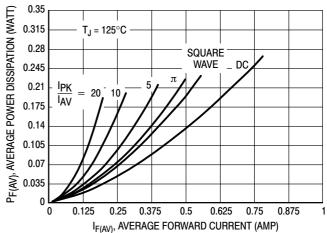


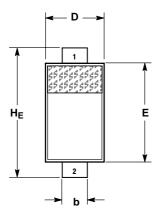
Figure 6. Power Dissipation

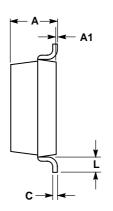
MBR0520LT1, MBR0520LT3

查询"MBR0520LT1-D"供应商

PACKAGE DIMENSIONS

SOD-123 CASE 425-04 **ISSUE E**



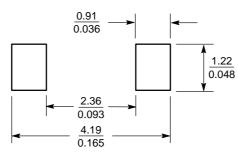


- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14 5M 1982
- 2. CONTROLLING DIMENSION: INCH.

	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.94	1.17	1.35	0.037	0.046	0.053
A1	0.00	0.05	0.10	0.000	0.002	0.004
b	0.51	0.61	0.71	0.020	0.024	0.028
C			0.15			0.006
D	1.40	1.60	1.80	0.055	0.063	0.071
E	2.54	2.69	2.84	0.100	0.106	0.112
HE	3.56	3.68	3.86	0.140	0.145	0.152
L	0.25			0.010		

STYLE 1: PIN 1. CATHODE 2. ANODE

SOLDERING FOOTPRINT*



SCALE 10:1

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ON Semiconductor and un are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada

Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com

Phone: 81-3-5773-3850

N. American Technical Support: 800-282-9855 Toll Free USA/Canada

Europe, Middle East and Africa Technical Support: Phone: 421 33 790 2910

Japan Customer Focus Center

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative