

520 MHz Dual Modulus Prescaler

The MC12018 is a dual modulus prescaler which divides by 128 and 129. An internal regulator is provided to allow this device to be used over a wide range of power supply voltages. The devices may be operated by applying a supply voltage of 5.0 Vdc $\pm 10\%$ at Pin 7, or by applying an unregulated voltage source from 5.5 Vdc to 9.5 Vdc to Pin 8.

- 520 MHz Toggle Frequency
- Low–Power 8.0 mA Typical
- Control Input Is Compatible With Standard CMOS and TTL
- Supply Voltage 4.5 V to 9.5 V
- $\bullet~$ On–Chip 10 $k\Omega$ Resistor from Positive Edge to Ground

SIMPLIFIED BLOCK DIAGRAM Control Input 2 $0.001\,\mu\text{F}$ Signal 3 POS Edge ÷N / N+1 Gnd NEG $0.001 \, \mu F$ 'Edge V_{reg} 4 7 $0.1 \, \mu F$ Gnd VCC Voltage Regulator 8 0.1 μF 1. V_{reg} at Pin 7 is not guaranteed to be between 4.5 and 5.5 V when V_{CC} is being applied to Pin 8 2. Pin 7 is not to be used as a source of regulated output voltage 3. 10 $k\Omega$ pulldown recommended with negative edge output (Pin 2)

MC12018

MECL PLL COMPONENTS ÷128/129 DUAL MODULUS PRESCALER

SEMICONDUCTOR TECHNICAL DATA

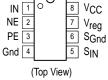


D SUFFIXPLASTIC PACKAGE
CASE 751
(SO-8)



P SUFFIX PLASTIC PACKAGE CASE 626

PIN CONNECTIONS



ORDERING INFORMATION

Device	Operating Temperature Range	Package
MC12018D	$T_{\Delta} = -40^{\circ} \text{ to } +85^{\circ}\text{C}$	SO-8
MC12018P	1A = -40 to +65 C	Plastic

MC12018

MAXIMUM RATINGS

查询"MC12018" 快炒 商	Symbol	Value	Unit
Regulated Voltage, Pin 7	V _{reg}	8.0	Vdc
Power Supply Voltage, Pin 8	Vcc	10	Vdc
Operating Temperature Range	TA	-40 to +85	°C
Storage Temperature Range	T _{stg}	-65 to +175	°C

NOTE; ESD data available upon request.

ELECTRICAL CHARACTERISTICS ($V_{CC} = 5.5 \text{ to } 9.5 \text{ V}; V_{reg} = 4.5 \text{ to } 5.5 \text{ V}; T_A = -40 \text{ to } 85^{\circ}\text{C}$), unless otherwise noted.)

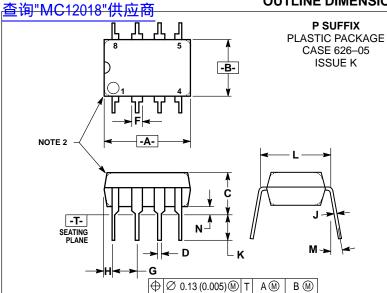
Characteristic	Symbol	Min	Тур	Max	Unit
Toggle Frequency (Sine Wave Input)	f _{max} f _{min}	520 -	_ _	- 75	MHz
Supply Current	Icc	-	8.0	10.7	mA
Control Input HIGH (÷128)	VIH	2.0	-	-	V
Control Input LOW (÷129)	VIL	-	-	0.8	V
Differntial Output Voltage (I _{source} = -200μA)	V _{out}	0.8	1.0	_	V
Input Voltage Sensitivity 75MHz 125–520MHz	V _{in}	400 200	- -	800 800	mVpp
PLL Response Time (Notes 1 and 2)	t _{PLL}	_	_	t _{out} -50	ns

NOTES: 1. tp_LL = the period of time the PLL has from the prescaler rising output tranistion (50%) to the modulus control input edge transition (50%) to ensure proper modulus selection.

2. tout = period of output waveform.

MC12018

OUTLINE DIMENSIONS



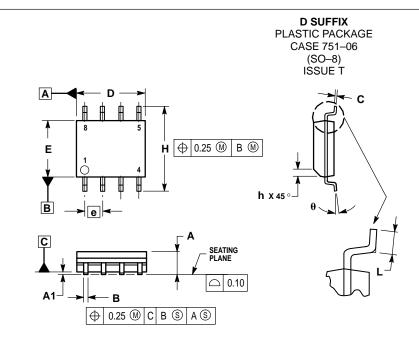
NOTES:

- DIMENSION L TO CENTER OF LEAD WHEN FORMED PARALLEL.
- PARALLEL.

 2. PACKAGE CONTOUR OPTIONAL (ROUND OR SQUARE CORNERS).

 3. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

	MILLIMETERS		INC	HES
DIM	MIN	MAX	MIN	MAX
Α	9.40	10.16	0.370	0.400
В	6.10	6.60	0.240	0.260
С	3.94	4.45	0.155	0.175
D	0.38	0.51	0.015	0.020
F	1.02	1.78	0.040	0.070
G	2.54	BSC	0.100	BSC
Н	0.76	1.27	0.030	0.050
J	0.20	0.30	0.008	0.012
K	2.92	3.43	0.115	0.135
L	7.62	BSC	0.300	BSC
М	_	10°	_	10°
N	0.76	1.01	0.030	0.040



NOTES:

- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
- 2. DIMENSIONS ARE IN MILLIMETER.
 3. DIMENSION D AND E DO NOT INCLUDE MOLD
- MIMENSION D AND E DO NOT INCLUDE MOLD PROTRUSION.
 MAXIMUM MOLD PROTRUSION 0,15 PER SIDE.
 DIMENSION B DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION. SHALL BE 0,127 TOTAL IN EXCESS OF THE DAMBAR PROTRUSION SHALL BE 0.127 TOTAL IN EXCESS. OF THE B DIMENSION AT MAXIMUM MATERIAL CONDITION.

	MILLIMETERS		
DIM	MIN	MAX	
Α	1.35	1.75	
A1	0.10	0.25	
В	0.35	0.49	
C	0.19	0.25	
D	4.80	5.00	
Е	3.80	4.00	
е	1.27 BSC		
Н	5.80	6.20	
h	0.25	0.50	
L	0.40	1.25	
θ	0 °	7 °	

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