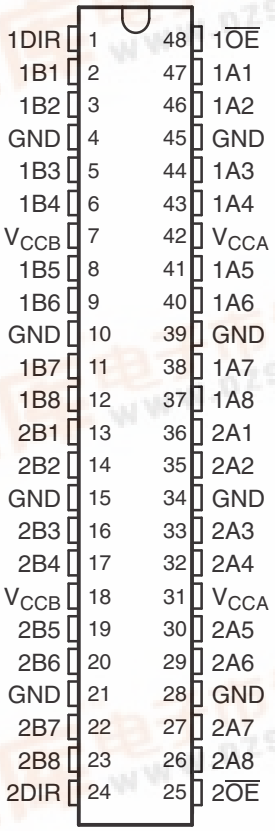
16-BIT DUAL-SUPPLY BUS TRANSCEIVER WITH CONFIGURABLE VOLTAGE TRANSLATION AND 3-STATE OUTPUTS

FEATURES

- **Qualified for Automotive Applications**
- **Control Inputs V_{IH}/V_{IL} Levels Are Referenced to V_{CCA} Voltage**
- **V_{CC} Isolation Feature – If Either V_{CC} Input Is at GND, Both Ports Are in the High-Impedance State**
- **Overvoltage-Tolerant Inputs/Outputs Allow Mixed-Voltage-Mode Data Communications**
- **Fully Configurable Dual-Rail Design Allows Each Port to Operate Over the Full 1.2-V to 3.6-V Power-Supply Range**
- **I_{off} Supports Partial-Power-Down Mode Operation**
- **I/Os Are 4.6-V Tolerant**
- **Max Data Rates**
  - **380 Mbps (1.8-V to 3.3-V Translation)**
  - **200 Mbps (<1.8-V to 3.3-V Translation)**
  - **200 Mbps (Translate to 2.5 V or 1.8 V)**
  - **150 Mbps (Translate to 1.5 V)**
  - **100 Mbps (Translate to 1.2 V)**
- **Latch-Up Performance Exceeds 100 mA Per JESD 78, Class II**
- **ESD Protection Exceeds JESD 22**
  - **8000-V Human-Body Model (A114-A)**
  - **200-V Machine Model (A115-A)**
  - **1000-V Charged-Device Model (C101)**

DGV PACKAGE
(TOP VIEW)



DESCRIPTION/ORDERING INFORMATION

This 16-bit noninverting bus transceiver uses two separate configurable power-supply rails. The SN74AVC16T245 is optimized to operate with V_{CCA}/V_{CCB} set at 1.4 V to 3.6 V. It is operational with V_{CCA}/V_{CCB} as low as 1.2 V. The A port is designed to track V_{CCA}. V_{CCA} accepts any supply voltage from 1.2 V to 3.6 V. The B port is designed to track V_{CCB}. V_{CCB} accepts any supply voltage from 1.2 V to 3.6 V. This allows for universal low-voltage bidirectional translation between any of the 1.2-V, 1.5-V, 1.8-V, 2.5-V, and 3.3-V voltage nodes.

The SN74AVC16T245 is designed for asynchronous communication between data buses. The device transmits data from the A bus to the B bus or from the B bus to the A bus, depending on the logic level at the direction-control (DIR) input. The output-enable ($\overline{OE}$) input can be used to disable the outputs so the buses effectively are isolated.

The SN74AVC16T245 is designed so that the control pins (1DIR, 2DIR, $1\overline{OE}$, and $2\overline{OE}$) are supplied by V_{CCA}.

This device is fully specified for partial-power-down applications using I_{off}. The I_{off} circuitry disables the outputs, preventing damaging current backflow through the device when it is powered down.

The V_{CC} isolation feature ensures that if either V_{CC} input is at GND, both ports are in the high-impedance state.

To ensure the high-impedance state during power up or power down, $\overline{OE}$ should be tied to V_{CC} through a pullup resistor; the minimum value of the resistor is determined by the current-sinking capability of the driver.

ORDERING INFORMATION(1)

T_A	PACKAGE(2)		ORDERABLE PART NUMBER	TOP-SIDE MARKING
–40°C to 125°C	TVSOP – DGV	Reel of 2000	CAVC16T245QDGVRQ1	WF245Q

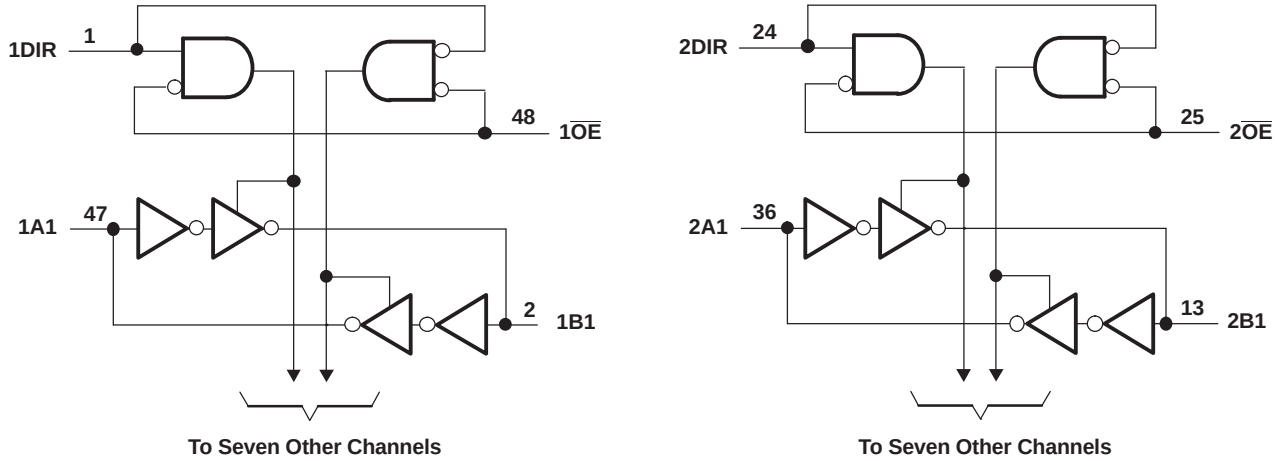
(1) For the most current package and ordering information, see the Package Option Addendum at the end of this document, or see the TI web site at www.ti.com.
(2) Package drawings, thermal data, and symbolization are available at www.ti.com/packaging.

FUNCTION TABLE(1) (EACH 16-BIT SECTION)

CONTROL INPUTS		OUTPUT CIRCUITS		OPERATION
$\overline{OE}$	DIR	A PORT	B PORT	
L	L	Enabled	Hi-Z	B data to A bus
L	H	Hi-Z	Enabled	A data to B bus
H	X	Hi-Z	Hi-Z	Isolation

(1) Input circuits of the data I/Os always are active.

LOGIC DIAGRAM (POSITIVE LOGIC)



ABSOLUTE MAXIMUM RATINGS(1)

over operating free-air temperature range (unless otherwise noted)

			MIN	MAX	UNIT
V_{CCA} V_{CCB}	Supply voltage range		–0.5	4.6	V
V_I	Input voltage range(2)	I/O ports (A port)	–0.5	4.6	V
		I/O ports (B port)	–0.5	4.6	
		Control inputs	–0.5	4.6	
V_O	Voltage range applied to any output in the high-impedance or power-off state(2)	A port	–0.5	4.6	V
		B port	–0.5	4.6	
V_O	Voltage range applied to any output in the high or low state(2)(3)	A port	–0.5	V_{CCA} + 0.5	V
		B port	–0.5	V_{CCB} + 0.5	
I_{IK}	Input clamp current	V_I < 0		–50	mA
I_{OK}	Output clamp current	V_O < 0		–50	mA
I_O	Continuous output current			±50	mA
	Continuous current through each V_{CCA}, V_{CCB}, and GND			±100	mA
θ_{JA}	Package thermal impedance(4)			58	°C/W
T_{stg}	Storage temperature range		–65	150	°C

(1) Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.
(2) The input voltage and output negative-voltage ratings may be exceeded if the input and output current ratings are observed.
(3) The output positive-voltage rating may be exceeded up to 4.6 V maximum if the output current rating is observed.
(4) The package thermal impedance is calculated in accordance with JESD 51-7.

RECOMMENDED OPERATING CONDITIONS[1][2][3]

			V_{CCI}	V_{CCO}	MIN	MAX	UNIT
V_{CCA}	Supply voltage				1.2	3.6	V
V_{CCB}	Supply voltage				1.2	3.6	V
V_{IH}	High-level input voltage	Data inputs[4]	1.2 V to 1.95 V		V_{CCI} × 0.65		V
			1.95 V to 2.7 V		1.6		
			2.7 V to 3.6 V		2		
V_{IL}	Low-level input voltage	Data inputs[4]	1.2 V to 1.95 V			V_{CCI} × 0.35	V
			1.95 V to 2.7 V			0.7	
			2.7 V to 3.6 V			0.8	
V_{IH}	High-level input voltage	DIR (referenced to V_{CCA})[5]	1.2 V to 1.95 V		V_{CCA} × 0.65		V
			1.95 V to 2.7 V		1.6		
			2.7 V to 3.6 V		2		
V_{IL}	Low-level input voltage	DIR (referenced to V_{CCA})[5]	1.2 V to 1.95 V			V_{CCA} × 0.35	V
			1.95 V to 2.7 V			0.7	
			2.7 V to 3.6 V			0.8	
V_I	Input voltage				0	3.6	V
V_O	Output voltage	Active state			0	V_{CCO}	V
		3-state			0	3.6	
I_{OH}	High-level output current			1.2 V		–3	mA
				1.4 V to 1.6 V		–6	
				1.65 V to 1.95 V		–8	
				2.3 V to 2.7 V		–9	
				3 V to 3.6 V		–12	
I_{OL}	Low-level output current			1.2 V		3	mA
				1.4 V to 1.6 V		6	
				1.65 V to 1.95 V		8	
				2.3 V to 2.7 V		9	
				3 V to 3.6 V		12	
Δt/Δv	Input transition rise or fall rate					5	ns/V
T_A	Operating free-air temperature				–40	125	°C

(1) V_{CCI} is the V_{CC} associated with the data input port.
(2) V_{CCO} is the V_{CC} associated with the output port.
(3) All unused data inputs of the device must be held at V_{CCI} or GND to ensure proper device operation. Refer to the TI application report, *Implications of Slow or Floating CMOS Inputs*, literature number SCBA004.
(4) For V_{CCI} values not specified in the data sheet, V_{IH} min = V_{CCI} × 0.7 V, V_{IL} max = V_{CCI} × 0.3 V.
(5) For V_{CCA} values not specified in the data sheet, V_{IH} min = V_{CCA} × 0.7 V, V_{IL} max = V_{CCA} × 0.3 V.

ELECTRICAL CHARACTERISTICS(1)(2)

over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER		TEST CONDITIONS		V_{CCA}	V_{CCB}	T_A = 25°C			–40°C to 125°C		UNIT
						MIN	TYP	MAX	MIN	MAX	
V_{OH}		I_{OH} = –100 µA	$V_I = V_{IH}$	1.2 V to 3.6 V	1.2 V to 3.6 V				V_{CCO} – 0.2		V
		I_{OH} = –3 mA		1.2 V	1.2 V		0.95				
		I_{OH} = –6 mA		1.4 V	1.4 V				1.0		
		I_{OH} = –8 mA		1.65 V	1.65 V				1.15		
		I_{OH} = –9 mA		2.3 V	2.3 V				1.75		
		I_{OH} = –12 mA		3 V	3 V				2.3		
V_{OL}		I_{OL} = 100 µA	$V_I = V_{IL}$	1.2 V to 3.6 V	1.2 V to 3.6 V					0.2	V
		I_{OL} = 3 mA		1.2 V	1.2 V		0.15				
		I_{OL} = 6 mA		1.4 V	1.4 V					0.4	
		I_{OL} = 8 mA		1.65 V	1.65 V					0.45	
		I_{OL} = 9 mA		2.3 V	2.3 V					0.55	
		I_{OL} = 12 mA		3 V	3 V					0.7	
I_I	Control inputs	$V_I = V_{CCA}$ or GND		1.2 V to 3.6 V	1.2 V to 3.6 V		±0.025	±0.25		±2	µA
I_{off}	A or B port	V_I or V_O = 0 to 3.6 V		0 V	0 to 3.6 V		±0.1	±2.5		±10	µA
	A or B port			0 to 3.6 V	0 V		±0.5	±2.5		±10	
I_{OZ}(3)	A or B port	$V_O = V_{CCO}$ or GND, $\overline{V_I}$ = V_{CCI} or GND, $\overline{OE}$ = V_{IH}		3.6 V	3.6 V		±0.5	±2.5		±10	µA
I_{CCA}		$V_I = V_{CCI}$ or GND, I_O = 0		1.2 V to 3.6 V	1.2 V to 3.6 V					30	µA
				0 V	3.6 V					–40	
				3.6 V	0 V					30	
I_{CCB}		$V_I = V_{CCI}$ or GND, I_O = 0		1.2 V to 3.6 V	1.2 V to 3.6 V					30	µA
				0 V	3.6 V					30	
				3.6 V	0 V					–40	
$I_{CCA} + I_{CCB}$		$V_I = V_{CCI}$ or GND, I_O = 0		1.2 V to 3.6 V	1.2 V to 3.6 V					60	µA
C_i	Control inputs	V_I = 3.3 V or GND		3.3 V	3.3 V		3.5				pF
C_{io}	A or B port	V_O = 3.3 V or GND		3.3 V	3.3 V		7				pF

(1) V_{CCO} is the V_{CC} associated with the output port.
(2) V_{CCI} is the V_{CC} associated with the input port.
(3) For I/O ports, the parameter I_{OZ} includes the input leakage current.

SWITCHING CHARACTERISTICS

over recommended operating free-air temperature range, V_{CCA} = 1.2 V (see Figure 11)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	V_{CCB} = 1.2 V	V_{CCB} = 1.5 V	V_{CCB} = 1.8 V	V_{CCB} = 2.5 V	V_{CCB} = 3.3 V	UNIT
			TYP	TYP	TYP	TYP	TYP	
t_{PLH}	A	B	4.1	3.3	3	2.8	3.2	ns
t_{PHL}			4.1	3.3	3	2.8	3.2	
t_{PLH}	B	A	4.4	4	3.8	3.6	3.5	ns
t_{PHL}			4.4	4	3.8	3.6	3.5	
t_{PZH}	$\overline{OE}$	A	6.4	6.4	6.4	6.4	6.4	ns
t_{PZL}			6.4	6.4	6.4	6.4	6.4	
t_{PZH}	$\overline{OE}$	B	6	4.6	4	3.4	3.2	ns
t_{PZL}			6	4.6	4	3.4	3.2	
t_{PHZ}	$\overline{OE}$	A	6.6	6.6	6.6	6.6	6.8	ns
t_{PLZ}			6.6	6.6	6.6	6.6	6.8	
t_{PHZ}	$\overline{OE}$	B	6	4.9	4.9	4.2	5.3	ns
t_{PLZ}			6	4.9	4.9	4.2	5.3	

SWITCHING CHARACTERISTICS

over recommended operating free-air temperature range, V_{CCA} = 1.5 V ± 0.1 V (see Figure 11)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	V_{CCB} = 1.2 V	V_{CCB} = 1.5 V ± 0.1 V		V_{CCB} = 1.8 V ± 0.15 V		V_{CCB} = 2.5 V ± 0.2 V		V_{CCB} = 3.3 V ± 0.3 V		UNIT
			TYP	MIN	MAX	MIN	MAX	MIN	MAX	MIN	MAX	
t_{PLH}	A	B	3.6	0.5	9.2	0.5	8.2	0.5	7.1	0.5	6.7	ns
t_{PHL}			3.6	0.5	9.2	0.5	8.2	0.5	7.1	0.5	6.7	
t_{PLH}	B	A	3.3	0.5	9.2	0.5	8.9	0.5	8.6	0.5	8.5	ns
t_{PHL}			3.3	0.5	9.2	0.5	8.9	0.5	8.6	0.5	8.5	
t_{PZH}	$\overline{OE}$	A	4.3	0.5	13.1	0.5	13.1	0.5	13.1	0.5	13.1	ns
t_{PZL}			4.3	0.5	13.1	0.5	13.1	0.5	13.1	0.5	13.1	
t_{PZH}	$\overline{OE}$	B	5.6	0.5	13.1	0.5	11.1	0.5	8.9	0.5	8.2	ns
t_{PZL}			5.6	0.5	13.1	0.5	11.1	0.5	8.9	0.5	8.2	
t_{PHZ}	$\overline{OE}$	A	4.5	0.5	12.1	0.5	12.1	0.5	12.1	0.5	12.1	ns
t_{PLZ}			4.5	0.5	12.1	0.5	12.1	0.5	12.1	0.5	12.1	
t_{PHZ}	$\overline{OE}$	B	5.5	0.5	11.7	0.5	10.5	0.5	9.5	0.5	9.3	ns
t_{PLZ}			5.5	0.5	11.7	0.5	10.5	0.5	9.5	0.5	9.3	

SWITCHING CHARACTERISTICS

over recommended operating free-air temperature range, V_{CCA} = 1.8 V ± 0.15 V (see Figure 11)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	V_{CCB} = 1.2 V	V_{CCB} = 1.5 V ± 0.1 V		V_{CCB} = 1.8 V ± 0.15 V		V_{CCB} = 2.5 V ± 0.2 V		V_{CCB} = 3.3 V ± 0.3 V		UNIT
			TYP	MIN	MAX	MIN	MAX	MIN	MAX	MIN	MAX	
t_{PLH}	A	B	3.4	0.5	8.9	0.5	7.8	0.5	6.7	0.5	6.3	ns
t_{PHL}			3.4	0.5	8.9	0.5	7.8	0.5	6.7	0.5	6.3	
t_{PLH}	B	A	3	0.5	8.2	0.5	7.8	0.5	7.5	0.5	7.4	ns
t_{PHL}			3	0.5	8.2	0.5	7.8	0.5	7.5	0.5	7.4	
t_{PZH}	$\overline{OE}$	A	3.4	0.5	10.8	0.5	10.8	0.5	10.8	0.5	10.8	ns
t_{PZL}			3.4	0.5	10.8	0.5	10.8	0.5	10.8	0.5	10.8	
t_{PZH}	$\overline{OE}$	B	5.4	0.5	12.2	0.5	10.4	0.5	8.3	0.5	7.5	ns
t_{PZL}			5.4	0.5	12.2	0.5	10.4	0.5	8.3	0.5	7.5	
t_{PHZ}	$\overline{OE}$	A	4.2	0.5	10.7	0.5	10.7	0.5	10.7	0.5	10.7	ns
t_{PLZ}			4.2	0.5	10.7	0.5	10.7	0.5	10.7	0.5	10.7	
t_{PHZ}	$\overline{OE}$	B	5.2	0.5	11.4	0.5	10.1	0.5	8.9	0.5	8.7	ns
t_{PLZ}			5.2	0.5	11.4	0.5	10.1	0.5	8.9	0.5	8.7	

SWITCHING CHARACTERISTICS

over recommended operating free-air temperature range, V_{CCA} = 2.5 V ± 0.2 V (see Figure 11)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	V_{CCB} = 1.2 V	V_{CCB} = 1.5 V ± 0.1 V		V_{CCB} = 1.8 V ± 0.15 V		V_{CCB} = 2.5 V ± 0.2 V		V_{CCB} = 3.3 V ± 0.3 V		UNIT
			TYP	MIN	MAX	MIN	MAX	MIN	MAX	MIN	MAX	
t_{PLH}	A	B	3.2	0.5	9.6	0.5	7.5	0.5	6.3	0.5	5.8	ns
t_{PHL}			3.2	0.5	8.6	0.5	7.5	0.5	6.3	0.5	5.8	
t_{PLH}	B	A	2.6	0.5	7.1	0.5	6.7	0.5	6.3	0.5	6.2	ns
t_{PHL}			2.6	0.5	7.1	0.5	6.7	0.5	6.3	0.5	6.2	
t_{PZH}	$\overline{OE}$	A	2.5	0.5	8.3	0.5	8.3	0.5	8.3	0.5	8.3	ns
t_{PZL}			2.5	0.5	8.3	0.5	8.3	0.5	8.3	0.5	8.3	
t_{PZH}	$\overline{OE}$	B	5.2	0.5	12.4	0.5	10.3	0.5	8.1	0.5	7.5	ns
t_{PZL}			5.2	0.5	12.4	0.5	10.3	0.5	8.1	0.5	7.5	
t_{PHZ}	$\overline{OE}$	A	3	0.5	9.1	0.5	9.1	0.5	9.1	0.5	9.1	ns
t_{PLZ}			3	0.5	9.1	0.5	9.1	0.5	9.1	0.5	9.1	
t_{PHZ}	$\overline{OE}$	B	5	0.5	10.9	0.5	9.6	0.5	9.1	0.5	8.2	ns
t_{PLZ}			5	0.5	10.9	0.5	9.6	0.5	9.1	0.5	8.2	

SWITCHING CHARACTERISTICS

over recommended operating free-air temperature range, V_{CCA} = 3.3 V ± 0.3 V (see Figure 11)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	V_{CCB} = 1.2 V	V_{CCB} = 1.5 V ± 0.1 V		V_{CCB} = 1.8 V ± 0.15 V		V_{CCB} = 2.5 V ± 0.2 V		V_{CCB} = 3.3 V ± 0.3 V		UNIT
			TYP	MIN	MAX	MIN	MAX	MIN	MAX	MIN	MAX	
t_{PLH}	A	B	3.2	0.5	8.5	0.5	7.4	0.5	6.2	0.5	5.7	ns
t_{PHL}			3.2	0.5	8.5	0.5	7.4	0.5	6.2	0.5	5.7	
t_{PLH}	B	A	2.8	0.5	6.7	0.5	6.3	0.5	5.8	0.5	5.7	ns
t_{PHL}			2.8	0.5	6.7	0.5	6.3	0.5	5.8	0.5	5.7	
t_{PZH}	$\overline{OE}$	A	2.2	0.5	7.3	0.5	7.2	0.5	7.1	0.5	7	ns
t_{PZL}			2.2	0.5	7.3	0.5	7.2	0.5	7.1	0.5	7	
t_{PZH}	$\overline{OE}$	B	5.1	0.5	12.3	0.5	10.2	0.5	7.9	0.5	7	ns
t_{PZL}			5.1	0.5	12.3	0.5	10.2	0.5	7.9	0.5	7	
t_{PHZ}	$\overline{OE}$	A	3.4	0.5	8	0.5	8	0.5	8	0.5	8	ns
t_{PLZ}			3.4	0.5	8	0.5	8	0.5	8	0.5	8	
t_{PHZ}	$\overline{OE}$	B	4.9	0.5	10.7	0.5	9.5	0.5	8.2	0.5	8	ns
t_{PLZ}			4.9	0.5	10.7	0.5	9.5	0.5	8.2	0.5	8	

OPERATING CHARACTERISTICS

T_A = 25°C

PARAMETER			TEST CONDITIONS	V_{CCA} = V_{CCB} = 1.2 V	V_{CCA} = V_{CCB} = 1.5 V	V_{CCA} = V_{CCB} = 1.8 V	V_{CCA} = V_{CCB} = 2.5 V	V_{CCA} = V_{CCB} = 3.3 V	UNIT
				TYP	TYP	TYP	TYP	TYP	
C_{pdA} (1)	A to B	Outputs enabled	C_L = 0, f = 10 MHz, t_r = t_f = 1 ns	1	1	1	1	2	pF
		Outputs disabled		1	1	1	1	1	
	B to A	Outputs enabled		13	13	14	15	16	
		Outputs disabled		1	1	1	1	1	
C_{pdB} (1)	A to B	Outputs enabled	C_L = 0, f = 10 MHz, t_r = t_f = 1 ns	13	13	14	15	16	pF
		Outputs disabled		1	1	1	1	1	
	B to A	Outputs enabled		1	1	1	1	2	
		Outputs disabled		1	1	1	1	1	

(1) Power dissipation capacitance per transceiver

TYPICAL CHARACTERISTICS

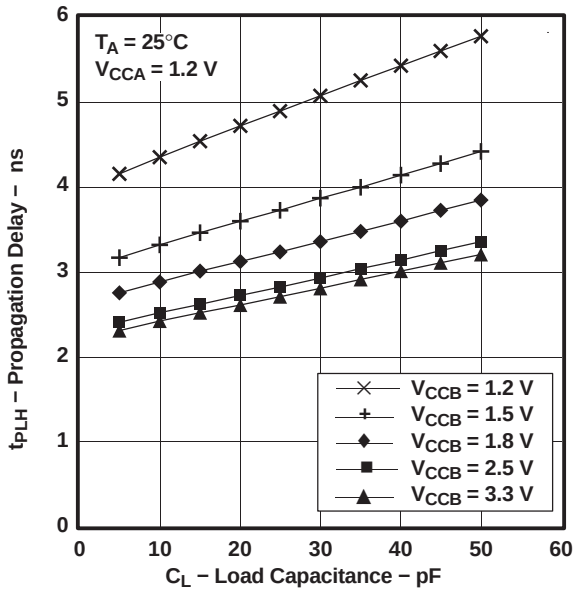


Figure 1.

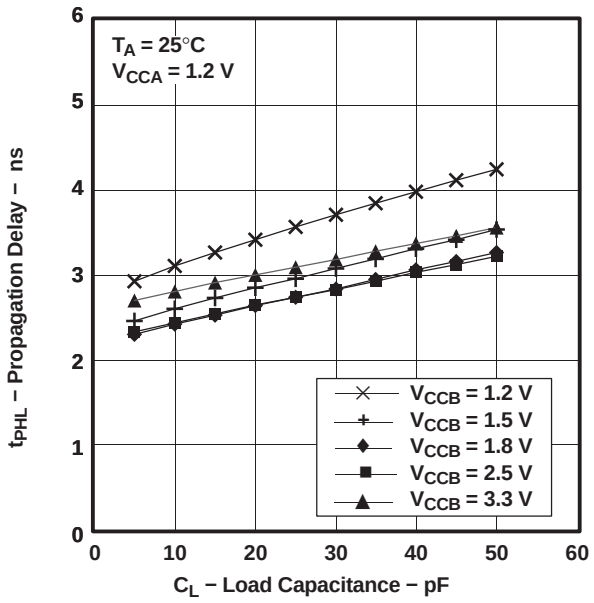


Figure 2.

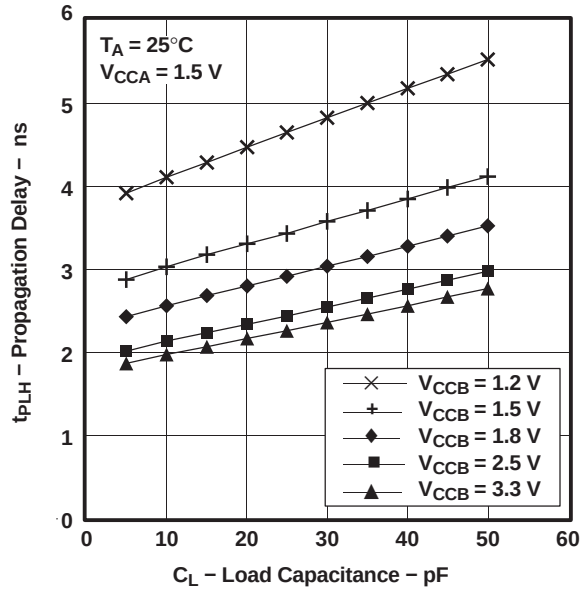


Figure 3.

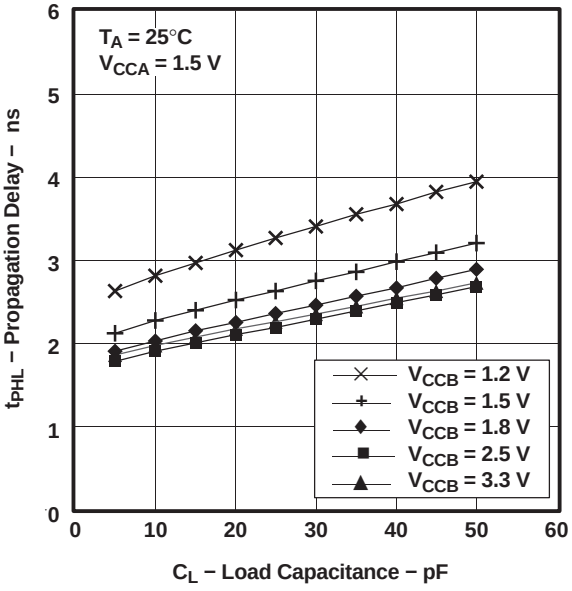
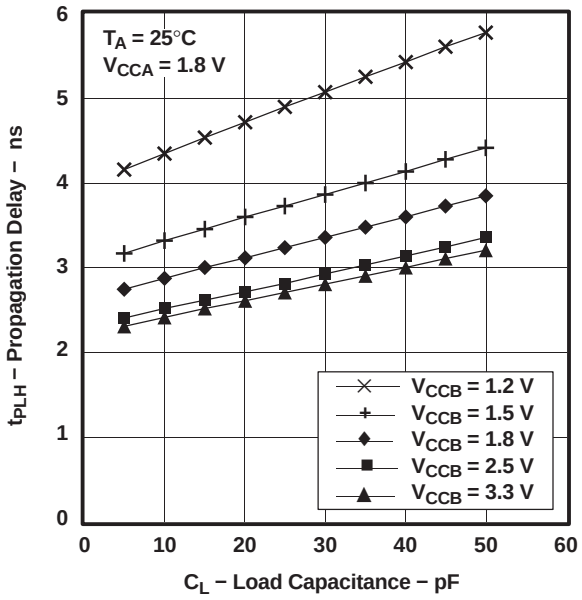


Figure 4.

TYPICAL CHARACTERISTICS (continued)



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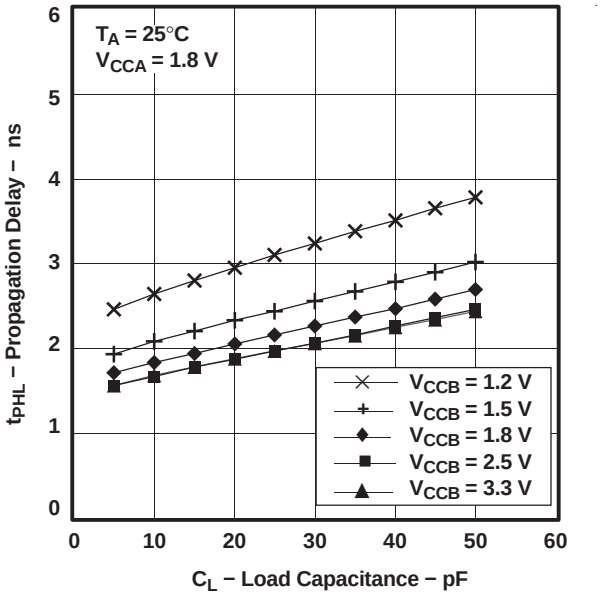

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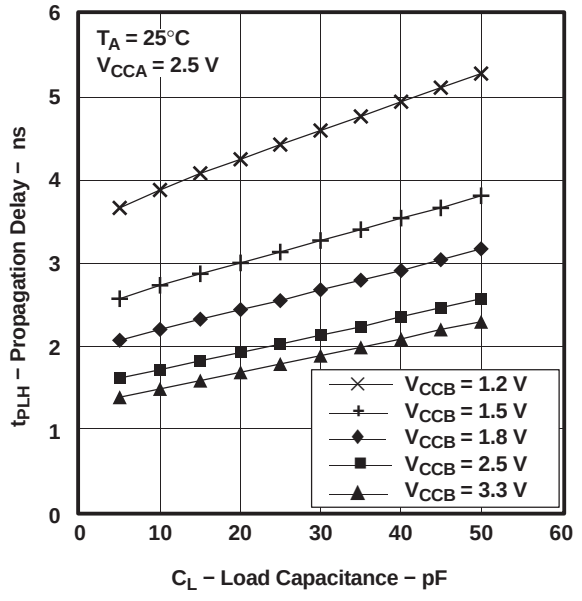

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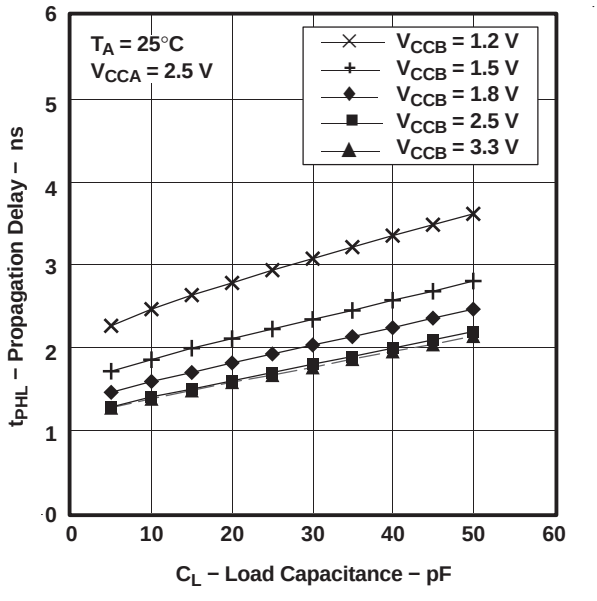


Figure 8.

TYPICAL CHARACTERISTICS (continued)

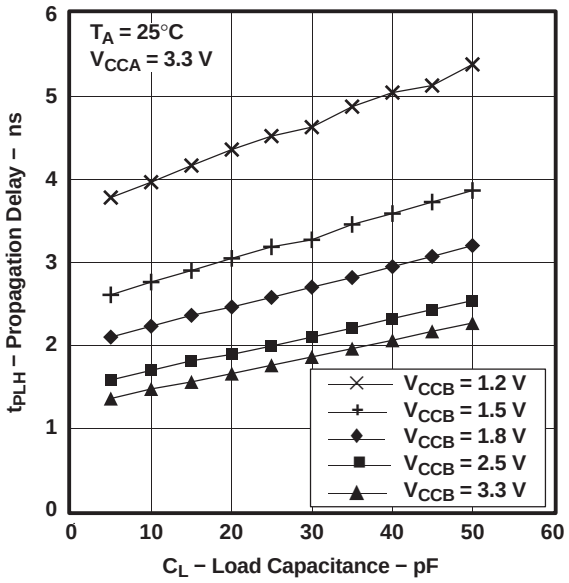


Figure 9.

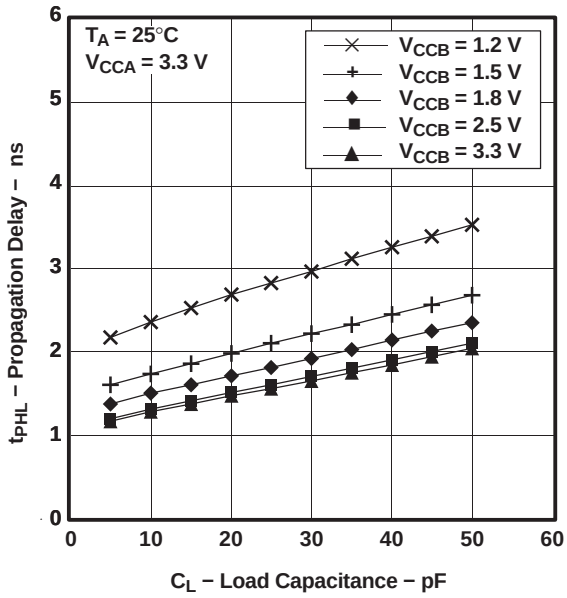
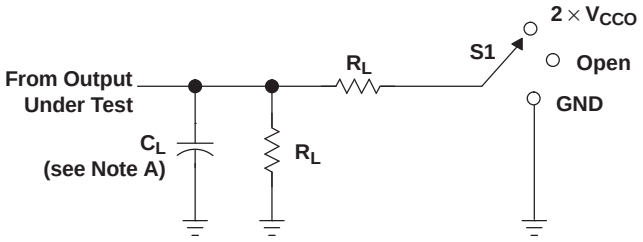


Figure 10.

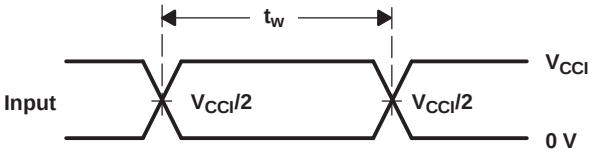
PARAMETER MEASUREMENT INFORMATION



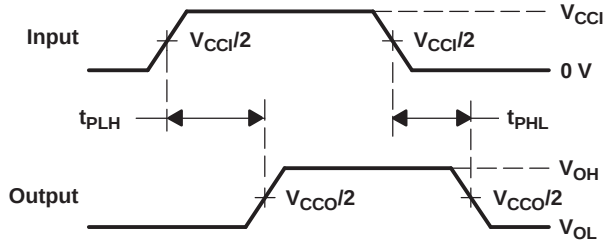
LOAD CIRCUIT

TEST	S1
t_{pd}	Open
t_{PLZ}/t_{PZL}	$2 \times V_{CCO}$
t_{PHZ}/t_{PZH}	GND

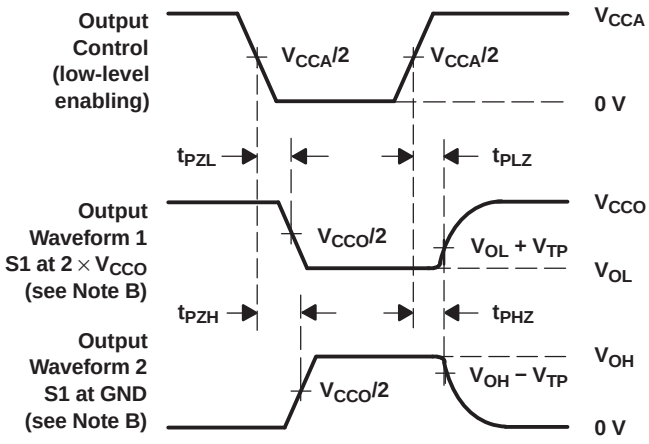
V_{CCO}	C_L	R_L	V_{TP}
1.2 V	15 pF	2 kΩ	0.1 V
1.5 V ± 0.1 V	15 pF	2 kΩ	0.1 V
1.8 V ± 0.15 V	15 pF	2 kΩ	0.15 V
2.5 V ± 0.2 V	15 pF	2 kΩ	0.15 V
3.3 V ± 0.3 V	15 pF	2 kΩ	0.3 V



VOLTAGE WAVEFORMS
PULSE DURATION



VOLTAGE WAVEFORMS
PROPAGATION DELAY TIMES



VOLTAGE WAVEFORMS
ENABLE AND DISABLE TIMES

NOTES:
A. C_L includes probe and jig capacitance.
B. Waveform 1 is for an output with internal conditions such that the output is low, except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high, except when disabled by the output control.
C. All input pulses are supplied by generators having the following characteristics: PRR ≤ 10 MHz, Z_O = 50 Ω, dv/dt ≥ 1 V/ns.
D. The outputs are measured one at a time, with one transition per measurement.
E. t_{PLZ} and t_{PHZ} are the same as t_{dis}.
F. t_{PZL} and t_{PZH} are the same as t_{en}.
G. t_{PLH} and t_{PHL} are the same as t_{pd}.
H. V_{CCI} is the V_{CC} associated with the input port.
I. V_{CCO} is the V_{CC} associated with the output port.

Figure 11. Load Circuit and Voltage Waveforms

PACKAGING INFORMATION

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead/Ball Finish	MSL Peak Temp (3)
CAVC16T245QDGVRQ1	ACTIVE	TVSOP	DGV	48	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-3-260C-168 HR

(1) The marketing status values are defined as follows:
ACTIVE: Product device recommended for new designs.
LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.
NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.
PREVIEW: Device has been announced but is not in production. Samples may or may not be available.
OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.
TBD: The Pb-Free/Green conversion plan has not been defined.
Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.
Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.
Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

(3) MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

OTHER QUALIFIED VERSIONS OF SN74AVC16T245-Q1 :
- Catalog: SN74AVC16T245

NOTE: Qualified Version Definitions:
- Catalog - TI's standard catalog product

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