# 查询"MPC9108"供应商

# Advance Information Multiple Output Clock Synthesizer

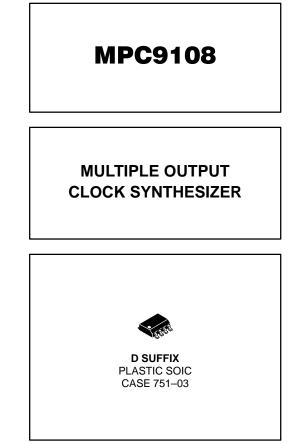
The MPC9108 is a multiple CMOS output clock synthesizer targeted for disk drive applications. The device interfaces to a 20MHz crystal as its frequency source. From this source the device provides a buffered copy of the 20MHz clock as well as synthesized 40MHz and 50MHz output clocks.

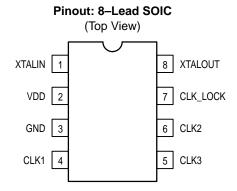
- Fully Integrated PLL
- Fully Integrated Crystal Oscillator
- · Low cost, low jitter design
- Low cost 8-lead SOIC packaging

In addition to the output clock frequencies, the MPC9108 also offers a lock indicator output. When the internal PLL achieves phase and frequency lock the CLK\_LOCK signal will go HIGH. The pin will remain HIGH unless the PLL loses lock due to input clock or power supply disturbances.

The XTALIN pin (pin 1) can be over-driven with a standard 5V CMOS signal. When an externally generated reference is used the XTALOUT pin should be left open.

The MPC9108 operates from a 5.0V supply across the commercial temperature range of  $0^{\circ}$ C to  $70^{\circ}$ C. The 8–lead SOIC package is used to optimize board space efficiency as well as minimizing cost.





#### **Pin Descriptions**

Pin Name	Pin Number	I/O	Function			
XTALIN	1	I	20MHz Crystal Connection, External Reference Point			
VDD	2	-	+5V Power Supply			
GND	3	-	Ground			
CLK1	4	0	20MHz Output, Buffer Xtal Output			
CLK2	5	0	50MHz Output, PLL Controlled			
CLK3	6	0	40MHz Output, PLL Controlled			
CLK_LOCK	7	0	HIGH When PLL is Locked			
XTALOUT	8	0	Crystal Oscillator Connection			

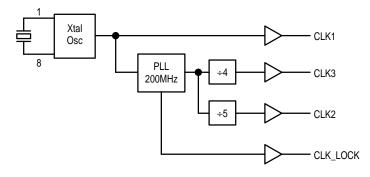
This document contains information on a new product. Specifications and information herein are subject to change without notice.



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### **BLOCK DIAGRAM**



#### **MAXIMUM RATINGS\***

Symbol	Parameter	Value	Unit
V <sub>CC</sub>	DC Supply Voltage (Referenced to GND)	-0.5 to +7.0	V
VIN	Input Voltage	-0.5 to +7.0	V
т <sub>А</sub>	Operating Temperature Range (In Free-Air)	0 to +70	°C
т <sub>А</sub>	Ambient Temperature Range (Under Bias)	-55 to +125	°C
T <sub>STG</sub>	Storage Temperature Range	-65 to +150	°C

\* Maximum Ratings are those values beyond which damage to the device may occur. Functional operation should be restricted to the Recommended Operating Conditions.

## DC CHARACTERISTICS (0°C < TA < 70°C; V\_DD = 5V $\pm$ 10%; Unless Otherwise Stated)

Symbol	Characteristic	Min	Тур	Max	Unit	Condition
VIL	Input Low Voltage			0.8	V	$V_{DD} = 5V$
VIH	Input High Voltage	2.0			V	$V_{DD} = 5V$
۱ <sub>IL</sub>	Input Low Current			-5	μΑ	V <sub>IN</sub> = 0.5V
IIH	Input High Current			5	μΑ	$V_{IN} = V_{DD}$
V <sub>OL</sub>	Output Low Voltage CLKn			0.4	V	I <sub>OL</sub> = 4mA
VOL	Output Low Voltage CLK_LOCK			0.4	V	I <sub>OL</sub> = 10mA
VOH	Output High Voltage	0.8V <sub>DD</sub>			V	I <sub>OH</sub> = -30mA
I <sub>DD</sub>	Supply Current		25	40	mA	No Load; Note 1.
FD	Output Frequency Change Over Supply & Temp		0.002	0.01	%	With Respect to Typ Freq
ISC	Short Circuit Current	25	40		mA	Each Output Clock
Cl	Input Capacitance			10	pF	Except X1, X2
CL	Xtal Load Capacitance		20		pF	Pins X1, X2

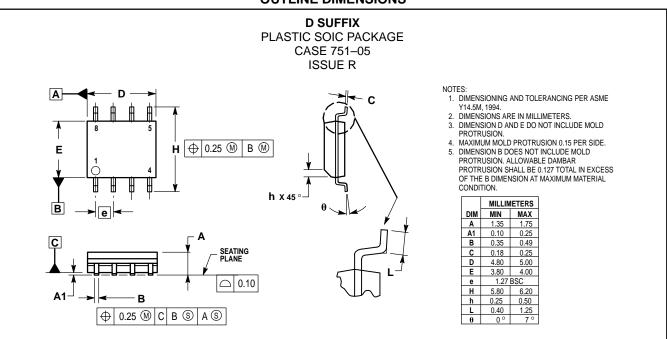
1. All clocks operating at highest frequencies.

查询"MPC9108"供应商 AC CHARACTERISTICS (0°C < T<sub>A</sub> < 70°C; V<sub>DD</sub> = 5V ±10%; Unless Otherwise Stated)

Symbol	Characteristic		Min	Тур	Max	Unit	Condition
<sup>t</sup> ICr	Input Clock Rise Time				20	ns	
<sup>t</sup> ICf	Input Clock Fall Time				20	ns	
tr	Output Rise Time	0.8 to 2.0V		1.0	2.0	ns	30pf Load
t <sub>r</sub>	Rise Time	20% to 80% V <sub>DD</sub>		2.0	4.0	ns	30pf Load
t <sub>f</sub>	Output Fall Time	2.0 to 0.8V		1.0	2.0	ns	30pf Load
t <sub>f</sub>	Fall Time	20% to 80% V <sub>DD</sub>		2.0	4.0	ns	30pf Load
dt	Duty Cycle	Pins 4, 6, 5	45/55	48/52	55/45	%	30pf Load
f <sub>i</sub>	Input Frequency			20		MHz	
<sup>t</sup> jab	Jitter Absolute	Pins 4, 6, 5	-500		500	ps	
<sup>t</sup> lock	Output Lock Time		0.02	3.0	4.0	ms	

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#### **OUTLINE DIMENSIONS**



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