# **Power MOSFET** 750 mAmps, 20 Volts

## N-Channel SOT-23

These miniature surface mount MOSFETs low R<sub>DS(on)</sub> assure minimal power loss and conserve energy, making these devices ideal for use in space sensitive power management circuitry. Typical applications are dc-dc converters and power management in portable and battery-powered products such as computers, printers, PCMCIA cards, cellular and cordless telephones.

### **Features**

- Low R<sub>DS(on)</sub> Provides Higher Efficiency and Extends Battery Life
- Miniature SOT-23 Surface Mount Package Saves Board Space
- Pb-Free Packages are Available

### **MAXIMUM RATINGS** (T<sub>J</sub> = 25°C unless otherwise noted)

Rating	Symbol	Value	Unit
Drain-to-Source Voltage	V <sub>DSS</sub>	20	Vdc
Gate-to-Source Voltage - Continuous	V <sub>GS</sub>	± 20	Vdc
Drain Current  - Continuous @ T <sub>A</sub> = 25°C  - Pulsed Drain Current (t <sub>p</sub> ≤ 10 μs)	I <sub>D</sub> I <sub>DM</sub>	750 2000	mA
Total Power Dissipation @ T <sub>A</sub> = 25°C	P <sub>D</sub>	400	mW
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	300	°C/W
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 seconds	T <sub>L</sub>	260	°C

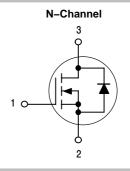
Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.



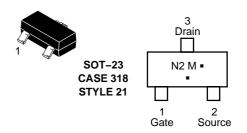
## ON Semiconductor®

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# 750 mAMPS, 20 VOLTS $R_{DS(on)} = 90 \text{ m}\Omega$



### **MARKING DIAGRAM/ PIN ASSIGNMENT**



N2 = Device Code Μ = Date Code\* = Pb-Free Package

(Note: Microdot may be in either location)

### **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
MGSF1N02LT1	SOT-23	3000/Tape & Reel
MGSF1N02LT1G	SOT-23 (Pb-Free)	3000/Tape & Reel
MGSF1N02LT3	SOT-23	10,000/Tape & Reel
MGSF1N02LT3G	SOT-23 (Pb-Free)	10,000/Tape & Reel

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

Preferred devices are recommended choices for future use and best overall value.

<sup>\*</sup>Date Code orientation and overbar may vary depending upon manufacturing location.

### MGSF1N02LT1

**ELECTRICAL CHARACTERISTICS** T<sub>A</sub> = 25°C unless otherwise noted)

Characteristic			Min	Тур	Max	Unit
OFF CHARACTERISTICS						
Drain–to–Source Breakdown Voltage (V <sub>GS</sub> = 0 Vdc, I <sub>D</sub> = 10 μAdc)			20	_	-	Vdc
Zero Gate Voltage Drain Current $(V_{DS} = 20 \text{ Vdc}, V_{GS} = 0 \text{ Vdc})$ $(V_{DS} = 20 \text{ Vdc}, V_{GS} = 0 \text{ Vdc}, T_J = 125^{\circ}\text{C})$			_ _	- -	1.0 10	μAdc
Gate-Body Leakage Current (V <sub>GS</sub> = ± 20 Vdc, V <sub>DS</sub> = 0 Vdc)			-	_	±100	nAdc
ON CHARACTERISTICS (Note 1)						
Gate Threshold Voltage $(V_{DS} = V_{GS}, I_D = 250 \mu Adc)$	V <sub>GS(th)</sub>	1.0	1.7	2.4	Vdc	
Static Drain-to-Source On-Resista ( $V_{GS} = 10 \text{ Vdc}$ , $I_D = 1.2 \text{ Adc}$ ) ( $V_{GS} = 4.5 \text{ Vdc}$ , $I_D = 1.0 \text{ Adc}$ )	r <sub>DS(on)</sub>	- -	0.075 0.115	0.090 0.130	Ω	
DYNAMIC CHARACTERISTICS						
Input Capacitance	(V <sub>DS</sub> = 5.0 Vdc)	C <sub>iss</sub>	-	125	-	pF
Output Capacitance	(V <sub>DS</sub> = 5.0 Vdc)	C <sub>oss</sub>	-	120	-	
Transfer Capacitance	(V <sub>DG</sub> = 5.0 Vdc)	C <sub>rss</sub>	-	45	-	
SWITCHING CHARACTERISTICS (	Note 2)					
Turn-On Delay Time		t <sub>d(on)</sub>	-	2.5	-	ns
Rise Time	(V <sub>DD</sub> = 15 Vdc, I <sub>D</sub> = 1.0 Adc,	t <sub>r</sub>	-	1.0	-	
Turn-Off Delay Time	$R_L = 50 \Omega$ )	t <sub>d(off)</sub>	-	16	-	1
Fall Time		t <sub>f</sub>	-	8.0	-	
Gate Charge (See Figure 6)		Q <sub>T</sub>	-	6000	-	pC
SOURCE-DRAIN DIODE CHARACT	TERISTICS	•		•	•	
Continuous Current	IS	_	_	0.6	Α	
Pulsed Current	I <sub>SM</sub>	-	-	0.75	-	
Forward Voltage (Note 2)			-	0.8	_	V

### TYPICAL ELECTRICAL CHARACTERISTICS

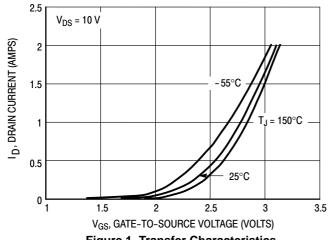


Figure 1. Transfer Characteristics

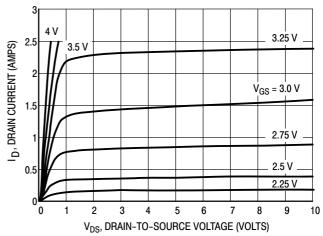


Figure 2. On-Region Characteristics

Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 2%.
 Switching characteristics are independent of operating junction temperature.

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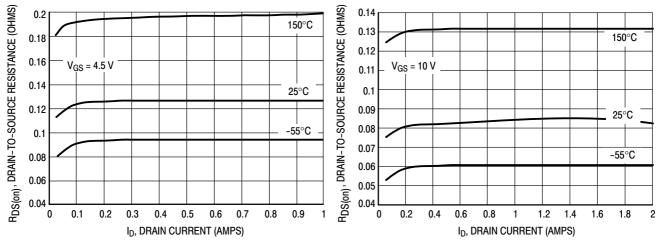


Figure 3. On-Resistance versus Drain Current

Figure 4. On-Resistance versus Drain Current

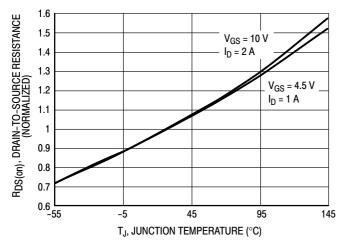


Figure 5. On-Resistance Variation with Temperature

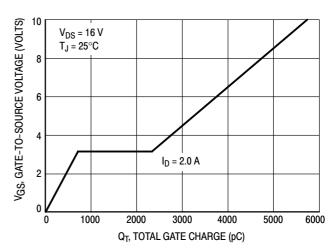


Figure 6. Gate Charge

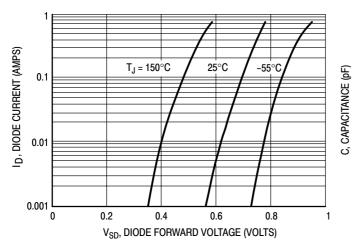


Figure 7. Body Diode Forward Voltage

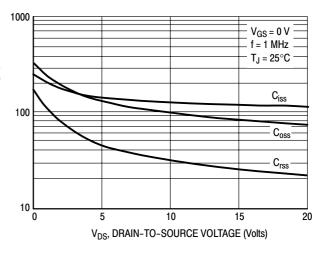


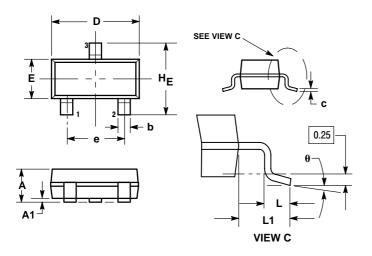
Figure 8. Capacitance

### MGSF1N02LT1

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### PACKAGE DIMENSIONS

### SOT-23 (TO-236) CASE 318-08 **ISSUE AN**



#### NOTES:

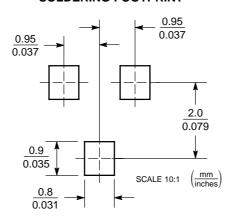
- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
   CONTROLLING DIMENSION: INCH.
   MAXIMUM LEAD THICKNESS INCLUDES DIMENSIONING AND TOLERANCING PER

- LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF
- 318-01 THRU -07 AND -09 OBSOLETE, NEW STANDARD 318-08.

	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.89	1.00	1.11	0.035	0.040	0.044
A1	0.01	0.06	0.10	0.001	0.002	0.004
b	0.37	0.44	0.50	0.015	0.018	0.020
С	0.09	0.13	0.18	0.003	0.005	0.007
D	2.80	2.90	3.04	0.110	0.114	0.120
E	1.20	1.30	1.40	0.047	0.051	0.055
е	1.78	1.90	2.04	0.070	0.075	0.081
L	0.10	0.20	0.30	0.004	0.008	0.012
L1	0.35	0.54	0.69	0.014	0.021	0.029
HE	2.10	2.40	2.64	0.083	0.094	0.104

STYLE 21: PIN 1. GATE 2. SOURCE DRAIN

### **SOLDERING FOOTPRINT\***



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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