Vishay Semiconductors

Bicolor SMD LED



192

DESCRIPTION

These devices have been designed to meet the increasing demand for surface mounting technology.

The package of the VLMKG3400 is the PLCC-4.

It consists of a lead frame which is embedded in a white thermoplast. The reflector inside this package is filled up with clear epoxy.

This SMD device consists of a red and green chip. So it is possible to choose the color in one device.

PRODUCT GROUP AND PACKAGE DATA

Product group: LED
Package: SMD PLCC-4
Product series: bicolor
Angle of half intensity: ± 60°

FEATURES

- SMD LED with exceptional brightness
- Multicolored
- Luminous intensity categorized
- EIA and ICE standard package
- Compatible with automatic placement equipment
- Compatible with IR reflow, vapor phase and wave soldering processes according to CECC 00802 and J-STD-020C
- Available in 8 mm tape
- · Low profile package
- Non-diffused lens: excellent for coupling to light pipes and backlighting
- Low power consumption
- Luminous intensity ratio in one packaging unit I_{Vmax}/I_{Vmin.} ≤ 1.6
- Lead (Pb)-free device
- Preconditioning: according to JEDEC level 2a
- Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC
- ESD-withstand voltage: up to 2 kV according to JESD22-A114-B

APPLICATIONS

- Automotive: dashboards, switches and optical indicators
- Telecommunication: indicator and backlighting in telephone and fax
- Indicator and backlight in office equipment
- Flat backlight for LCDs, switches and symbols
- · General use

PARTS TABLE			
PART	COLOR, LUMINOUS INTENSITY	TECHNOLOGY	
VLMKG3400-GS08	Super red, $I_V = (56 \text{ to } 140) \text{ mcd}$ Green, $I_V = (35.5 \text{ to } 90) \text{ mcd}$	AllnGaP on GaAs	
VLMKG3400-GS18	Super red, $I_V = (56 \text{ to } 140) \text{ mcd}$ Green, $I_V = (35.5 \text{ to } 90) \text{ mcd}$	AllnGaP on GaAs	

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ABSOLUTE MAXIMUM RATINGS 1) VLMKG3400						
PARAMETER	TEST CONDITION		SYMBOL	VALUE	UNIT	
Reverse voltage per diode ²⁾	I _R = 10 μA	1	V _R	5	V	
DC Forward current per diode	T _{amb} ≤ 80 °C	1 chip on	I _F	30	mA	
Surge forward current per diode		-	I _{FSM}	0.1	Α	
Power dissipation per diode			P _V	80	mW	
Junction temperature			T _j	125	°C	
Operating temperature range			T _{amb}	- 40 to + 100	°C	
Storage temperature range			T _{stg}	- 40 to + 100	°C	
Thermal resistance junction/ ambient	mounted on PC board (pad size > 16 mm ²)	1 chip on 2 chips on	R _{thJA}	560 780	K/W	

Note:

- T_{amb} = 25 °C, unless otherwise specified
 Driving the LED in reverse direction is suitable for short term application

OPTICAL AND ELECTRICAL CHARACTERISTICS 1) VLMKG3400, SUPERRED							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
Luminous intensity	I _F = 20 mA	VLMKG3400	I _V	56		140	mcd
Dominant wavelength	I _F = 20 mA		λ_{d}	627	633	639	nm
Peak wavelength	I _F = 20 mA		λ_{p}		643		nm
Angle of half intensity	I _F = 20 mA		φ		± 60		deg
Forward voltage	I _F = 20 mA		V _F		1.9	2.6	V
Reverse current	V _R = 5 V,		I _R			10	μΑ
Junction capacitance	V _R = 0, f = 1 MHz		C _j		15		pF

Note:

¹⁾ $T_{amb} = 25$ °C, unless otherwise specified

OPTICAL AND ELECTRICAL CHARACTERISTICS 1) VLMKG3400, GREEN							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
Luminous intensity	I _F = 20 mA	VLMKG3400	I _V	35.5		90	mcd
Dominant wavelength	I _F = 20 mA		λ_{d}	564	570	575	nm
Peak wavelength	I _F = 20 mA		λ_{p}		572		nm
Angle of half intensity	I _F = 20 mA		φ		± 60		deg
Forward voltage	I _F = 20 mA		V _F		2.0	2.6	V
Reverse current	V _R = 5 V		I _R			10	μΑ
Junction capacitance	V _R = 0, f = 1 MHz		C _j		15		pF

Note:

¹⁾ $T_{amb} = 25$ °C, unless otherwise specified

CROSSING TABLE	
VISHAY	OSRAM
VLMKG3400	LSGT676



L	LUMINOUS INTENSITY CLASSIFICATION AND GROUP COMBINATIONS, VLMKG3400						
		SUPER RED					
		P2 56 to 71 mcd	Q1 71 to 90 mcd	Q2 90 to 112 mcd	R1 112 to 140 mcd		
	N2 35.5 to 45 mcd	VLMKG3400	VLMKG3400	VLMKG3400	VLMKG3400		
G R E	P1 45 to 56 mcd	VLMKG3400	VLMKG3400	VLMKG3400	VLMKG3400		
E	P2 56 to 71 mcd	VLMKG3400	VLMKG3400	VLMKG3400	VLMKG3400		
	Q1 71 to 90 mcd	VLMKG3400	VLMKG3400	VLMKG3400	VLMKG3400		

Note:

Luminous intensity is tested at a current pulse duration of 25 ms and an accuracy of \pm 11 %.

The above type numbers represent the order groups which include only a few brightness groups. Only one group will be shipped on each reel (there will be no mixing of two groups on each reel).

In order to ensure availability, single brightness groups will not be orderable.

In a similar manner for colors where wavelength groups are measured and binned, single wavelength groups will be shipped in any one reel. In order to ensure availability, single wavelength groups will not be orderable.

COLOR CLASSIFICATION					
	DOMINANT WA	VELENGTH (nm)			
GROUP	ROUP GREEN				
	MIN.	MAX.			
4	564	567			
5	566	569			
6	568	571			
7	570	573			
8	572	575			

Note:

Wavelengths are tested at a current pulse duration of 25 ms and an accuracy of $\pm\,1\,$ nm.

TYPICAL CHARACTERISTICS

T_{amb} = 25 °C, unless otherwise specified

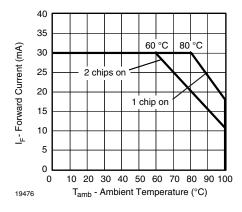


Figure 1. Forward Current vs. Ambient Temperature

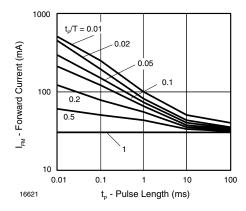


Figure 2. Forward Current vs. Pulse Duration

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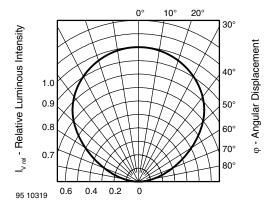


Figure 3. Rel. Luminous Intensity vs. Angular Displacement

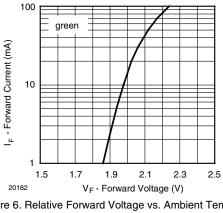


Figure 6. Relative Forward Voltage vs. Ambient Temperature

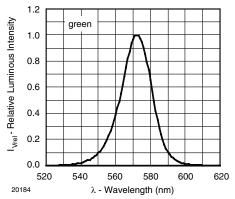


Figure 4. Relative Intensity vs. Wavelength

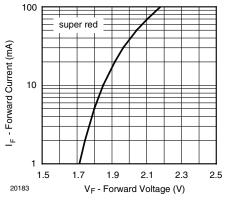


Figure 7. Relative Forward Voltage vs. Ambient Temperature

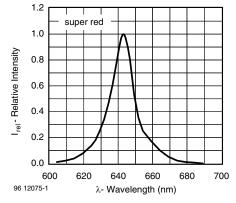


Figure 5. Relative Intensity vs. Wavelength

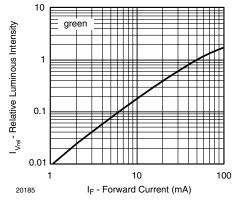


Figure 8. Relative Luminous Intensity vs. Forward Current

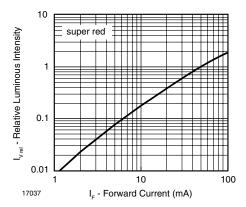


Figure 9. Relative Luminous Intensity vs. Forward Current

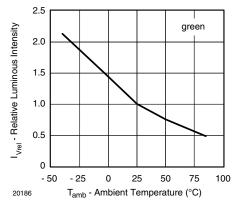


Figure 10. Rel. Luminous Intensity vs. Ambient Temperature

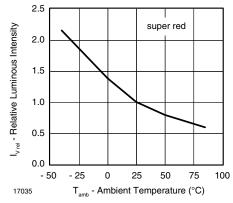


Figure 11. Rel. Luminous Intensity vs. Ambient Temperature

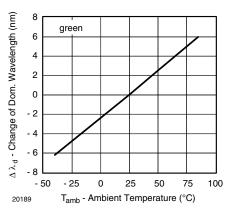


Figure 12. Change of Dominant Wavelength vs.
Ambient Temperature

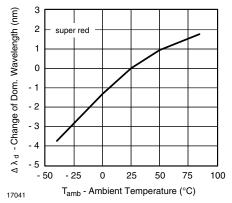


Figure 13. Change of Dominant Wavelength vs.

Ambient Temperature

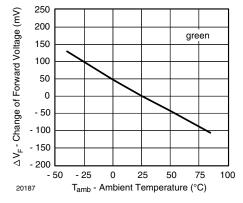


Figure 14. Change of Forward Voltage vs. Ambient Temperature

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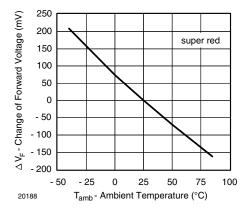
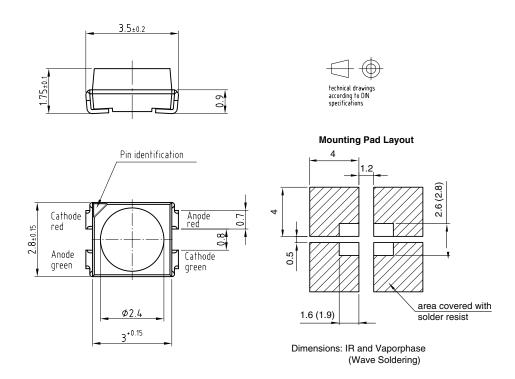


Figure 15. Change of Forward Voltage vs. Ambient Temperature

PACKAGE DIMENSIONS in millimeters



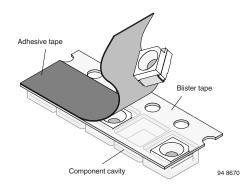
Drawing-No.: 6.541-5057.02-4 Issue: 2; 30.05.07 20190

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METHOD OF TAPING/POLARITY AND TAPE AND REEL

SMD LED (VLM.3 - SERIES)

Vishay's LEDs in SMD packages are available in an antistatic 8 mm blister tape (in accordance with DIN IEC 40 (CO) 564) for automatic component insertion. The blister tape is a plastic strip with impressed component cavities, covered by a top tape.



TAPING OF VLM.3...

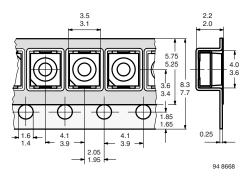


Figure 16. Tape Dimensions in mm for PLCC-2

REEL PACKAGE DIMENSION IN MILLIMETERS FOR SMD LEDS, TAPE OPTION GS08 (= 1500 PCS.)

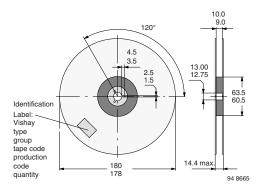


Figure 17. Reel Dimensions - GS08

REEL PACKAGE DIMENSION IN MILLIMETERS FOR SMD LEDS, TAPE OPTION GS18 (= 8000 PCS.) PREFERRED

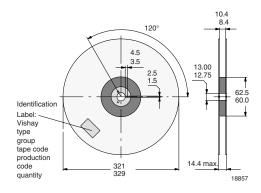


Figure 18. Reel Dimensions - GS18

SOLDERING PROFILE

IR Reflow Soldering Profile for lead (Pb)-free soldering Preconditioning acc. to JEDEC Level 2a

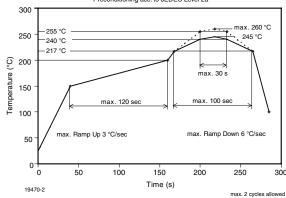


Figure 19. Vishay Lead (Pb)-free Reflow Soldering Profile (acc. to J-STD-020C)

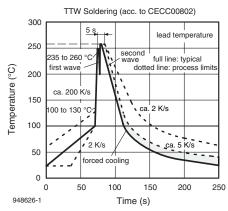
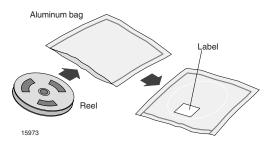


Figure 20. Double Wave Soldering of Opto Devices (all Packages)

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DRY PACKING

The reel is packed in an anti-humidity bag to protect the devices from absorbing moisture during transportation and storage.



FINAL PACKING

The sealed reel is packed into a cardboard box. A secondary cardboard box is used for shipping purposes.

RECOMMENDED METHOD OF STORAGE

Dry box storage is recommended as soon as the aluminium bag has been opened to prevent moisture absorption. The following conditions should be observed, if dry boxes are not available:

- Storage temperature 10 °C to 30 °C
- Storage humidity ≤ 60 % RH max.

After more than 672 h under these conditions moisture content will be too high for reflow soldering.

In case of moisture absorption, the devices will recover to the former condition by drying under the following condition:

192 h at $40 \,^{\circ}\text{C} + 5 \,^{\circ}\text{C/-} 0 \,^{\circ}\text{C}$ and $< 5 \,^{\circ}\text{KH}$ (dry air/ nitrogen) or

96 h at 60 °C + 5 °C and < 5 % RH for all device containers or

24 h at 100 °C + 5 °C not suitable for reel or tubes.

An EIA JEDEC standard JESD22-A112 level 2a label is included on all dry bags.





Example of JESD22-A112 level 2a label

ESD PRECAUTION

Proper storage and handling procedures should be followed to prevent ESD damage to the devices especially when they are removed from the antistatic shielding bag. Electro-static sensitive devices warning labels are on the packaging.

VISHAY SEMICONDUCTORS STANDARD BAR CODE LABELS

The Vishay Semiconductors standard bar code labels are printed at final packing areas. The labels are on each packing unit and contain Vishay Semiconductors specific data.

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Ozone Depleting Substances Policy Statement

It is the policy of Vishay Semiconductor GmbH to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively.
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA.
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design and may do so without further notice.

Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use Vishay Semiconductors products for any unintended or unauthorized application, the buyer shall indemnify Vishay Semiconductors against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

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