



MOTOROLA

查询"10576/BFAJC"供应商

Hex "D" Master-Slave Flip-Flop

ELECTRICALLY TESTED PER:

JM 38510/06103

The 10576 contains six high-speed, master slave type "D" flip-flops. Clocking is common to all six flip-flops. Data is entered into the master when the clock is low. Master to slave data transfer takes place on the positive-going Clock transition. Thus, outputs may change only on a positive-going Clock transition. A change in the information present at the data (D) input will not affect the output information any other time due to the master-slave construction of this device.

- 630 mW Max/Pkg (No Load)
- $f_{\text{toggle}} = 150 \text{ MHz (typ)}$
- $t_r, t_f = 2.0 \text{ ns typ (20\% - 80\%)}$

3

PIN ASSIGNMENTS

FUNCTION	DIL	FLATS	LCC	BURN-IN (CONDITION C)
VCC1	1	5	2	GND
Q0	2	6	3	51 Ω to V _{TT}
Q1	3	7	4	51 Ω to V _{TT}
Q2	4	8	5	51 Ω to V _{TT}
D0	5	9	7	GND
D1	6	10	8	GND
D2	7	11	9	GND
VEE	8	12	10	VEE
Clock	9	13	12	CP1
D3	10	14	13	GND
D4	11	15	14	GND
D5	12	16	15	GND
Q3	13	1	17	51 Ω to V _{TT}
Q4	14	2	18	51 Ω to V _{TT}
Q5	15	3	19	51 Ω to V _{TT}
VCC2	16	4	20	GND

BURN - IN CONDITIONS:

V_{TT} = - 2.0 V MAX/ - 2.2 V MIN

VEE = - 5.7 V MAX/ - 5.2 V MIN

TRUTH TABLE

C	D	Q _{n+1}
L	$\emptyset$	Q _n
* H	L	L
* H	H	H

$\emptyset$ = Don't Care

* A clock H is a clock transition from a Low to a High state

Military 10576



AVAILABLE AS

1) JAN: JM 38510/06103

2) SMD: N/A

3) 883: 10576/BXAJC

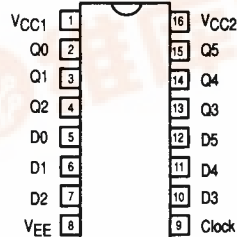
X = CASE OUTLINE AS FOLLOWS:

PACKAGE: CERDIP: E

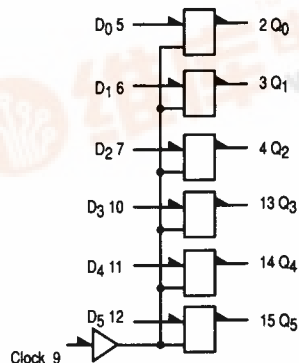
CERFLAT: F

LCC: 2

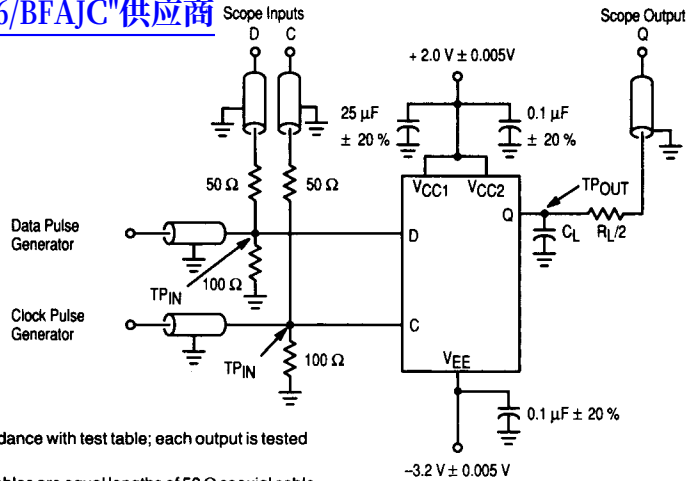
The letter "M" appears before the slash on LCC.



LOGIC DIAGRAM



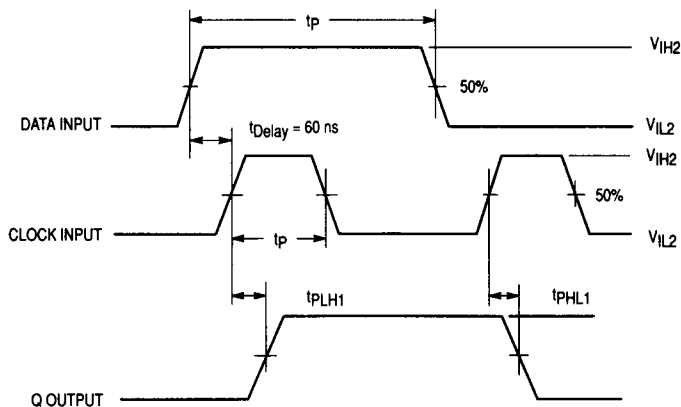
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NOTES

1. Perform test in accordance with test table; each output is tested separately.
2. All input and output cables are equal lengths of 50 Ω coaxial cable. Wire length should be ≤ 0.25 inch (6.35 mm) from TP_{IN} to input pin and TP_{OUT} to output pin.
3. Output not under test connected to a 100 Ω resistor to ground.
4. Note that observed pulse amplitude is attenuated by one half.
5. $R_{L/2} = 50 \Omega \pm 5.0\%$.
6. Scope input = 50 Ω to ground.
7. C_L (test jig) ≤ 5.0 pF.
8. $Z_{OUT} = 50 \Omega$.

Figure 1. Synchronous Switching Test Circuit



NOTES

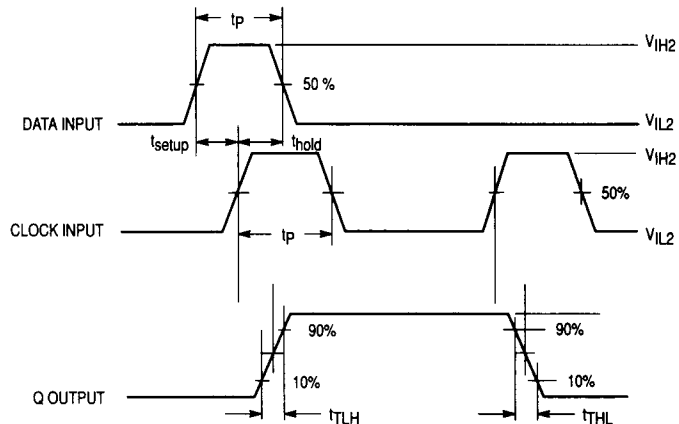
1. Note that observed pulse amplitude is attenuated by one half.
2. $t_p(\text{data}) = 150$ ns.
3. $t_p(\text{clock}) = 40$ ns.
4. $PRR = 1.0$ MHz.
5. $t_{THL} = t_{TLH} = 2.0$ ns (20% to 80%).

Figure 2. Synchronous Switching Test Circuit Waveforms



1. Perform test in accordance with test table; each output is tested separately.
2. All input and output cables are equal lengths of 50 Ω coaxial cable. Wire length should be ≤ 0.25 inch (6.35 mm) from TP_{IN} to input pin and TP_{OUT} to output pin.
3. Output not under test connected to a 100 Ω resistor to ground.
4. Note that observed pulse amplitude is attenuated by one half.
5. $R_L/2 = 50 \Omega \pm 5.0\%$.
6. Scope input = 50 Ω to ground.
7. C_L (test jig) ≤ 5.0 pF.
8. $Z_{OUT} = 50 \Omega$.

Figure 3. Setup and Hold Test Circuit



NOTES

1. Note that observed pulse amplitude is attenuated by one half.
2. $t_p(\text{data}) = 40 \text{ ns}$.
3. $t_p(\text{clock}) = 40 \text{ ns}$.
4. $\text{PRR} = 1.0 \text{ MHz}$.
5. $t_{\text{THL}} = t_{\text{TLH}} = 2.0 \text{ ns}$ (20% to 80%).

Figure 4. t_{SETUP} and t_{HOLD} Waveforms

10576 QUIESCENT LIMIT TABLE *

* ELECTRICAL CHARACTERISTICS

Each MECL 10K series circuit has been designed to meet the dc specifications shown in the test table, after thermal equilibrium has been established. The circuit is in a test socket or mounted on a printed circuit board and transverse air flow greater than 500 linear fpm is maintained. Outputs are terminated through a 100 Ω resistor to - 2.0 volts.

Test Temperature	Test Voltage Values (Volts)							
	V _{IH}	V _{IL}	V _{IH1}	V _{ITL}	V _{IH1}	V _{IL1}	V _{EE}	V _{VEEL}
T _A = 25 °C	-0.78	-1.85	-1.105	-1.475	+1.11	+0.31	-5.2	-3.2
T _A = 125 °C	-0.63	-1.82	-1.000	-1.400	+1.24	+0.36	-5.2	-3.2
T _A = - 55 °C	-0.88	-1.92	-1.255	-1.510	+1.01	+0.28	-5.2	-3.2

Symbol	Parameter	Limits								Units	TEST VOLTAGE APPLIED TO PINS BELOW							
		+ 25 °C		+ 125 °C		- -55 °C					Pinouts referenced are for DIL package, check Pin Assignments VCC = 0 V, Output Load = 100 Ω to - 2.0 V							
		Subgroup 1		Subgroup 2		Subgroup 3												
		Min	Max	Min	Max	Min	Max		V _{IH1}	V _{IL1}	V _{IH}	V _{ITL}	Clk	V _{EE}	V _{CC}	P. U. T.		
VOH	High Output Voltage	- 0.93	- 0.78	- 0.825	- 0.63	- 1.08	- 0.88	V	5 - 7, 10 - 12				9	8	16	2 - 4, 13 - 15		
VOL	Low Output Voltage	- 1.85	- 1.62	- 1.82	- 1.545	- 1.92	- 1.655	V		5 - 7, 10 - 12				8	16	2 - 4, 13 - 15		
VOTL	Low Output Voltage		- 1.60		- 1.525		- 1.635	V	6, 7, 10 - 12	6, 7, 10 - 12	6, 7, 10 - 12	5 - 7, 10 - 12	9	8	16	2 - 4, 13 - 15		
VOTH	High Output Voltage	- 0.95		- 0.845		- 1.10		V	5 - 7, 10 - 12	5 - 7, 10 - 12		5 - 7, 10 - 12	9	8	16	2 - 4, 13 - 15		
I _{IH1}	Input Current High		220		375		375	μA	5 - 7, 10 - 12					8	16	5 - 7, 10 - 12		
I _{IH2}	Input Current High		310		527		527	μA	9					8	16	9		
I _{IL}	Input Current Low	0.5		0.3		0.5		μA		5 - 7, 10 - 12				8	16	5 - 7, 10 - 12		
I _{EE}	Power Supply Drain Current	- 110		- 121		- 121		mA						8	16	8		

10576 QUIESCENT LIMIT TABLE *

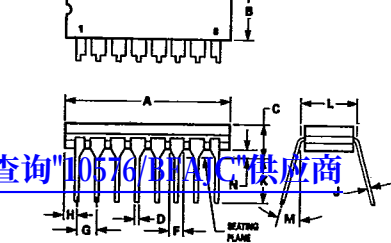
* ELECTRICAL CHARACTERISTICS

Each MECL 10K series circuit has been designed to meet the dc specifications shown in the test table, after thermal equilibrium has been established. The circuit is in a test socket or mounted on a printed circuit board and transverse air flow greater than 500 linear fpm is maintained. Outputs are terminated through a 100 Ω resistor to 0.0 volts.

Test Temperature	Test Voltage Values (Volts)							
	V _{IH}	V _{IL}	V _{TH}	V _{TL}	V _{IH1}	V _{IL1}	V _{EE}	V _{EEL}
T _A = 25 °C	-0.78	-1.85	-1.105	-1.475	+1.11	+0.31	-5.2	-3.2
T _A = 125 °C	-0.63	-1.82	-1.000	-1.400	+1.24	+0.36	-5.2	-3.2
T _A = -55 °C	-0.88	-1.92	-1.255	-1.510	+1.01	+0.28	-5.2	-3.2

Symbol	Parameter	Limits							Units	TEST VOLTAGE APPLIED TO PINS BELOW				
Functional Parameters:		+ 25 °C		+ 125 °C		- 55 °C				Pinouts referenced are for DIL package, check Pin Assignments VCC = 2.0 V, Output Load = 100 Ω to GND				
		Subgroup 9		Subgroup 10		Subgroup 11								
		Min	Max	Min	Max	Min	Max							
t _{TLH}	Rise Time	1.1	4.0	1.0	4.5	1.0	4.3	ns	VIN	VOUT	VCC	VEEL	P. U. T.	
t _{FHL}	Fall Time	1.1	4.0	1.0	4.5	1.0	4.3	ns	5 - 7, 10 - 12	2 - 4, 13 - 15	16	8	5 - 7, 10 - 12	
t _{PLH}	Propagation Delay	1.5	4.5	1.3	5.3	1.2	4.9	ns	5 - 7, 10 - 12	2 - 4, 13 - 15	16	8	5 - 7, 10 - 12	
t _{PLH}	Propagation Delay	1.5	4.5	1.3	5.3	1.2	4.9	ns	5 - 7, 10 - 12	2 - 4, 13 - 15	16	8	5 - 7, 10 - 12	
f _{Toggle}	Toggle Frequency	125		125		115		MHz	5 - 7, 10 - 12	2 - 4, 13 - 15	16	8	5 - 7, 10 - 12	

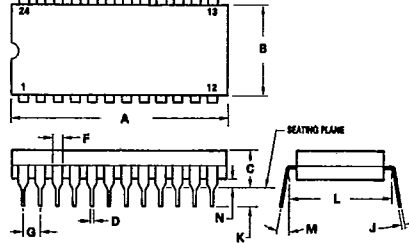
查询"10516/B1A/C"供应商



DIM	MIN	MAX	MIN	MAX
A	19.05	19.81	0.750	0.780
B	6.22	6.98	0.245	0.275
C	4.06	5.08	0.160	0.200
D	0.38	0.51	0.015	0.020
F	1.40	1.65	0.055	0.065
G	2.54 BSC		0.100 BSC	
H	0.51	1.14	0.020	0.045
J	0.20	0.30	0.008	0.012
K	3.18	4.06	0.125	0.160
L	7.37	7.87	0.290	0.310
M	—	15°	—	15°
N	0.51	1.02	0.020	0.040

NOTES:

- LEADS WITHIN 0.13 mm (0.005) RADIUS OF TRUE POSITION AT SEATING PLANE AT MAXIMUM MATERIAL CONDITION.
- PACKAGE INDEX: NOTCH IN LEAD NOTCH IN CERAMIC OR INK DOT.
- DIM "L" TO CENTER OF LEADS WHEN FORMED PARALLEL.

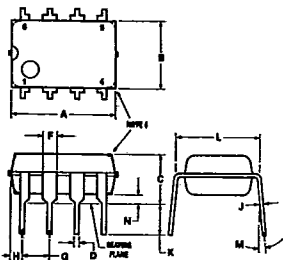


DIM	MIN	MAX	MIN	MAX
A	31.24	32.77	1.230	1.290
B	12.70	15.49	0.500	0.610
C	4.06	5.59	0.160	0.220
D	0.41	0.51	0.016	0.020
F	1.27	1.52	0.050	0.060
G	2.54 BSC		0.100 BSC	
J	0.20	0.30	0.008	0.012
K	3.18	4.06	0.125	0.160
L	15.24 BSC		0.600 BSC	
M	0°	15°	0°	15°
N	0.51	1.27	0.020	0.050

NOTES:

- DIM "L" TO CENTER OF LEADS WHEN FORMED PARALLEL.
- LEADS WITHIN 0.13 mm (0.005) RADIUS OF TRUE POSITION AT SEATING PLANE AT MAXIMUM MATERIAL CONDITION. (WHEN FORMED PARALLEL).

P SUFFIX
PLASTIC PACKAGE
CASE 626-04

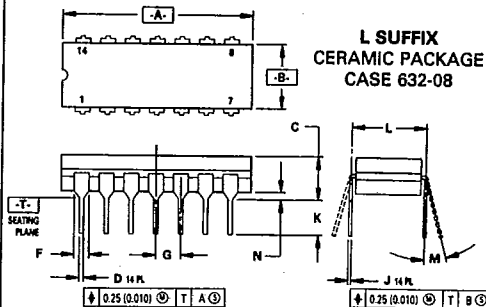


DIM	MIN	MAX	MIN	MAX
A	9.40	10.16	0.370	0.400
B	6.10	6.60	0.240	0.260
C	3.94	4.45	0.155	0.175
D	0.38	0.51	0.015	0.020
F	1.02	1.52	0.040	0.060
G	2.54 BSC		0.100 BSC	
H	0.76	1.27	0.030	0.050
J	0.20	0.30	0.008	0.012
K	2.92	3.43	0.115	0.135
L	7.62 BSC		0.300 BSC	
M	—	10°	—	10°
N	0.51	0.76	0.020	0.030

NOTES:

- LEAD POSITIONAL TOLERANCE:
 $\phi 0.13 (0.005) \text{ T A } \text{B } \text{C}$
- DIMENSION L TO CENTER OF LEADS WHEN FORMED PARALLEL.
- PACKAGE CONTOUR OPTIONAL (ROUND OR SQUARE CORNERS).
- DIMENSIONS A AND B ARE DATUMS.
- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

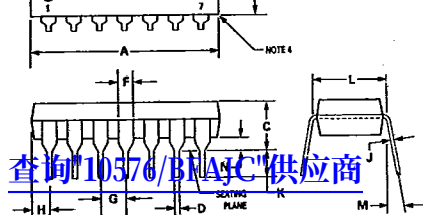
L SUFFIX
CERAMIC PACKAGE
CASE 632-08



DIM	MIN	MAX	MIN	MAX
A	19.05	19.94	0.750	0.785
B	6.23	7.11	0.245	0.280
C	3.94	5.08	0.155	0.200
D	0.38	0.50	0.015	0.020
F	1.40	1.65	0.055	0.065
G	2.54 BSC		0.100 BSC	
J	0.21	0.38	0.008	0.015
K	3.18	4.31	0.125	0.170
L	7.62 BSC		0.300 BSC	
M	0°	15°	0°	15°
N	0.51	1.01	0.020	0.040

NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- CONTROLLING DIMENSION: INCH.
- DIMENSION L TO CENTER OF LEAD WHEN FORMED PARALLEL.
- DIM F MAY NARROW TO 0.76 (0.030) WHERE THE LEAD ENTERS THE CERAMIC BODY.

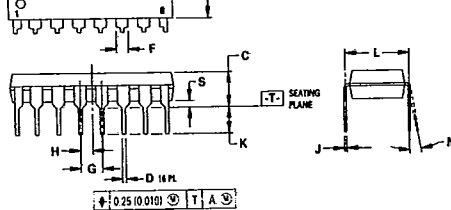


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DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	18.16	19.56	0.715	0.770
B	6.10	6.60	0.240	0.260
C	3.69	4.69	0.145	0.185
D	0.38	0.53	0.015	0.021
E	1.02	1.78	0.040	0.070
F	2.54 BSC		0.100 BSC	
G	1.32	2.41	0.052	0.095
H	0.20	0.38	0.008	0.015
J	2.92	3.43	0.115	0.135
K	7.62 BSC		0.300 BSC	
L	0°	10°	0°	10°
M	0.39	1.01	0.015	0.039

NOTES:

- LEADS WITHIN 0.13 mm (0.005) RADIUS OF TRUE POSITION AT SEATING PLANE AT MAXIMUM MATERIAL CONDITION.
- DIMENSION "L" TO CENTER OF LEADS WHEN FORMED PARALLEL.
- DIMENSION "B" DOES NOT INCLUDE MOLD FLASH.
- ROUNDED CORNERS OPTIONAL.

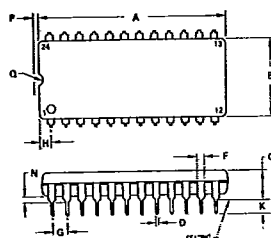


DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	18.80	19.55	0.740	0.770
B	6.35	6.85	0.250	0.270
C	3.69	4.44	0.145	0.175
D	0.39	0.53	0.015	0.021
E	1.02	1.77	0.040	0.070
F	2.54 BSC		0.100 BSC	
G	1.27 BSC		0.050 BSC	
H	0.21	0.38	0.008	0.015
J	2.80	2.90	0.110	0.130
K	7.50	7.74	0.295	0.305
L	0°	10°	0°	10°
S	0.51	1.01	0.020	0.040

NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- CONTROLLING DIMENSION: INCH.
- DIMENSION "L" TO CENTER OF LEADS WHEN FORMED PARALLEL.
- DIMENSION "B" DOES NOT INCLUDE MOLD FLASH.
- ROUNDED CORNERS OPTIONAL.

P SUFFIX PLASTIC PACKAGE CASE 649-03

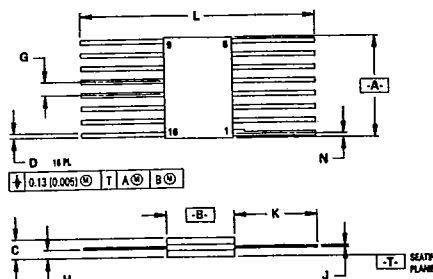


DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	31.50	32.13	1.240	1.265
B	13.21	13.72	0.520	0.540
C	4.70	5.21	0.185	0.205
D	0.38	0.51	0.015	0.020
E	1.02	1.52	0.040	0.060
F	2.54 BSC		0.100 BSC	
G	1.65	2.16	0.065	0.085
H	0.20	0.30	0.008	0.012
J	2.92	3.43	0.115	0.135
K	14.99	15.49	0.590	0.610
L	—	10°	—	10°
M	0.51	1.02	0.020	0.040
N	0.13	0.38	0.005	0.015
O	0.51	0.76	0.020	0.030

NOTES:

- LEADS WITHIN 0.13 mm (0.005) RADIUS OF TRUE POSITION AT SEATING PLANE AT MAXIMUM MATERIAL CONDITION.
- DIMENSION "L" TO CENTER OF LEADS WHEN FORMED PARALLEL.

F SUFFIX CERAMIC PACKAGE CASE 650-05



DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	9.40	9.90	0.370	0.390
B	6.73	6.80	0.245	0.260
C	1.53	2.15	0.060	0.085
D	0.36	0.40	0.014	0.016
E	1.27 BSC		0.050 BSC	
F	0.64	0.01	0.025	0.040
G	0.11	0.17	0.004	0.007
H	6.35	9.39	0.250	0.370
I	18.93	—	0.745	—
N	—	0.50	—	0.020

NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- CONTROLLING DIMENSION: INCH.
- DIMENSION "A" AND "B" ALLOW FOR LID MISALIGNMENT, AND GLASS MENISCUS.
- DIMENSION "H" SHALL BE MEASURED AT THE POINT OF EXIT OF THE LEAD FROM THE BODY.
- LEAD NUMBER 1 IDENTIFIED BY TAB ON LEAD OR DOT ON COVER.
- DIMENSION "J" INCLUDES SOLDER LEAD FINISH.
- LEAD NUMBERS SHOWN FOR REFERENCE ONLY.

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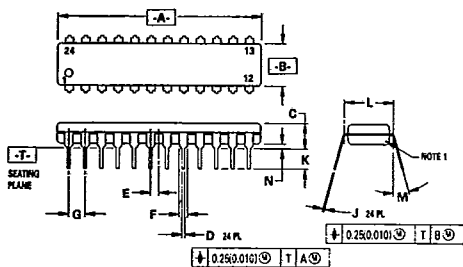
	MILLIMETERS		INCHES	
DIM	MIN	MAX	MIN	MAX
A	0.91	10.92	0.360	0.430
B	6.32	6.39	0.245	0.275
C	4.32	5.06	0.170	0.200
D	0.41	0.51	0.016	0.020
F	1.40	1.65	0.055	0.065
G	2.54 BSC		0.100 BSC	
H	1.14	1.65	0.045	0.065
J	0.20	0.30	0.008	0.012
K	3.18	4.06	0.125	0.160
L	7.37	7.87	0.290	0.310
M	—	15°	—	15°
N	0.51	1.02	0.020	0.040

- NOTES:
- LEADS WITHIN 0.13 mm (0.005) RAD OF TRUE POSITION AT SEATING PLANE AT MAXIMUM MATERIAL CONDITION.
 - DIMENSION "L" TO CENTER OF LEADS WHEN FORMED PARALLEL.

	MILLIMETERS		INCHES	
DIM	MIN	MAX	MIN	MAX
A	22.72	23.24	0.915	0.915
B	6.10	6.60	0.240	0.260
C	3.56	4.57	0.140	0.180
D	0.56	0.56	0.014	0.022
F	1.27	1.78	0.050	0.070
G	2.54 BSC		0.100 BSC	
H	1.02	1.52	0.040	0.060
J	0.20	0.30	0.008	0.012
K	2.92	3.43	0.115	0.135
L	7.62 BSC		0.300 BSC	
M	0°	15°	0°	15°
N	0.51	1.02	0.020	0.040

- NOTES:
- POSITIONAL TOLERANCE OF LEADS (D), SHALL BE WITHIN 0.25mm(0.010) AT MAXIMUM MATERIAL CONDITION, IN RELATION TO SEATING PLANE AND EACH OTHER.
 - DIMENSION L TO CENTER OF LEADS WHEN FORMED PARALLEL.
 - DIMENSION B DOES NOT INCLUDE MOLD FLASH.

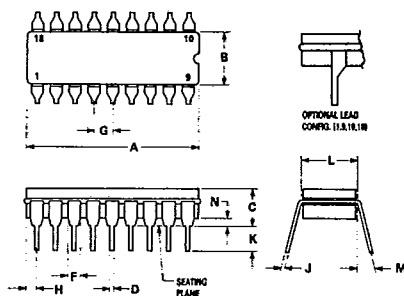
P SUFFIX PLASTIC PACKAGE CASE 724-03



	MILLIMETERS		INCHES	
DIM	MIN	MAX	MIN	MAX
A	31.25	32.13	1.230	1.265
B	6.35	6.86	0.250	0.270
C	3.69	4.44	0.145	0.175
D	0.38	0.51	0.015	0.020
E	1.27 BSC		0.050 BSC	
F	1.02	1.52	0.040	0.060
G	2.54 BSC		0.100 BSC	
H	0.18	0.30	0.007	0.012
J	0.18	0.30	0.007	0.012
K	2.80	3.55	0.110	0.140
L	7.62 BSC		0.300 BSC	
M	0°	15°	0°	15°
N	0.51	1.01	0.020	0.040

- NOTES:
- CHAMFERED CONTOUR OPTIONAL.
 - DIM "L" TO CENTER OF LEADS WHEN FORMED PARALLEL.
 - DIMENSIONS AND TOLERANCES PER ANSI Y14.5M, 1987.
 - CONTROLLING DIMENSION: INCH.

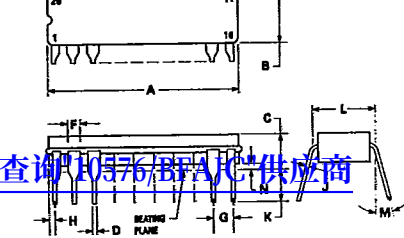
L SUFFIX CERAMIC PACKAGE CASE 726-04



	MILLIMETERS		INCHES	
DIM	MIN	MAX	MIN	MAX
A	22.75	23.11	0.900	0.910
B	6.10	7.49	0.240	0.295
C	—	5.08	—	0.200
D	0.38	0.53	0.015	0.021
F	1.40	1.78	0.055	0.070
G	2.54 BSC		0.100 BSC	
H	0.51	1.14	0.020	0.045
J	0.20	0.30	0.008	0.012
K	3.18	4.32	0.125	0.170
L	7.62 BSC		0.300 BSC	
M	0°	15°	0°	15°
N	0.51	1.02	0.020	0.040

- NOTES:
- LEADS, TRUE POSITIONED WITHIN 0.25 mm (0.010) DIA. AT SEATING PLANE, AT MAXIMUM MATERIAL CONDITION.
 - DIM "L" TO CENTER OF LEADS WHEN FORMED PARALLEL.
 - DIM "A" & "B" INCLUDES MENISCUS.
 - "F" DIMENSION IS FOR FULL LEADS. "HALF" LEADS ARE OPTIONAL AT LEAD POSITIONS 1, 9, 10, AND 18.

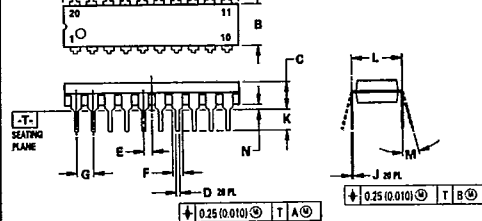
查询 10376 BT A/C 供应商



DIM	MIN	MAX	MIN	MAX
A	23.88	25.15	0.940	0.990
B	6.80	7.49	0.269	0.295
C	3.81	5.08	0.150	0.200
D	0.38	0.56	0.015	0.022
F	1.40	1.65	0.055	0.065
G	2.54 BSC		0.100 BSC	
H	0.51	1.27	0.020	0.050
J	0.20	0.30	0.008	0.012
K	3.18	4.06	0.125	0.160
L	7.62 BSC		0.300 BSC	
M	0°	15°	0°	15°
N	0.25	1.02	0.010	0.040

NOTES:

- LEADS WITHIN 0.25 mm (0.010) DIA., TRUE POSITION AT SEATING PLANE, AT MAXIMUM MATERIAL CONDITION.
- DIM L TO CENTER OF LEADS WHEN FORMED PARALLEL.
- DIM A AND B INCLUDES MENISCUS.

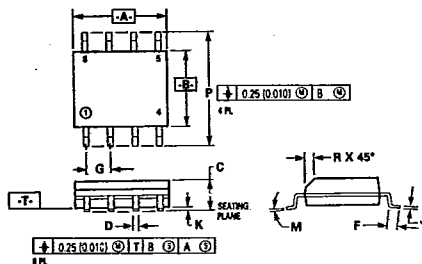


DIM	MIN	MAX	MIN	MAX
A	25.66	27.17	1.010	1.070
B	6.10	6.80	0.240	0.269
C	3.81	4.57	0.150	0.180
D	0.39	0.55	0.015	0.022
E	1.27 BSC		0.050 BSC	
F	1.27	1.77	0.050	0.070
G	2.54 BSC		0.100 BSC	
J	0.21	0.38	0.008	0.015
K	2.80	3.55	0.110	0.140
L	7.62 BSC		0.300 BSC	
M	0°	15°	0°	15°
N	0.51	1.01	0.020	0.040

NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- CONTROLLING DIMENSION: INCH.
- DIMENSION "L" TO CENTER OF LEAD WHEN FORMED PARALLEL.
- DIMENSION "B" DOES NOT INCLUDE MOLD FLASH.

D SUFFIX
PLASTIC SOIC PACKAGE
CASE 751-03

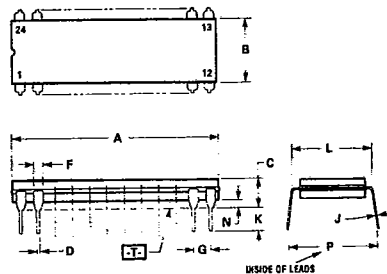


DIM	MIN	MAX	MIN	MAX
A	4.80	5.00	0.189	0.196
B	3.80	4.00	0.150	0.157
C	1.35	1.75	0.054	0.068
D	0.35	0.49	0.014	0.019
F	0.40	1.25	0.016	0.049
G	1.77 BSC		0.050 BSC	
J	0.18	0.25	0.007	0.009
K	0.10	0.25	0.004	0.009
M	0°	7°	0°	7°
P	5.80	6.20	0.228	0.244
R	0.25	0.50	0.010	0.019

NOTES:

- DIMENSIONS "A" AND "B" ARE DATUMS AND "T" IS A DATUM SURFACE.
- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- CONTROLLING DIM: MILLIMETER.
- DIMENSION "A" AND "B" DO NOT INCLUDE MOLD PROTRUSION.
- MAXIMUM MOLD PROTRUSION 0.15 (0.006) PER SIDE.

L SUFFIX
CERAMIC PACKAGE
CASE 758-01

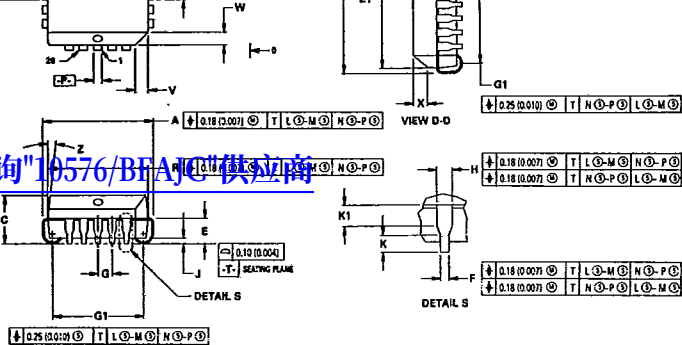


DIM	MIN	MAX	MIN	MAX
A	31.50	32.64	1.240	1.285
B	7.24	7.75	0.285	0.305
C	3.68	4.44	0.145	0.175
D	0.38	0.53	0.015	0.021
F	1.14	1.57	0.045	0.062
G	2.54 BSC		0.100 BSC	
J	0.20	0.33	0.008	0.013
K	2.54	4.19	0.100	0.165
L	7.62	7.87	0.300	0.310
N	0.51	1.27	0.020	0.050
P	9.14	10.16	0.350	0.400

NOTES:

- DIMENSION A IS DATUM.
- POSITIONAL TOLERANCE FOR LEADS: 24 PLACES $\pm 0.25 (0.010) \text{ T} \text{ A}$.
- [T] IS SEATING PLANE.
- DIMENSION L TO CENTER OF LEADS WHEN FORMED PARALLEL.
- DIMENSIONING AND TOLERANCING PER ANSI Y14.5, 1973.

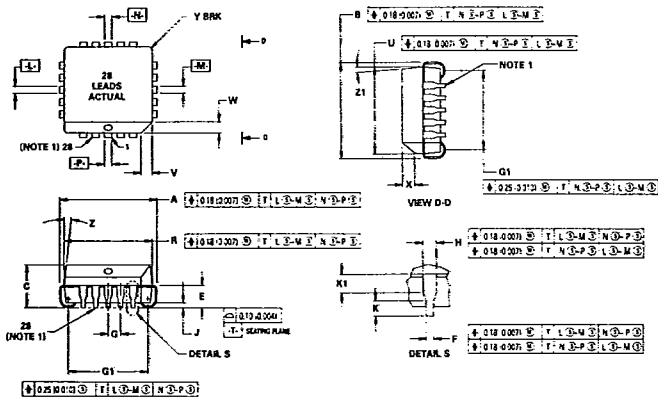
查询"10576/BFAJC"供应商



FN SUFFIX
PLASTIC PACKAGE
CASE 775-02

- IS 0.25 (0.010) PER SIDE.
4. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
5. CONTROLLING DIMENSION: INCH.

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	9.78	10.03	0.385	0.395
B	9.78	10.03	0.385	0.395
C	4.20	4.57	0.165	0.180
E	2.29	2.79	0.090	0.110
F	0.33	0.48	0.013	0.019
G	1.27 BSC		0.050 BSC	
H	0.66	0.81	0.026	0.032
J	0.51	—	0.020	—
K	0.64	—	0.025	—
R	8.89	9.04	0.350	0.356
U	8.89	9.04	0.350	0.356
V	1.07	1.21	0.042	0.048
W	1.07	1.21	0.042	0.048
X	1.07	1.42	0.042	0.056
Y	—	0.50	—	0.020
Z	2°	10°	2°	10°
G1	7.88	8.38	0.310	0.330
K1	1.02	—	0.040	—
Z1	2°	10°	2°	10°



FN SUFFIX
PLASTIC PACKAGE
CASE 776-02

- NOTES:
1. DUE TO SPACE LIMITATION, CASE 776-02 SHALL BE REPRESENTED BY A GENERAL (SMALLER) CASE OUTLINE DRAWING RATHER THAN SHOWING ALL 28 LEADS.
2. DATUMS -L-, -M-, -N-, AND -P- DETERMINED WHERE TOP OF LEAD SHOULDER EXIT PLASTIC BODY AT MOLD PARTING LINE.
3. DIM G1, TRUE POSITION TO BE MEASURED AT DATUM -T-, SEATING PLANE.
4. DIM R AND U DO NOT INCLUDE MOLD PROTRUSION; ALLOWABLE MOLD PROTRUSION IS 0.25 (0.010) PER SIDE.
5. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
6. CONTROLLING DIMENSION: INCH.

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	12.32	12.57	0.485	0.495
B	12.32	12.57	0.485	0.495
C	4.20	4.57	0.165	0.180
E	2.29	2.79	0.090	0.110
F	0.33	0.48	0.013	0.019
G	1.27 BSC		0.050 BSC	
H	0.66	0.81	0.026	0.032
J	0.51	—	0.020	—
K	0.64	—	0.025	—
R	11.43	11.58	0.450	0.456
U	11.43	11.58	0.450	0.456
V	1.07	1.21	0.042	0.048
W	1.07	1.21	0.042	0.048
X	1.07	1.42	0.042	0.056
Y	—	0.50	—	0.020
Z	2°	10°	2°	10°
G1	10.42	10.92	0.410	0.430
K1	1.02	—	0.040	—
Z1	2°	10°	2°	10°

Surface Mount Packages allow more optimum device performance with the smaller Surface Mount configuration. Internal lead lengths, parasitic capacitance and inductance that placed limitations on chip performance have been reduced.

The lower profile of Surface Mount Packages allows more boards to be utilized in a given amount of space. They are stacked closer together and utilize less total volume than insertion populated PC boards.

Printed circuit costs are lowered with the reduction of the number of board layers required. The elimination or reduction of the number of plated through holes in the board, contribute significantly to lower PC board prices.

Surface Mount assembly does not require the preparation of components that are common on insertion technology lines. Surface Mount components are sent directly to the assembly line, eliminating an intermediate step.

Automatic placement equipment is available that can place Surface Mount components at the rate of a few thousand per hour to hundreds of thousands of components per hour.

Surface Mount Technology is cost effective, allowing the manufacturer the opportunity to produce smaller units and offer increased functions with the same size product.

MECL AVAILABILITY IN SURFACE MOUNT

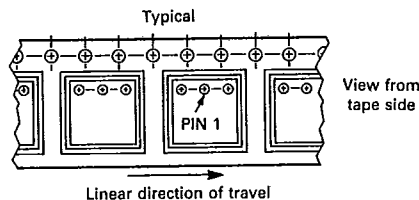
Motorola is now offering MECL 10K and MECL 10KH in the PLCC (Plastic Leaded Chip Carrier) packages.

MECL in PLCC may be ordered in conventional plastic rails or on Tape and Reel. Refer to the Tape and Reel section for ordering details.

TAPE AND REEL

Motorola has now added the convenience of Tape and Reel packaging for our growing family of standard Integrated Circuit products. The packaging fully conforms to

MECHANICAL POLARIZATION



ORDERING INFORMATION

- Minimum Lot Size/Device Type = 3000 Pieces.
- No Partial Reel Counts Available.
- To order devices which are to be delivered in Tape and Reel, add the appropriate suffix to the device number being ordered.

EXAMPLE:

ORDERING CODE

MC10100FN
MC10100FNR2
MC10H100FN
MC10H100FNR2
MC12015D
MC12015DR2

SHIPMENT METHOD

Magazines (Rails)
13 inch Tape and Reel
Magazines (Rails)
13 inch Tape and Reel
Magazines (Rails)
13 inch Tape and Reel

DUAL-IN-LINE PACKAGE TO PLCC PIN CONVERSION DATA

The following tables give the equivalent I/O pinouts of Dual-In-Line (DIL) packages and Plastic Leaded Chip Carrier (PLCC) packages.

Conversion Tables

16 PIN DIL	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
20 PIN PLCC	2	3	4	5	7	8	9	10	12	13	14	15	17	18	19	20

20 PIN DIL	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
20 PIN PLCC	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20

24 PIN DIL	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24
28 PIN PLCC	2	3	4	5	6	7	9	10	11	12	13	14	16	17	18	19	20	21	23	24	25	26	27	28