

Field Effect Transistor

Silicon N Channel MOS Type (t-MOS II)

High Speed, High Current Switching, DC-DC Converter,

Chopper Regulator and Motor Drive Applications

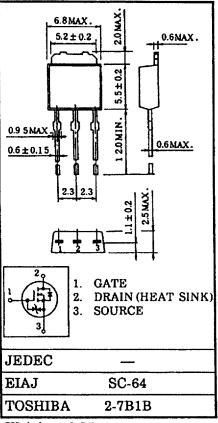
Features

- Low Drain-Source ON Resistance
- R_{DS(ON)} = 3.6Ω (Typ.) High Forward Transfer Admittance
 - $|Y_{fs}| = 0.62S$ (Typ.)
- Low Leakage Current
 - $-I_{DSS} = 300\mu A \text{ (Max.) } @ V_{DS} = 400V$
- Enhancement-Mode
 - V_{th} = 1.5 ~ 3.5V @ V_{DS} = 10V, I_D = 1mA

Absolute Maximum Ratings (Ta = 25C)

CHARACTERISTIC		SYMBOL	RATING	UNIT	
Drain-Source Voltage		V _{DSS}	400	٧	
Drain-Gate Voltage (R _S = 20kΩ)		V _{DGR}	400	٧	
Gate-Source Voltage		Vass	±20	٧	
Drain Current	DC	ь	1	Α	
	Pulse	I _{DP}	4		
Drain Power Dissipation (Tc = 25°C)		PD	10	W	
Channel Temperature		T _{th}	150	°C	
Storage Temperature Range		- J _{tg}	-55 ~ 150	°C	

Unit in mm



Weight: 0.35g

Thermal Characteristics

CHARACTERISTIC	SYMBOL	MAX.	UNIT	
Thermal Resistance, Channel to Case	R(ch-c)	12.5	°C/W	
Thermal Resistance, Channel to Ambient	fil(ch-a)	125	°C/W	

This transistor is an electrostatic sensitive device. Please handle with care.

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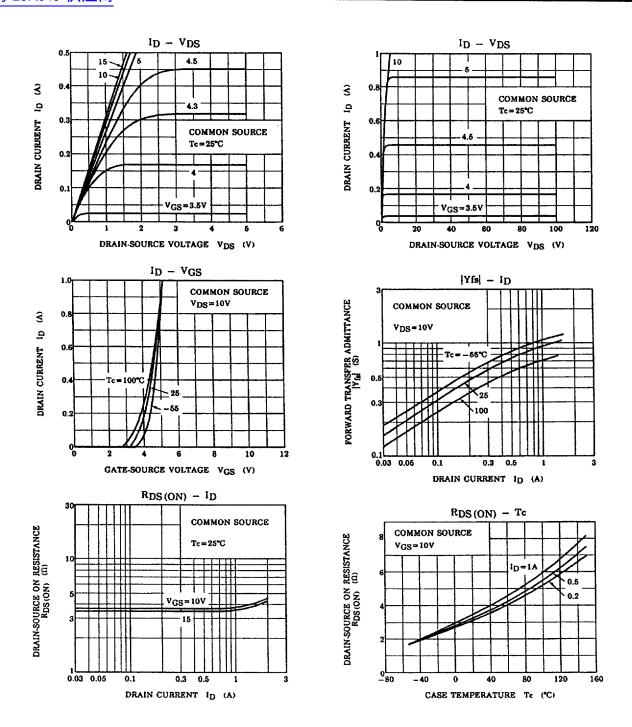
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Electrical Characteristics (Ta = 25C)

CHARACTERISTIC		SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	רואט
Gate Leakage Current		ess	$V_{GS} = \pm 20V$, $V_{DS} = 0V$	_		±100	nA
Drain Cut-off Current Drain-Source Breakdown Voltage		I _{DSS}	I _{DSS} V _{DS} = 400V, V _{GS} = 0V (6R) DSS I _D = 10mA, V _{GS} = 0V	_	_	300	μA
		YER) DSS		400	-	-	٧
Gate Threshold Voltage		Y h	V _{DS} = 10V, I _D = 1mA	1.5	-	3.5	٧
Drain-Source ON Resistance		Pos (ON)	I _D = 0.5A, V _{GS} = 10V	-	3.6	5.0	Ω
Forward Transfer Admittance		Y _{fs} I	V _{DS} = 10V, I _D = 0.5A	0.4	0.62	_	S
Input Capacitance Reverse Transfer Capacitance		Gss	V _{DS} = 10V, V _{GS} = 0V, f = 1MHz	-	150	210	pF
		Css		-	15	25	
Output Capacitance		Coss	· · · · · · ·	-	45	70	
Switching Turn-o Time Fall Til	Rise Time	t _r	V _{GS} _{0V} I _D =0.5A V _{OS} _{0V} V _{OUT}	-	15	30	ns
	Turn-on Time	- bn		_	40	80	
	Fall Time	1		-	40	80	
	Turn-off Time	Pu	50Ω R _L =400Ω	-	135	270	
			$V_{\text{IN}}: t_{\text{r}}, t_{\text{r}} < 5 \text{ns}, V_{\text{DD}} = 200 \text{V}$ $Duty \leq 1\%, t_{\text{W}} = 10 \mu \text{s}$				
Total Gate Charge (Gate-Source Plus Gate-Drain)		Qg	V _{DD} = 320V, V _{GS} = 10V, I _D = 1A	-	12	24	nC
Gate-Source Charge		Q _s		-	48	_	
Gate-Drain ("Miller") Charge		Q _d	1	-	7.2	-	

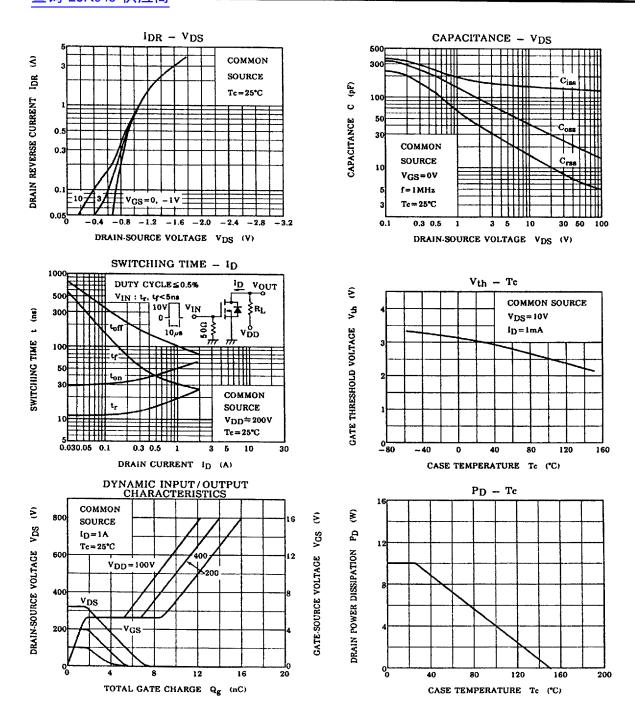
Source-Drain Diode Ratings and Characteristics (Ta = 25C)

CHARACTERISTICS	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Continuous Drain Reverse Current	I _{DR}	_	-	-	1	Α
Pulse Drain Reverse Current	I _{DRP}	_	-	-	4	A
Diode Forward Voltage	V _{DSF}	I _{DR} = 1A, V _{GS} = 0V	-	_	-1.7	V
Reverse Recovery Time	t _{rr}	I _{DR} = 1A, V _{GS} = 0V	-	300	_	ns
Reverse Recovered Charge	Qπ	dl _{DR} /dt = 50A/µs	-	0.75	-	μC



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