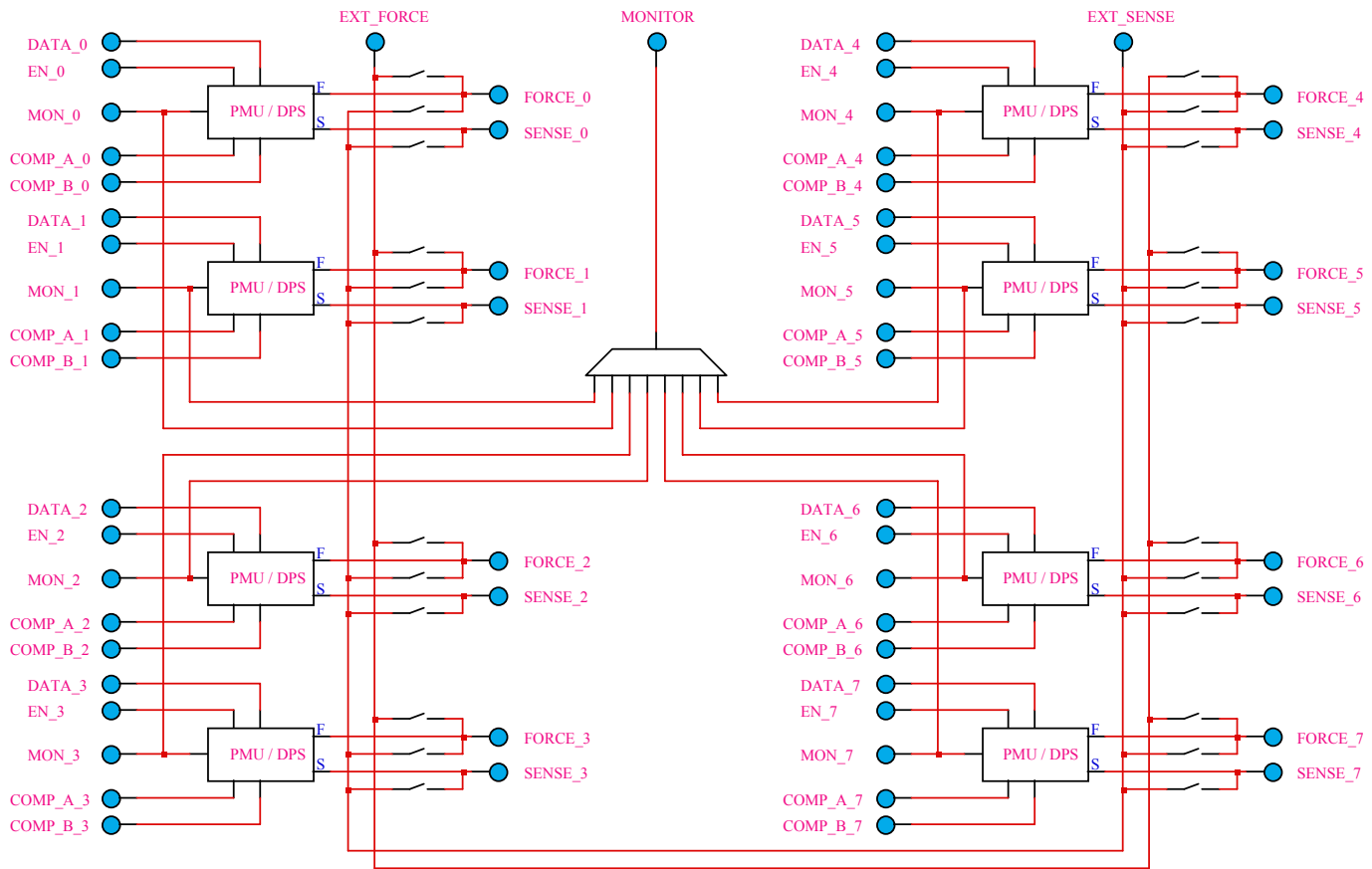


SOC Octal DPS / PMU / VI w/ Ganging 10 MHz Pin Electronics

[Get Full Datasheet](#)


Pluto Compatible

Per Pin DPS / PMU

- FV, FI, MV, MI – 4 Quadrant Operation
64 mA I_{max} in FV / MI
32 mA I_{max} in FI / MV
- 8 Current Ranges
(32 mA, 8 mA, 2 mA, 512 μ A, 128 μ A, 32 μ A, 8 μ A, 2 μ A)
- 14V FV Range
- FI Voltage Clamps
- FV Current Clamps
- Per Pin Monitor
- Central (Per Chip) Monitor

Pin Electronics Driver and Comparator

- 2 Level Driver w/ On Chip Buffers
- 14V Driver Output Swings
- 10 MHz Driver Operation
- 16V Comparator Input Voltage Range
- Extremely Low Input Leakage over a 16V Range

Ganging Capability

- High current applications
- No limit on ganged I_{max}
- Gang control circuitry built in

3 Bit Serial CPU Port

On Chip DAC to Generate DC Levels

- 10 DC Levels per Channel (16 Bits / Level)
- On Chip Offset and Gain Correction
- Ability to shift voltage ranges up and down

Package / Power Dissipation

- Lead Free
- 128 Lead, 14 mm X 20 mm, TQFP w/ Heat Slug
- Pd_q $\leq$ 125 mW / Channel; Pd_q $\leq$ 1 W / Chip
- On-Chip Thermal Monitor