### Freescale Semiconductor, Inc.Order this document by MC12019/D



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# **Dual Modulus Prescaler**

The MC12019 is a divide by 20 and 21 dual modulus prescaler. It will divide by 20 when the modulus control input is HIGH and divide by 21 when the modulus control input is LOW.

- 225 MHz Toggle Frequency
- Low-Power 7.5 mA Maximum at 5.5 V
- Control Input is Compatible with Standard Motorola CMOS Synthesizers
- Emitter Follower Output

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### MECL PLL COMPONENTS ÷20/21 DUAL MODULUS PRESCALER

SEMICONDUCTOR TECHNICAL DATA



D SUFFIX PLASTIC PACKAGE CASE 751 (SO-8)

## IN 1 0 8 NC NE 2 7 V<sub>CC</sub> Out 3 6 SGnd Gnd 4 5 SIN

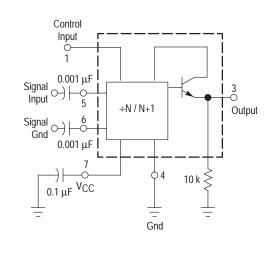
(Top View)

| ORDERING INFORMATION |                                |         |  |  |  |  |
|----------------------|--------------------------------|---------|--|--|--|--|
| Device               | Operating<br>Temperature Range | Package |  |  |  |  |

 $T_A = -40$  to  $85^{\circ}C$ 

MC12019D

### SIMPLIFIED BLOCK DIAGRAM





SO-8

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#### **MAXIMUM RATINGS**

| 查询"MC12019D"們应商                          | Symbol           | Value                 | Unit |
|--|------------------|-----------------------|------|
| A Power Supply Voltage, Pin-SCALE SEMICO | NDV€TOR,         | INC <sub>8.0005</sub> | Vdc  |
| Operating Temperature Range              | TA               | -40 to +85            | °C   |
| Storage Temperature Range                | T <sub>stg</sub> | -65 to +175           | °C   |

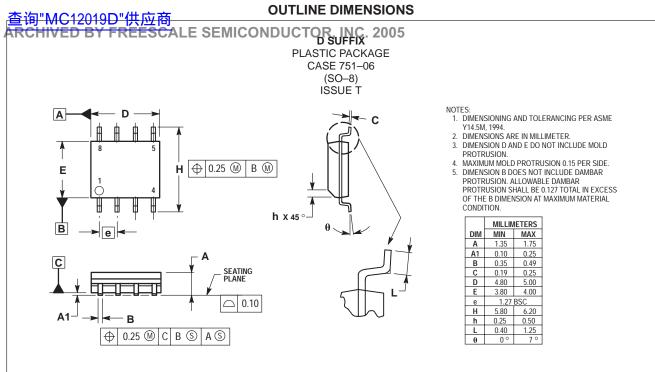
NOTE; ESD data available upon request.

### **ELECTRICAL CHARACTERISTICS** (V<sub>CC</sub> = 4.5 to 5.5 V; $T_A = -40$ to 85°C), unless otherwise noted.)

| Characteristic                                 | Symbol                               | Min      | Тур | Max                  | Unit |
|--|--------------------------------------|----------|-----|----------------------|------|
| Toggle Frequency (Sine Wave Input)             | f <sub>max</sub><br><sup>f</sup> min | 225<br>- |     | _<br>20              | MHz  |
| Supply Current                                 | ICC                                  | -        | -   | 7.5                  | mA   |
| Control Input HIGH (+20)                       | VIH                                  | 2.0      | -   | -                    | V    |
| Control Input LOW (+21)                        | VIL                                  | -        | -   | 0.8                  | V    |
| Output Swing Voltage (10 k $\Omega$ to ground) | Vout                                 | 600      | -   | 1200                 | mVpp |
| Input Voltage Sensitivity<br>20 MHz to 225 MHz | Vin                                  | 200      | _   | 800                  | mVPP |
| PLL Response Time (Notes 1 and 2)              | tPLL                                 | -        | -   | t <sub>out</sub> -70 | ns   |

NOTES: 1. tpLL = the period of time the PLL has from the prescaler rising output tranistion (50%) to the modulus control input edge transition (50%) to ensure proper modulus selection. 2.  $t_{OUT}$  = period of output waveform.

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