

N-Channel 60-V (D-S) MOSFET

PRODUCT SUMMARY

$V_{(BR)DSS}$ (V)	$r_{DS(on)}$ (Ω)	I_D (A)	Q_g (Typ)
60	0.0044 at $V_{GS} = 10$ V	90 ^d	105

FEATURES

- TrenchFET[®] Power MOSFETS
- 175 °C Junction Temperature
- 100 % R_g and UIS Tested

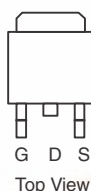


RoHS
COMPLIANT

APPLICATIONS

- Power Supply
 - Secondary Synchronous Rectification
- Industrial
- OR-ing

TO-263



Ordering Information: SUM90N06-4m4P-E3 (Lead (Pb)-free)



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS $T_C = 25$ °C, unless otherwise noted

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current ($T_J = 175$ °C)	I_D	90 ^d	A
		90 ^d	
Pulsed Drain Current	I_{DM}	240	
Avalanche Current	I_{AS}	70	
Single Avalanche Energy ^a	E_{AS}	245	mJ
Maximum Power Dissipation ^a	P_D	300 ^b	W
		3.75	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to 175	°C

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Limit	Unit
Junction-to-Ambient (PCB Mount) ^c	R_{thJA}	40	°C/W
Junction-to-Case (Drain)	R_{thJC}	0.5	

Notes:

- Duty cycle ≤ 1 %.
- See SOA curve for voltage derating.
- When Mounted on 1" square PCB (FR-4 material).
- Package limited.



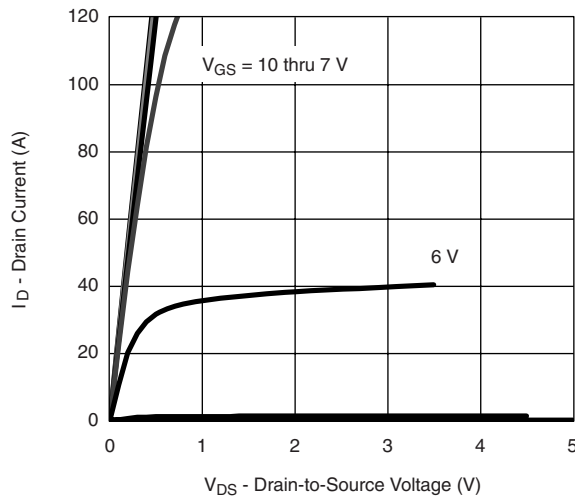
SPECIFICATIONS T _J = 25 °C, unless otherwise noted						
Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{DS} = 0 V, I _D = 250 μA	60			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	2.5		4.5	
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V			± 250	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 60 V, V _{GS} = 0 V			1	μA
		V _{DS} = 60 V, V _{GS} = 0 V, T _J = 125 °C			50	
		V _{DS} = 60 V, V _{GS} = 0 V, T _J = 150 °C			250	
On-State Drain Current ^a	I _{D(on)}	V _{DS} ≥ 10 V, V _{GS} = 10 V	70			A
Drain-Source On-State Resistance ^a	r _{DS(on)}	V _{GS} = 10 V, I _D = 20 A		0.0036	0.0044	Ω
		V _{GS} = 10 V, I _D = 20 A, T _J = 125 °C		0.0059	0.0077	
Forward Transconductance ^a	g _{fs}	V _{DS} = 15 V, I _D = 20 A		60		S
Dynamic ^b						
Input Capacitance	C _{iss}	V _{GS} = 0 V, V _{DS} = 30 V, f = 1 MHz		6190		pF
Output Capacitance	C _{oss}			990		
Reverse Transfer Capacitance	C _{rss}			340		
Total Gate Charge ^c	Q _g	V _{DS} = 30 V, V _{GS} = 10 V, I _D = 85 A		105	160	nC
Gate-Source Charge ^c	Q _{gs}			29		
Gate-Drain Charge ^c	Q _{gd}			28		
Gate Resistance	R _g	f = 1 MHz		1.4	2.8	Ω
Turn-On Delay Time ^c	t _{d(on)}	V _{DD} = 30 V, R _L = 0.4 Ω I _D ≅ 85 A, V _{GEN} = 10 V, R _g = 1 Ω		23	35	ns
Rise Time ^c	t _r			15	25	
Turn-Off Delay Time ^c	t _{d(off)}			36	55	
Fall Time ^c	t _f			8	15	
Source-Drain Diode Ratings and Characteristics (T _C = 25 °C) ^b						
Continuous Current	I _S				85	A
Pulsed Current	I _{SM}				240	
Forward Voltage ^a	V _{SD}	I _F = 30 A, V _{GS} = 0 V		0.84	1.5	V
Reverse Recovery Time	t _{rr}	I _F = 75 A, di/dt = 100 A/μs		61	100	ns
Peak Reverse Recovery Current	I _{RM(REC)}			3.0	4.5	A
Reverse Recovery Charge	Q _{rr}				91	140

Notes:

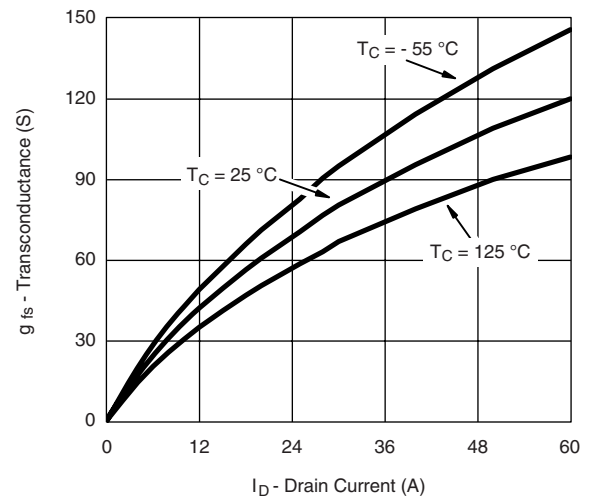
- a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
b. Guaranteed by design, not subject to production testing.
c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

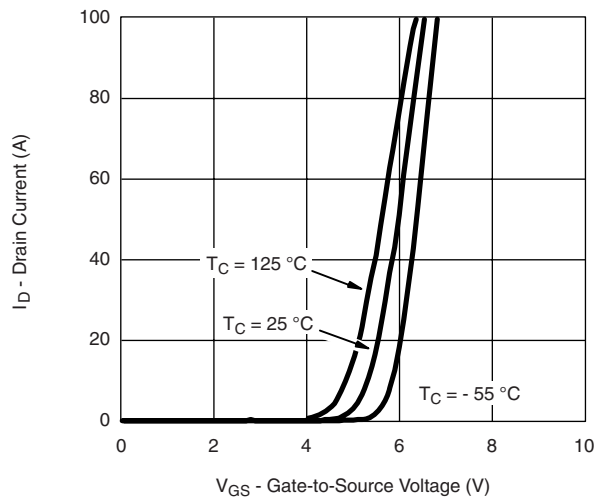
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



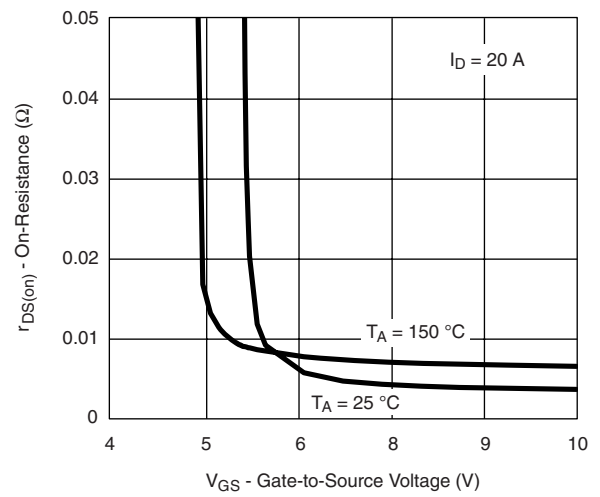
Output Characteristics



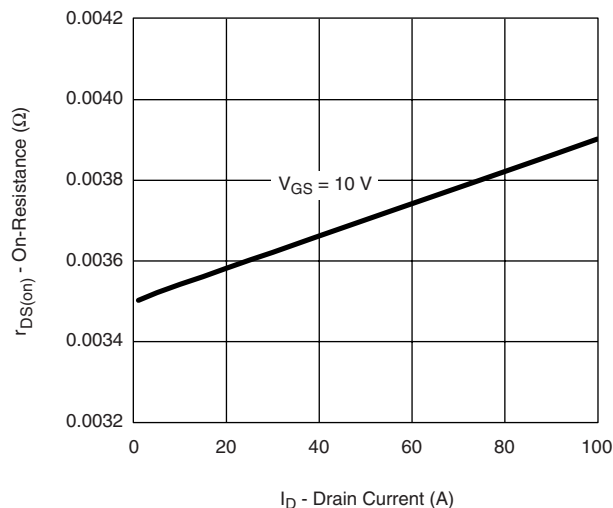
Transconductance



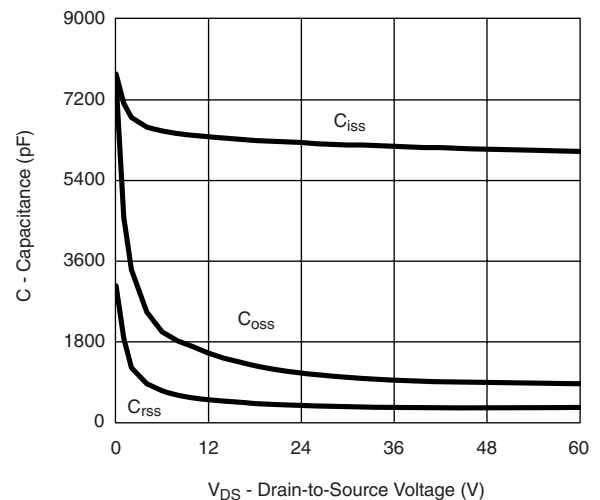
Transfer Characteristics



On-resistance vs. Gate-to-Source Voltage

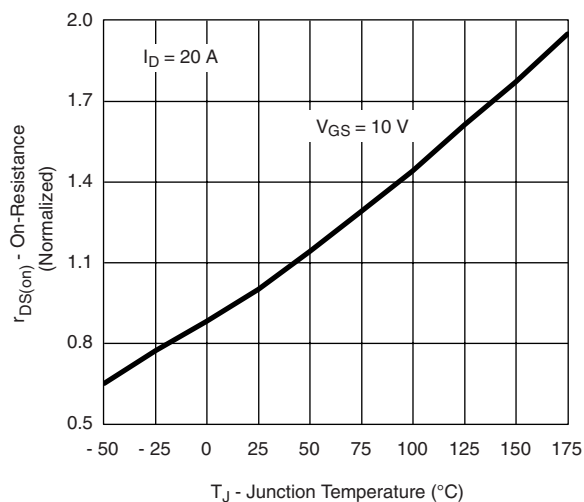


On-Resistance vs. Drain Current

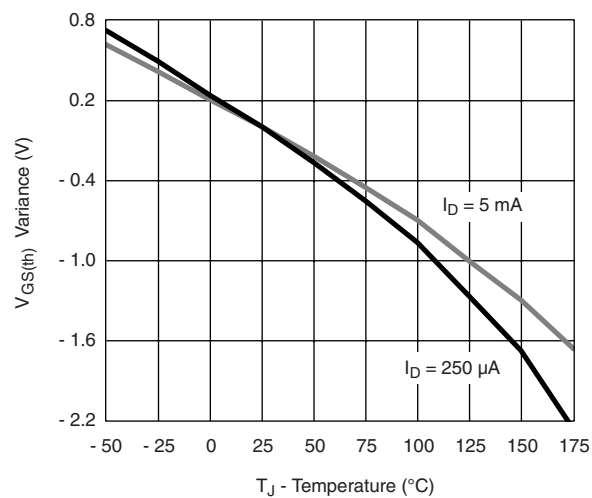


Capacitance

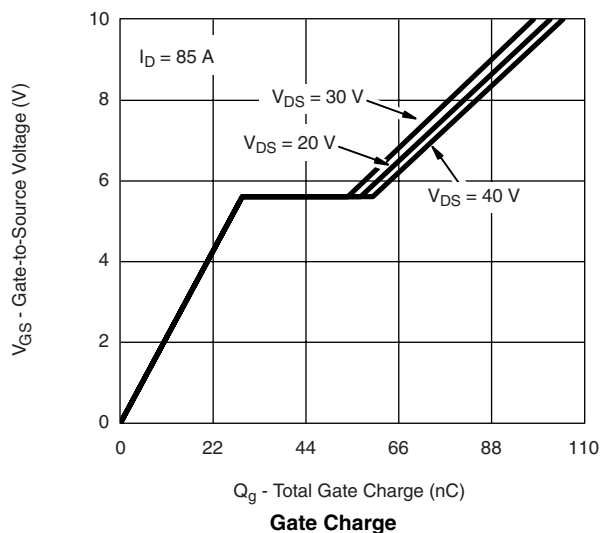
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



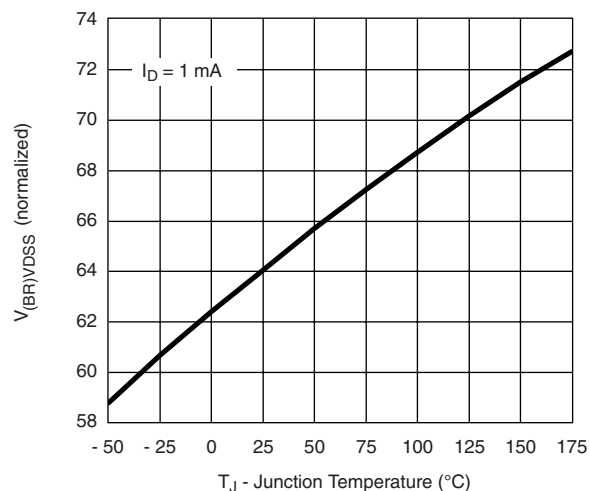
On-Resistance vs. Junction Temperature



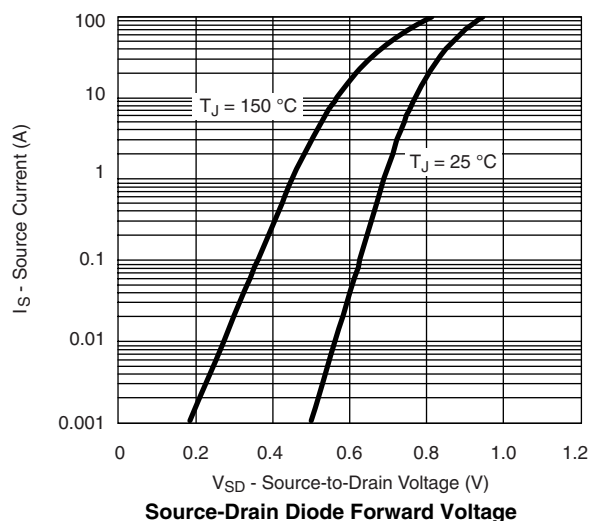
Threshold Voltage



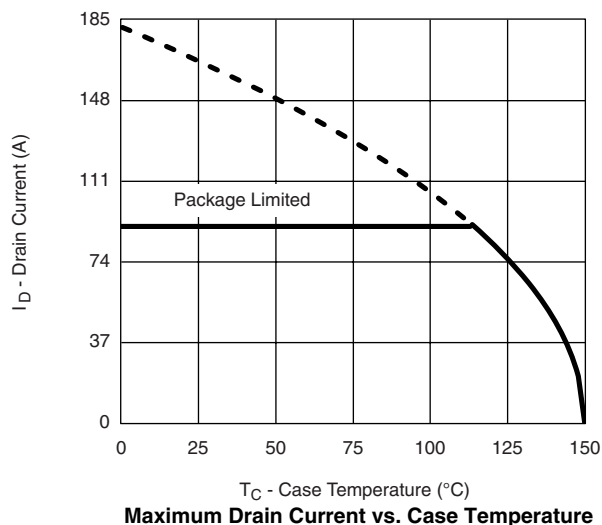
Gate Charge



On-Resistance vs. Junction Temperature

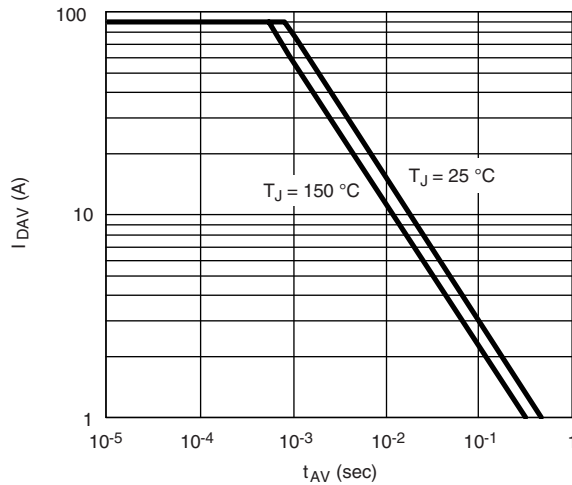


Source-Drain Diode Forward Voltage

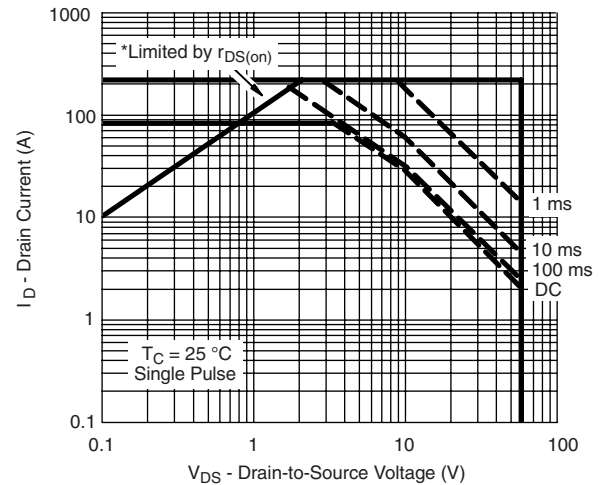


Maximum Drain Current vs. Case Temperature

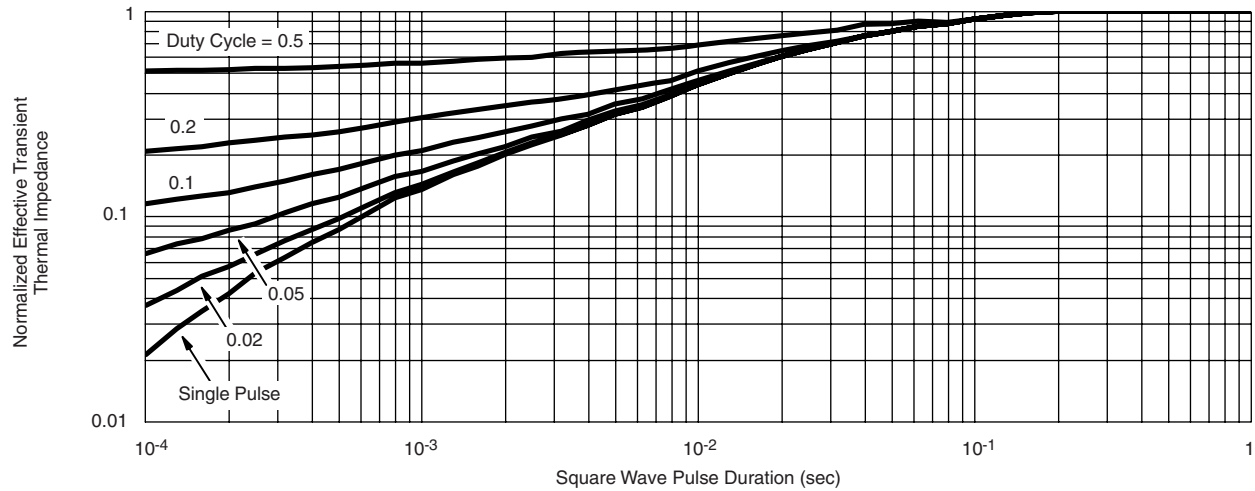
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Single Pulse Avalanche Current Capability vs. Time



Safe Operating Area
* $V_{GS} >$ minimum V_{GS} at which $r_{DS(on)}$ is specified



Normalized Thermal Transient Impedance, Junction-to-Case

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