

A_S-W25 & B_LS-W25 Series

0.25W, FIXED INPUT, ISOLATED & UNREGULATED **DUAL/SINGLE OUTPUT DC-DC CONVERTER**





multi-country patent protection RoHS

FEATURES

Small Footprint 1KVDC Isolation SIP Package Internal SMD Construction Temperature Range: -40°C to +85°C No Heat sink Required No External Component Required Industry Standard Pinout RoHS Compliance

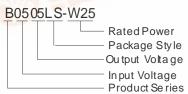
APPLICATIONS

The A S-W25 & B LS-W25 Series are specially designed for applications where a group of polar power supplies are isolated from the input power supply in a distributed power supply system on a circuit board.

These products apply to:

- 1) Where the voltage of the input power supply is fixed (voltage variation ≤ ±10%);
- Where isolation is necessary between input and output (isolation voltage ≤1000VDC);
- Where the regulation of the output voltage and the output ripple noise are not demanding. Such as: purely digital circuits, ordinary low frequency analog circuits, and IGBT power device driving circuits.

MODEL SELECTION



MORNSUN Science & Technology co., Ltd. Address: 2th floor 6th building, Hangzhou Industrial District, Guangzhou, China Tel: 86-20-38601850 Fax: 86-20-38601272 http://www.mornsun-power.com

_	Input		Ou			
Part Number	Voltage (VDC)		Voltage	Current (mA)	Efficiency (%, Typ)	
Number	Nominal	Nominal	(VDC)	Max	(70, Typ	
B0303LS-W25*	0.0	3.0-3.6	3.3	75.8	62	
B0305LS-W25	3.3		5	50	65	
A0505S- W25			±5	±25	62	
A0509S- W25*			±9	±13.8	64	
A0512S- W25			±12	±10.4	66	
A0515S- W25	5	4.5-5.5	±15	±8.3	65	
B0505LS- W25	5		5	50	64	
B0509 LS- W25*			9	27.8	65	
B0512 LS- W25		- 4	12	20.8	67	
B0515 LS- W25			15	16.7	65	
A1205S- W25*		P. 1	±5	±25	62	
A1209S- W25*			±9	±13.8	63	
A1212S- W25*			±12	±10.4	64	
A1215S- W25*		- 40	±15	±8.3	65	
B1203 LS- W25*	12	10.8-13.2	3.3	75.8	62	
B1205 LS- W25		The state of the s	5	50	65	
B1209 LS- W25*			9	27.8	66	
B1212 LS- W25			12	20.8	67	
B1215 LS- W25*			15	16.7	66	
A2405S- W25			±5	±25	63	
A2409S- W25*			±9	±13.8	64	
A2412S- W25*			±12	±10.4	65	
A2415S- W25*			±15	±8.3	65	
B2405 LS- W25	24	21.6-26.4	5	50	63	
B2409 LS- W25*			9	27.8	63	
B2412 LS- W25*			12	20.8	65	
B2415 LS- W25*			15	16.7	65	
B2424LS- W25*			24	10.4	64	

COMMON SPECI	FICATIONS	LTT	W	Ж;	
Item	Test conditions	Min	Тур	Max	Units
Storage humidity	The second N	11:10:1		95	%
Operating Temperature	912 12 4	-40		85	
Storage Temperature	E Ch.	-55		125	°C
Temp. rise at full load			15	25	
Lead temperature	1.5mm from case for 10 seconds			300	
Short circuit protection*				1	S
Cooling		F	ree air	convec	tion
Case material		Plastic (UL94-V0)			
MTBF		3500			K hours
Weight			2.1		g
*Supply voltage must be dis	continued at the end of short circuit dura	ition.			

ISOLATION SPECIFICATIONS A 1209S-W記載 Min Typ Max Units Isolation voltage Tested for 1 minute and 1 mA max 1000 VDC Isolation resistance Test at 500VDC 1000 MΩ

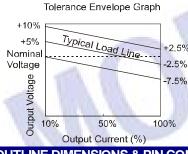
CATIONS					
Test conditions		Min	Тур	Max	Units
				0.25	W
For Vin change	(3.3V input)			±1.5	
of ±1%	(Others input)			±1.2	
10% to 100% load	(3.3V output)		12	20	%
	(5V output)		10.5	15	
	(9V output)		8.3	15	
	(12V output)		6.8	15	
	(15V output)		6.3	15	
		See to	olerance	envelope	graph
100% full load			0.03	%/°C	
20MHz Bandwidth			50	75	mVp-p
Full load, nominal input			100		KHz
	For Vin change of ±1% 10% to 100% load 100% full load 20MHz Bandwid Full load, nomin	Test conditions	Test conditions	Test conditions	Test conditions Min Typ Max 0.25 For Vin change of ±1% (3.3V input) ±1.5 (Others input) ±1.2 (3.3V output) 12 20 (5V output) 10.5 15 (9V output) 8.3 15 (12V output) 6.8 15 (15V output) 6.3 15 See tolerance envelope 100% full load 0.03 20MHz Bandwidth 50 75 Full load, nominal input 100

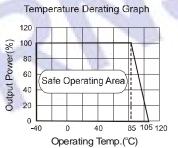
^{*}Test ripple and noise by "parallel cable" method. See detailed operation instructions at Testing of Power Converter section, application notes.

Note:

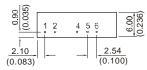
- All specifications measured at T_A=25°C, humidity<75%, nominal input voltage and rated output load unless otherwise specified.
- 2. See below recommended circuits for more details.
- 3. Dual output models unbalanced load: ±5%.

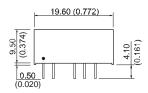
TYPICAL CHARACTERISTICS





OUTLINE DIMENSIONS & PIN CONNECTIONS

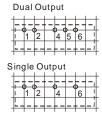




Note: Unit:mm(inch) Pin section:0.50*0.30mm(0.020*0.012inch) Pin section tolerances:±0.10mm(±0.004inch) General tolerances:±0.25mm(±0.010inch)

First Angle Projection ← ⊕

RECOMMENDED FOOTPRINT Top view, grid:2.54*2.54mm(0.1*0.1inch), diameter:1.00mm(0.039inch)



FOOTPRINT DETAILS

Pin	Single	Dual
1	Vin	Vin
2	GND	GND
4	0V	-Vo
5	No Pin	0V
6	+Vo	+Vo

APPLICATION NOTE

Requirement on output load

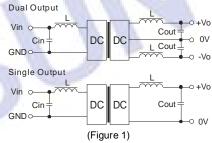
To ensure this module can operate efficiently and reliably, During operation, the minimum output load is not less than 10% of the full load, and that this product should never be operated under no load! If the actual output power is very small, please connect a resistor with proper resistance at the output end in parallel to increase the load.

Overload Protection

Under normal operating conditions, the output circuit of these products has no protection against overload. The simplest method is to connect a self-recovery fuse in series at the input end or add a circuit breaker to the circuit.

Recommended circuit

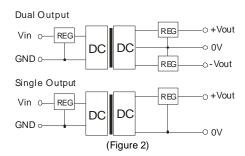
If you want to further decrease the input/output ripple, an "LC" filtering network may be connected to the input and output ends of the DC/DC converter, see (Figure 1).



It should also be noted that the inductance and the frequency of the "LC" filtering network should be staggered with the DC/DC frequency to avoid mutual interference. However, the capacitance of the output filter capacitor must be proper. If the capacitance is too big, a startup problem might arise. It's not recommended to connect any external capacitor in the application field.

Output Voltage Regulation and Over-voltage Protection Circuit

The simplest device for output voltage regulation, over-voltage and over-current protection is a linear voltage regulator with overheat protection that is connected to the input or output end in series (Figure 2).



No parallel connection or plug and play.