	JJJUZ-(38631	01GA	<u>\"供</u> [亚商			ŀ		ONS										
LTR	DESCRIPTION							DATE (YR-MO-DA)		APPROVED		I								
А	Char	Changes in accordance with NOR 5962-R262-92. – sbr							92-07-16		M. A. Frye									
В	Char	nges in	accord	lance w	vith NO	R 5962	-R065-	93. – d	rw					93-0	01-11			M. A	. Frye	
С	Revis	Revise for 'D' certification. Editorial changes throughout. – d				rw				99-1	0-01		F	Raymon	id Mon	nin				
				THE	ORIGI	NAL FII	RST PA	AGE OF	= THIS	DRAW	ING HA	AS BEE	EN REF	PLACE	D.					
				THE	ORIGI	NAL FII	RST P#	AGE OF	= THIS	DRAW	ING HA	AS BEE	EN REF		D.			1	I	1
REV				THE			RST P#	AGE OF	= THIS	DRAW	ING H	AS BEE	EN REF		D.					
SHEET				THE				AGE OF	= THIS	DRAW		AS BEE	EN REF		D.					
				THE			RST P4		= THIS	DRAW		AS BEE	EN REF		D.					
SHEET REV				THE			RST PA	AGE OF	THIS	DRAW	C	AS BEE	EN REF	C	D.	C				
SHEET REV SHEET																C 10				
SHEET REV SHEET REV STATUS OF SHEETS PMIC N/A	NDAF	CUIT		REV SHE PRE	/ EET	D BY ristophe BY	C 1	C 2	C	C	C 5	C 6	C 7 SE SI	C 8	С 9 9				US	
SHEET REV SHEET REV STATUS OF SHEETS PMIC N/A STA MICRO DR. THIS DRAWI	NDAF DCIRC AWIN JSE BY JRTMEN NCIES (CUIT G VAILAE ALL ITS OF THE	E	REV SHE PRE CHE	/ EET Chr CkED	D BY ristophe BY Ray M D BY Wichael	C 1 Pr A. Ra Ionnin	C 2 uuch	C	C 4	C 5 DE	C 6 EFEN	C 7 SE SI COLI		C 9 Y CE US, O	10 NTER	13216 PERIF	6		
SHEET REV SHEET REV STATUS OF SHEETS PMIC N/A STA MICRO DR/ THIS DRAWI FOR L DEPA AND AGE DEPARTME	NDAF DCIRC AWIN NG IS A JSE BY JSE BY JSE BY A RTMEN NCIES (NT OF I	CUIT G VAILAE ALL DF THE DEFEN:	E	REV SHE PRE CHE APP	/ EET Chr CKED	D BY Ray M D BY Michael APPRC 89-0	C 1 er A. Ra Ionnin I A. Fry DVAL D 04-28	C 2 uuch	C	C 4 MIC DR	C 5 DE CROC	C 6 EFEN	C 7 SE SI COLI		C 9 Y CE US, O	10 NTER HIO 4	13216 PERIF	6		
SHEET REV SHEET REV STATUS OF SHEETS PMIC N/A STA MICRO DR/ THIS DRAWI FOR L DEPA AND AGE DEPARTME	NDAF DCIRC AWIN JSE BY JRTMEN NCIES (CUIT G VAILAE ALL DF THE DEFEN:	E	REV SHE PRE CHE APP	/ EET PAREE Chr CKED	D BY ristophe BY Ray M D BY Michael APPRC 89-0	C 1 er A. Ra Ionnin I A. Fry DVAL D	C 2 uuch	C	C 4 MIC DRI	C 5 DE	C 6 EFEN CIRCL	C 7 SE SI COLI		C 9 Y CE US, O	10 NTER HIO 4	13216 PERIF	6	AL	

DSCC FORM 2233 APR 97

DISTRIBUTION STATEMENT A. Approved for public release; distribution is unlimited.

5962-E501-99

查 <mark>爸爸妈</mark> 2-8863101GA"供应商 1.1 <u>Scope</u> . This drawing describes device requirements for MIL-STD-883 compliant, non-JAN class level B microcircuits in accordance with MIL-PRF-38535, appendix A.									
1.2 Part or Identifying I	1.2 Part or Identifying Number (PIN). The complete PIN is as shown in the following example:								
5962-88631	01	G	A						
Drawing number	2 1	Case outline	Lead finish						
1.2.1 Device type(s)	(see 1.2.1) The device type(s) identify the circu	(see 1.2.2)	(see 1.2.3)						
		in function as folio		_					
<u>Device type</u> 01	<u>Generic number</u> DS1631		<u>Circuit function</u> Dual AND peripheral d compatible, open colle	river, CMOS					
1.2.2 Case outline(s).	The case outline(s) are as designa	ated in MIL-STD-18							
Outline letter	Descriptive designator	Terminals	Package style						
G		8	Can Duct in line						
	GDIP1-T8 or CDIP2-T		Dual-in-line						
	lead finish is as specified in MIL-P	RF-38535, append	dix A.						
1.3 Absolute maximum									
			o V _{CC} +0.3 V dc						
Output voltage	range	56 V dc							
Maximum power diss	ipation: <u>1</u> /		50 0						
	· _								
Lead temperature (so	oldering, 10 seconds)	+260°C							
	unction-to-case (θ _{JC}) e (T _J)		TD-1835						
1.4 <u>Recommended op</u>									
	erature range (T _c)	-55°C to +1	25°C						
	tage range (V_{CC})								
1/ For case P. derate 7	<u>1</u> / For case P, derate 7.6 mW/°C above +25°C; for case G, derate 5.2 mW/°C above +25°C								
1/ FOI Case F, derate 7	.6 IIIW/ C above +25 C, for case C		C above +25 C						
S	TANDARD	SIZE		5962-88631					
MICROCI	RCUIT DRAWING	A							
	PLY CENTER COLUMBUS S, OHIO 43216-5000		REVISION LEVEL C	SHEET 2					

2. APPLICABLE DOCUMENTS

查询。5962-8863101GA。供应商ards, and handbooks. The following specification, standards, and handbooks form a part of this drawing to the extent specified herein. Unless otherwise specified, the issues of these documents are those listed in the issue of the Department of Defense Index of Specifications and Standards (DoDISS) and supplement thereto, cited in the solicitation.

SPECIFICATION

DEPARTMENT OF DEFENSE

MIL-PRF-38535 - Integrated Circuits, Manufacturing, General Specification for.

STANDARDS

DEPARTMENT OF DEFENSE

MIL-STD-883	-	Test Method Standard Microcircuits.
MIL-STD-973	-	Configuration Management.
MIL-STD-1835	-	Interface Standard For Microcircuit Case Outlines.

HANDBOOKS

DEPARTMENT OF DEFENSE

MIL-HDBK-103 -	List of Standard Microcircuit Drawings.
MIL-HDBK-780 -	Standard Microcircuit Drawings.

(Unless otherwise indicated, copies of the specification, standards, and handbooks are available from the Standardization Document Order Desk, 700 Robbins Avenue, Building 4D, Philadelphia, PA 19111-5094.)

2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing takes precedence. Nothing in this document, however, supersedes applicable laws and regulations unless a specific exemption has been obtained.

3. REQUIREMENTS

3.1 Item requirements. The individual item requirements shall be in accordance with MIL-PRF-38535, appendix A for non-JAN class level B devices and as specified herein. Product built to this drawing that is produced by a Qualified Manufacturer Listing (QML) certified and qualified manufacturer or a manufacturer who has been granted transitional certification to MIL-PRF-38535 may be processed as QML product in accordance with the manufacturers approved program plan and qualifying activity approval in accordance with MIL-PRF-38535. This QML flow as documented in the Quality Management (QM) plan may make modifications to the requirements herein. These modifications shall not affect form, fit, or function of the device. These modifications shall not affect the PIN as described herein. A "Q" or "QML" certification mark in accordance with MIL-PRF-38535 is required to identify when the QML flow option is used. This drawing has been modified to allow the manufacturer to use the alternate die/fabrication requirements of paragraph A.3.2.2 of MIL-PRF-38535.

STANDARD MICROCIRCUIT DRAWING	SIZE A		5962-88631
DEFENSE SUPPLY CENTER COLUMBUS		REVISION LEVEL	SHEET
COLUMBUS, OHIO 43216-5000		C	3

3.2 <u>Design, construction, and physical dimensions</u>. The design, construction, and physical dimensions shall be as specified in MILPRF-38535A appendix A and herein. 当时 5962-8663101 GA 保卫语

3.2.1 <u>Case outlines</u>. The case outlines shall be in accordance with 1.2.2 herein.

3.2.2 Terminal connections. The terminal connections shall be as specified on figure 1.

3.2.3 <u>Truth table</u>. The truth table shall be as specified on figure 2.

3.2.4 <u>Test circuits and switching waveforms</u>. The test circuits and switching waveforms shall be as specified on figure 3.

3.3 <u>Electrical performance characteristics</u>. Unless otherwise specified herein, the electrical performance characteristics are as specified in table I and shall apply over the full case operating temperature range.

3.4 <u>Electrical test requirements</u>. The electrical test requirements shall be the subgroups specified in table II. The electrical tests for each subgroup are described in table I.

3.5 <u>Marking</u>. Marking shall be in accordance with MIL-PRF-38535, appendix A. The part shall be marked with the PIN listed in 1.2 herein. In addition, the manufacturer's PIN may also be marked as listed in MIL-HDBK-103 (see 6.6 herein). For packages where marking of the entire SMD PIN number is not feasible due to space limitations, the manufacturer has the option of not marking the "5962-" on the device.

3.5.1 <u>Certification/compliance mark</u>. The compliance mark for device class M shall be a "C" as required in MIL-PRF-38535, Appendix A. For product built in accordance with A.3.2.2 of MIL-PRF-38535, the "D" certification mark shall be used in place of the "C" certification mark.

3.6 <u>Certificate of compliance</u>. A certificate of compliance shall be required from a manufacturer in order to be listed as an approved source of supply in MIL-HDBK-103 (see 6.6 herein). The certificate of compliance submitted to DSCC-VA prior to listing as an approved source of supply shall affirm that the manufacturer's product meets the requirements of MIL-PRF-38535, appendix A and the requirements herein.

3.7 <u>Certificate of conformance</u>. A certificate of conformance as required in MIL-PRF-38535, appendix A shall be provided with each lot of microcircuits delivered to this drawing.

3.8 <u>Notification of change</u>. Notification of change to DSCC-VA shall be required in accordance with MIL-PRF-38535, appendix A.

3.9 <u>Verification and review</u>. DSCC, DSCC's agent, and the acquiring activity retain the option to review the manufacturer's facility and applicable required documentation. Offshore documentation shall be made available onshore at the option of the reviewer.

STANDARD MICROCIRCUIT DRAWING	SIZE A		5962-88631
DEFENSE SUPPLY CENTER COLUMBUS		REVISION LEVEL	SHEET
COLUMBUS, OHIO 43216-5000		C	4

Test	Symbol	ABLE I. <u>Electrical performance cha</u> Conditions -55°C ≤ T _C ≤+125°C unless otherwise specified	Group A subgroups	Device type	Lir	nits	Unit
					Min	Max	
ligh level input voltage	VIH	$V_{CC} = 5 V$, See figure 3	1, 2, 3	01	3.5		V
		$V_{CC} = 10 V$, See figure 3			8.0		
		$V_{CC} = 15 V$, See figure 3			12.5		
ow level input voltage	VIL	$V_{CC} = 5 V$, See figure 3	1, 2, 3	01		1.5	V
		$V_{CC} = 10 V$, See figure 3				2.0	
		$V_{CC} = 15 V$, See figure 3				2.5	
ligh level output current	Юн	$V_{IN} = 15 V (all inputs),$	1, 2, 3	01		250	μA
		$V_{CC} = 15 V, V_{OUT} = 54 V$					
		See figure 3	1.0.0				
ow level output voltage	V _{OL}	$V_{IN} = 1.5$ V, other inputs = 4.5 V	1, 2, 3	01		1.1	V
		$V_{CC} = 4.5 \text{ V}, I_{OL} = 100 \text{ mA}$ See figure 3					
		$V_{IN} = 1.5 V$, other inputs = 4.5 V				1.4	
		$V_{\rm IN} = 1.5$ V, other inputs = 4.5 V $V_{\rm CC} = 4.5$ V, $I_{\rm OL} = 300$ mA				1.4	
		See figure 3					
ligh level input current	I _{IH}	$V_{IN} = 15 V, V_{CC} = 15 V,$	1, 2, 3	01		10	μA
ngn lovor inpot oonont		Other input of driver = $0.0 V$., _, 0				μι
		See figure 3					
ow level input current	IIL	$V_{IN} = 0.4 V, V_{CC} = 15 V,$	1, 2, 3	01		-360	μA
		Other input of driver = 15 V					•
		See figure 3					
		$V_{IN} = 0.4 V, V_{CC} = 5 V,$				-115.5	
		Other input of driver = $5 V$,					
		See figure 3					
ligh level breakdown	V _{OH}	$V_{CC} = 15 \text{ V}, I_{OH} = 250 \ \mu\text{A}$	1, 2, 3	01	56		V
oltage		See figure 3					
Functional tests		See 4.3.1c	7, 8	01			
ee footnotes at end of tab	ole.						
		SIZE				5962-	8863

查询"5962-8863101GA"(供应备 ^I . <u>Electrical performance characteristics</u> – continued. <u>1</u> /								
Test	Symbol	$\label{eq:conditions} -55^{\circ}C \ \leq T_C \ \leq +125^{\circ}C \\ \text{unless otherwise specified}$	Group A subgroups	Device type	Lir	nits	Unit	
					Min	Max		
Power supply current	Іссн	$V_{IN} = 5 V$ (all inputs),	1, 2, 3	01		3	mA	
		$V_{CC} = 5 V$, See figure 3						
		$V_{IN} = 15 V$ (all inputs),				10		
		V_{CC} = 15 V, See figure 3						
	ICCL	$V_{IN} = 0 V$ (all inputs),				11		
		$V_{CC} = 5 V$, See figure 3						
		$V_{IN} = 0 V$ (all inputs),				20		
		$V_{CC} = 15 V$, See figure 3						
Propagation delay time	t _{PHL}	$V_{CC} = 5 V, C_{L} = 15 pF,$	9	01	.01	1.50	μS	
		V_{L} = 10 V, R_{L} = 50 Ω or	10, 11		.01	1.88		
	t _{PLH}	equivalent, See figure 3	9		.01	1.20		
		<u>2</u> /	10, 11		.01	1.50		

1/2 Power dissipation must be externally controlled at elevated temperatures (+25°C and +125°C). 2/2 The limits specified for subgroups 10 and 11 are guaranteed but not tested.

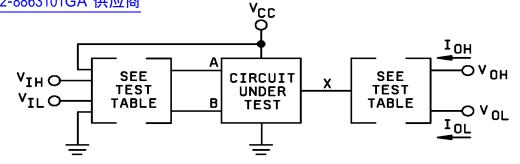
Case outlines	G and P						
Terminal							
number	Pin						
1	A1						
2	B1						
3	X1						
4	GND						
5	X2						
6	A2						
7	B2						
8	Vcc						
FIGURE 1. Terminal connections.							

Inpi	Output	
А	A B	
L	L	L
L	Н	L
Н	L	L
Н	Н	н

FIGURE 2. Truth table.

STANDARD MICROCIRCUIT DRAWING	SIZE A		5962-88631
DEFENSE SUPPLY CENTER COLUMBUS		REVISION LEVEL	SHEET
COLUMBUS, OHIO 43216-5000		C	6

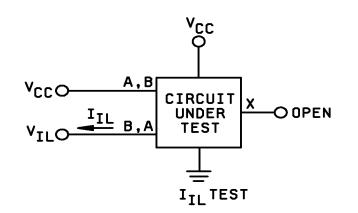
查询"5962-8863101GA"供应商

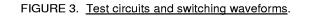




Input under test	Other input	Output		
		Apply	Measure	
VIH	VIH	I _{OH}	V _{OH}	
VIL	Vcc	I _{OL}	V _{OL}	

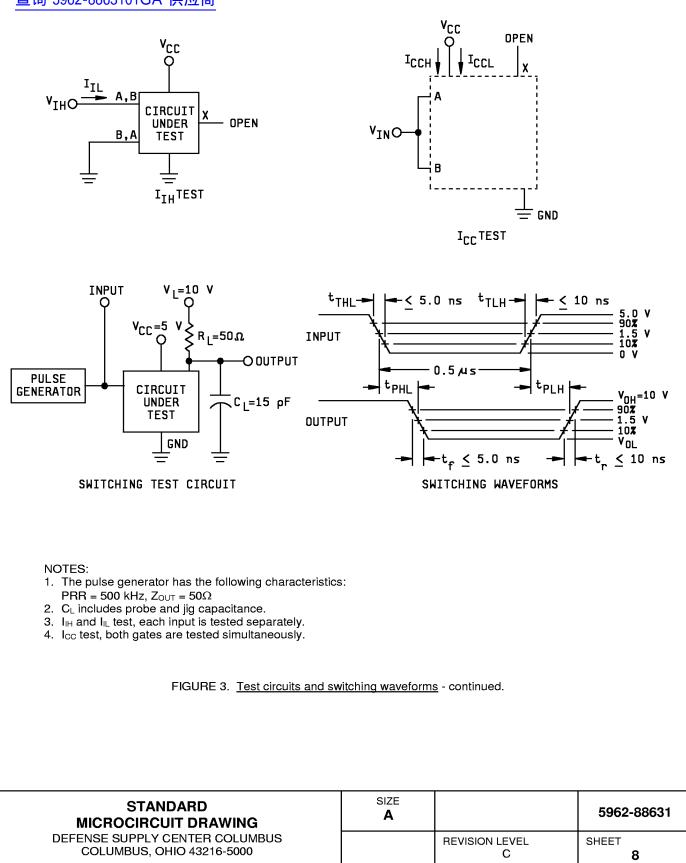
 $V_{\text{IH}},\,V_{\text{IL}},\,\text{test.}$





STANDARD MICROCIRCUIT DRAWING DEFENSE SUPPLY CENTER COLUMBUS COLUMBUS, OHIO 43216-5000	SIZE A		5962-88631
		REVISION LEVEL C	SHEET 7

查询"5962-8863101GA"供应商



4. QUALITY ASSURANCE PROVISIONS

聋询"5962-8863101GA"供应商 appendix A.

4.2 <u>Screening</u>. Screening shall be in accordance with method 5004 of MIL-STD-883, and shall be conducted on all devices prior to quality conformance inspection. The following additional criteria shall apply:

- a. Burn-in test, method 1015 of MIL-STD-883.
 - (1) Test condition A, B, C or D. The test circuit shall be maintained by the manufacturer under document revision level control and shall be made available to the preparing or acquiring activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in test method 1015 of MIL-STD-883.
 - (2) $T_A = +125^{\circ}C$, minimum.
- b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.

MIL-STD-883 test requirements	Subgroups (in accordance with MIL-STD-883, method 5005, table I)
Interim electrical parameters (method 5004)	1
Final electrical test parameters (method 5004)	1*, 2, 3, 9
Group A test requirements (method 5005)	1, 2, 3, 7, 8, 9, 10, 11
Groups C and D end-point electrical parameters (method 5005)	1, 2, 3

TABLE II. Electrical test requirements.

* PDA applies to subgroup 1.

4.3 <u>Quality conformance inspection</u>. Quality conformance inspection shall be in accordance with method 5005 of MIL-STD-883 including groups A, B, C, and D inspections. The following additional criteria shall apply.

4.3.1 Group A inspection.

- a. Tests shall be as specified in table II herein.
- b. Subgroups 4, 5, and 6 in table I, method 5005 of MIL-STD-883 shall be omitted.
- c. Subgroups 7 and 8 shall include verification of the truth table.

STANDARD MICROCIRCUIT DRAWING DEFENSE SUPPLY CENTER COLUMBUS COLUMBUS, OHIO 43216-5000	SIZE A		5962-88631
		REVISION LEVEL C	SHEET 9

参词 · 5962-86391 日 · 达科· 普通商

- a. End-point electrical parameters shall be as specified in table II herein.
- b. Steady-state life test conditions, method 1005 of MIL-STD-883.
 - (1) Test condition A, B, C, or D. The test circuit shall be maintained by the manufacturer under document revision level control and shall be made available to the preparing or acquiring activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in test method 1005 of MIL-STD-883.
 - (2) $T_A = +125^{\circ}C$, minimum.
 - (3) Test duration: 1,000 hours, except as permitted by method 1005 of MIL-STD-883.

5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-PRF-38535, appendix A.

6. NOTES

6.1 <u>Intended use</u>. Microcircuits conforming to this drawing are intended for use for Government microcircuit applications (original equipment), design applications, and logistics purposes.

6.2 <u>Replaceability</u>. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.

6.3 <u>Configuration control of SMD's</u>. All proposed changes to existing SMD's will be coordinated with the users of record for the individual documents. This coordination will be accomplished in accordance with MIL-STD-973 using DD Form 1692, Engineering Change Proposal.

6.4 <u>Record of users</u>. Military and industrial users shall inform Defense Supply Center Columbus when a system application requires configuration control and the applicable SMD. DSCC will maintain a record of users and this list will be used for coordination and distribution of changes to the drawings. Users of drawings covering microelectronics devices (FSC 5962) should contact DSCC-VA, telephone (614) 692-0525.

6.5 <u>Comments</u>. Comments on this drawing should be directed to DSCC-VA, Columbus, Ohio 43216-5000, or telephone (614) 692-0674.

6.6 <u>Approved sources of supply</u>. Approved sources of supply are listed in MIL-HDBK-103. The vendors listed in MIL-HDBK-103 have agreed to this drawing and a certificate of compliance (see 3.6 herein) has been submitted to and accepted by DSCC-VA.

STANDARD MICROCIRCUIT DRAWING DEFENSE SUPPLY CENTER COLUMBUS COLUMBUS, OHIO 43216-5000	SIZE A		5962-88631
		REVISION LEVEL C	SHEET 10

查询"5962-8863101GA"供应商

DATE: 99-10-01

Approved sources of supply for SMD 5962-88631 are listed below for immediate acquisition information only and shall be added to MIL-HDBK-103 during the next revision. MIL-HDBK-103 will be revised to include the addition or deletion of sources. The vendors listed below have agreed to this drawing and a certificate of compliance has been submitted to and accepted by DSCC-VA. This bulletin is superseded by the next dated revision of MIL-HDBK-103.

Standard microcircuit drawing PIN <u>1</u> /	Vendor CAGE number	Vendor similar PIN <u>2</u> /
5962-8863101GA	<u>3</u> /	DS1631H/883
5962-8863101PA	0EU86	AS1631C8/883C

- 1/ The lead finish shown for each PIN representing a hermetic package is the most readily available from the manufacturer listed for that part. If the desired lead finish is not listed contact the vendor to determine its availability.
- <u>2</u>/ <u>Caution</u>. Do not use this number for item acquisition. Items acquired to this number may not satisfy the performance requirements of this drawing.
- $\underline{3}$ / Not available from an approved source.

Vendor CAGE <u>number</u> Vendor name and address

0EU86

Austin Semiconductor Inc. 8701 Cross Park Dr. Austin, TX 78754-4566

The information contained herein is disseminated for convenience only and the Government assumes no liability whatsoever for any inaccuracies in the information bulletin.