Ordering number : ENN6647

P-Channel Silicon MOSFET

3LP01SP

Ultrahigh-Speed Switching Applications

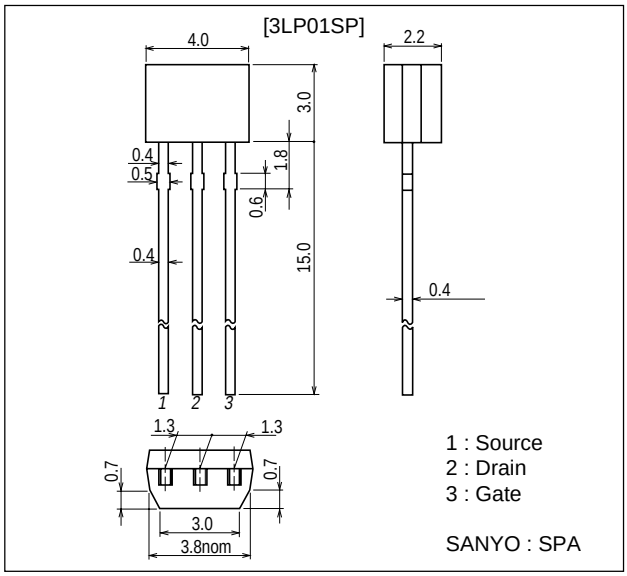
Features

- Low ON-resistance.
- Ultrahigh-speed switching.
- 2.5V drive.

Package Dimensions

unit : mm

2180



Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V_{DSS}		-30	V
Gate-to-Source Voltage	V_{GSS}		±10	V
Drain Current (DC)	I_D		-0.1	A
Drain Current (Pulse)	I_{DP}	PW≤10μs, duty cycle≤1%	-0.4	A
Allowable Power Dissipation	P_D		0.25	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	I_D=-1mA, V_{GS}=0	-30			V
Zero-Gate Voltage Drain Current	I_{DSS}	V_{DS}=-30V, V_{GS}=0			-10	μA
Gate-to-Sourse Leakage Current	I_{GSS}	V_{GS}=±8V, V_{DS}=0			±10	μA
Cutoff Voltage	$V_{GS(off)}$	V_{DS}=-10V, I_D=-100μA	-0.4		-1.4	V
Forward Transfer Admittance	|yfs|	V_{DS}=-10V, I_D=-50mA	80	110		mS

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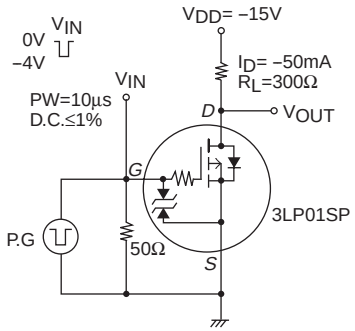
TOKYO OFFICE Tokyo Bldg., 1-10, 1 Chome, Ueno, Taito-ku, TOKYO, 110-8534 JAPAN

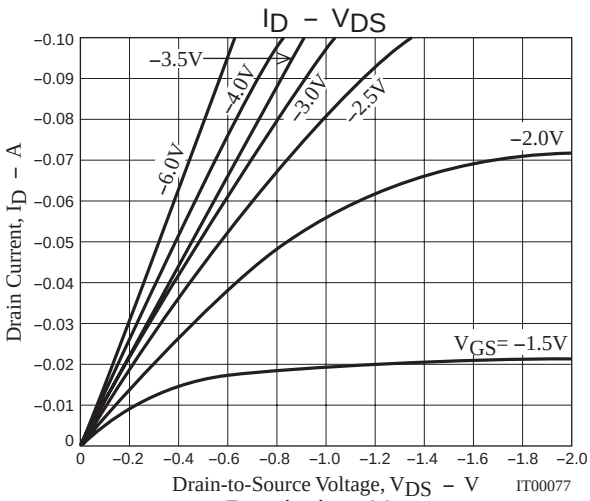
Continued from preceding page.

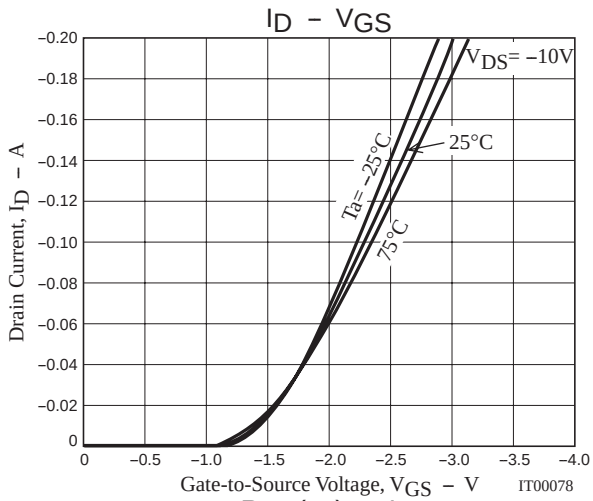
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Static Drain-to-Source On-State Resistance	$R_{DS}(on)1$	I_D=-50mA, V_{GS}=-4V		8	10.4	Ω
	$R_{DS}(on)2$	I_D=-30mA, V_{GS}=-2.5V		11	15.4	Ω
	$R_{DS}(on)3$	I_D=-1mA, V_{GS}=-1.5V		27	54	Ω
Input Capacitance	Ciss	V_{DS}=-10V, f=1MHz		7.5		pF
Output Capacitance	Coss	V_{DS}=-10V, f=1MHz		5.7		pF
Reverse Transfer Capacitance	Crss	V_{DS}=-10V, f=1MHz		1.8		pF
Turn-ON Delay Time	$t_d(on)$	See specified Test Circuit		24		ns
Rise Time	t_r	See specified Test Circuit		55		ns
Turn-OFF Delay Time	$t_d(off)$	See specified Test Circuit		120		ns
Fall Time	t_f	See specified Test Circuit		130		ns
Total Gate Charge	Qg	V_{DS}=-10V, V_{GS}=-10V, I_D=-100mA		1.43		nC
Gate-to-Source Charge	Qgs	V_{DS}=-10V, V_{GS}=-10V, I_D=-100mA		0.18		nC
Gate-to-Drain “Miller” Charge	Qgd	V_{DS}=-10V, V_{GS}=-10V, I_D=-100mA		0.25		nC
Diode Forward Voltage	V_{SD}	I_S=-100mA, V_{GS}=0		0.83	1.2	V

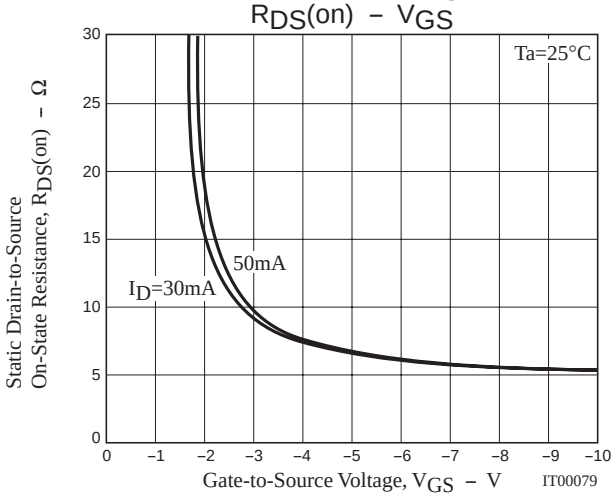
Marking : XA

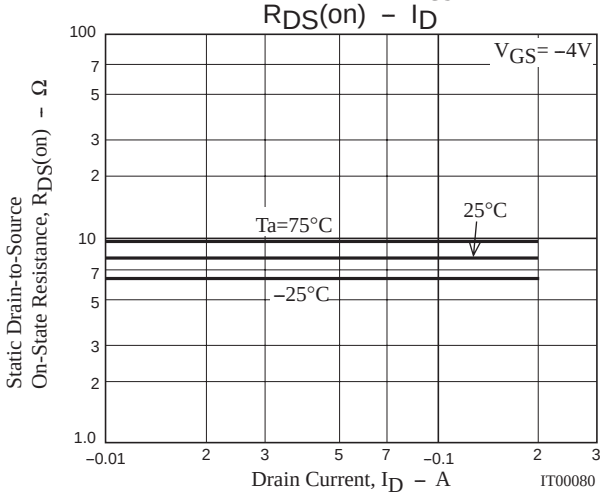
Switching Time Test Circuit



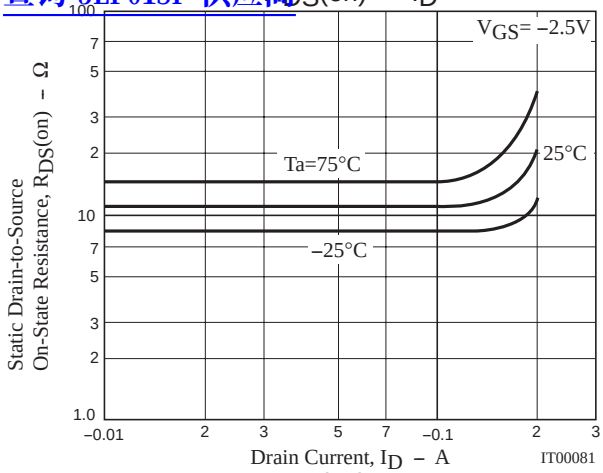








RDS(on) – ID



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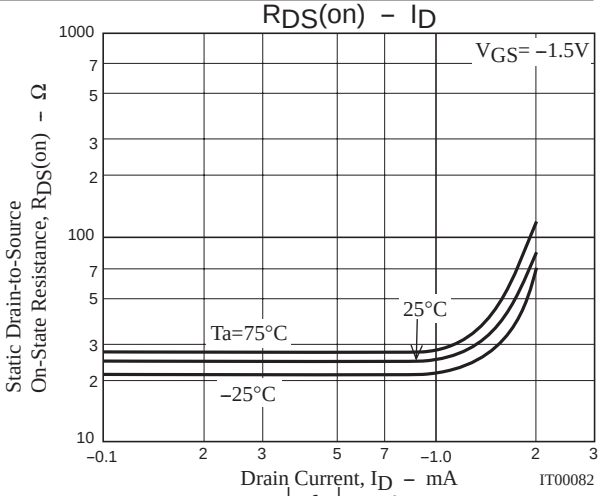
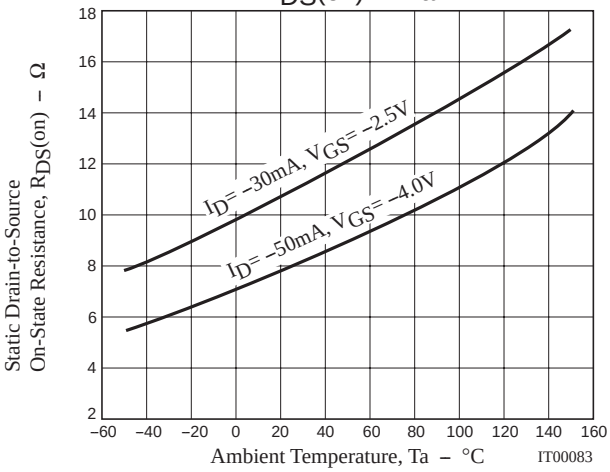
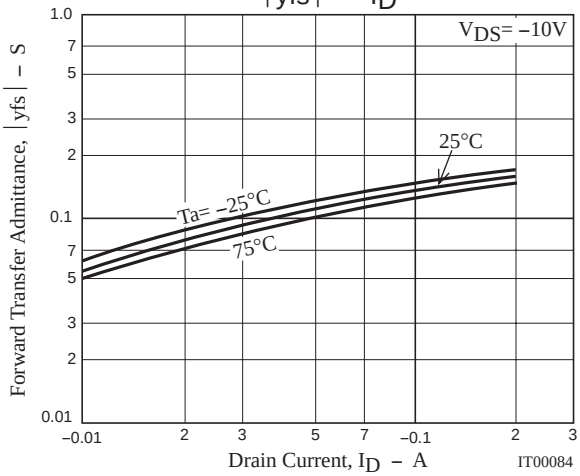


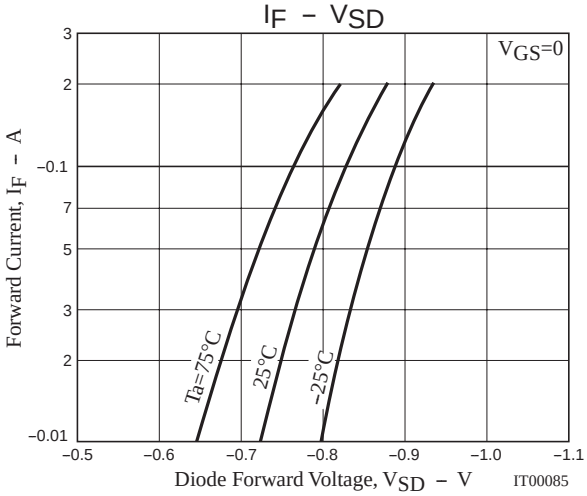
RDS(on) – Ta



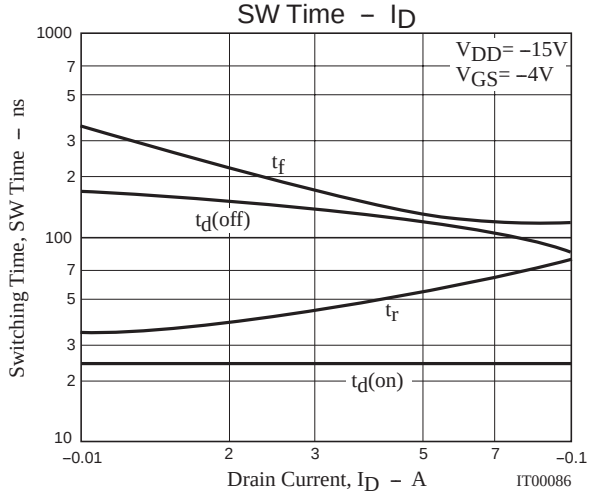
|yfs| – ID



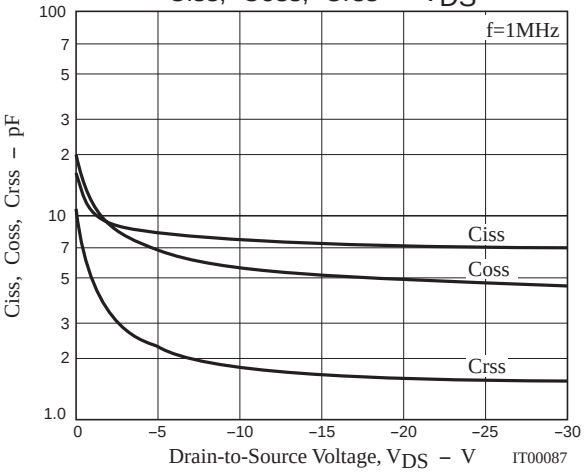
IF – VSD



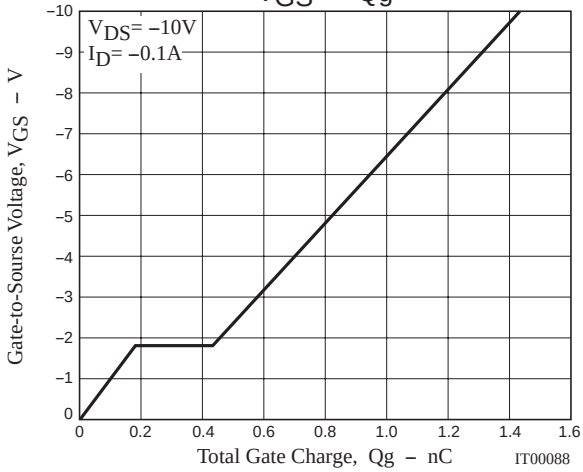
SW Time – ID

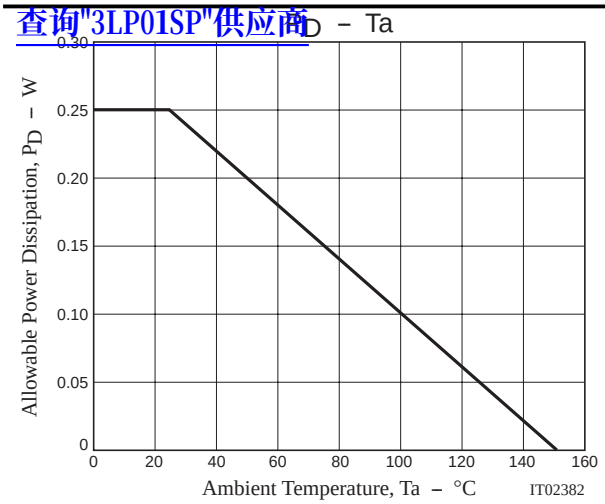


Ciss, Coss, Crss – VDS



VGS – Qg





Note on usage : Since the 3LP01SP is designed for high-speed switching applications, please avoid using this device in the vicinity of highly charged objects.