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HITACHI/(OPTOELECTRONICS) LLE D

SILICON N-CHANNEL MOS FET 查询"2SK400"供应商

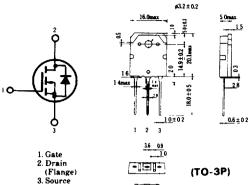
HIGH SPEED POWER SWITCHING, HIGH FREQUENCY POWER AMPLIFIER Complementary pair with 2SJ114

FEATURES

- Low On-Resistance.
- High Speed Switching.
- High Cutoff Frequency.
- No Secondary Breakdown.
- Suitable for Switching Regulator, DC-DC Converter, RF Amplifiers, and Ultrasonic Power Oscillators.

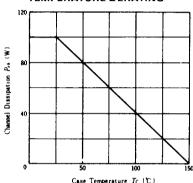
B ABSOLUTE MAXIMUM RATINGS $(T_a=25 \degree \text{C})$

Item	Symbol	Rating	Unit
Drain-Source Voltage	V _{DSS}	200	v
Gate-Source Voltage	V _{GSS}	±20	v
Drain Current	ID	8	A
Drain Peak Current	I D(peak)	12	Α
Body-Drain Diode Reverse Drain Current	IDR	8	A
Channel Dissipation	P _{ch} *	100	w
Channel Temperature	Tch	150	°C
Storage Temperature	Tsig	-55~+150	۰C
*Value at $T_t = 25 \circ C$	4	····	



(Dimensions in mm) 545±02 545±02

POWER VS. TEMPERATURE DERATING



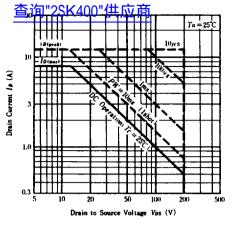
■ ELECTRICAL CHARACTERISTICS (T_a=25 °C)

Item	Symbol	Test Condition	min.	typ.	max.	Unit
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$I_{0} = 10 \text{mA}, V_{cs} = 0$	200	-		v
Gate-Source Leak Current	I _{GSS}	$V_{GS} = \pm 20 \text{V}$. $V_{DS} = 0$		_	±1	μA
Zero Gate Voltage Drain Current	IDSS	V_{DS} =160V, V_{GS} =0	- 1	-	1	mA
Gate-Source Cutoff Voltage	VGRoff	$I_{D}=1$ mA. $V_{DS}=10$ V	2.0		5.0	v
Static Drain-Source On State Resistance	R _{DSton)}	I_0=4A, V_cs=15V*	-	0.5	0.7	Ω
Drain-Source Saturation Voltage	V _{DS(on)}	$I_{D}=4A, V_{GS}=15V^{*}$	-	2.0	2.8	v
Forward Transfer Admittance	y/.	$I_{D} = 4A. V_{DS} = 10V^*$	1.0	1.8		s
Input Capacitance	Cus	V_{DS} =10V. V_{GS} =0 f=1MHz	- 1	750	- 1	pF
Output Capacitance	Coss			300	-	pF
Reverse Transfer Capacitance	Crss		—	60	-	pF
Turn-on Delay Time	t _{dion)}	$I_D=2A. V_{OS}=15V$ $R_L=15\Omega$	-	15	-	ns
Rise Time	t,		_	25	-	ns
Turn-off Delay Time	taiom		-	70	-	ns
Fall Time	t _f		-	40	-	ns
Body-Drain Diode Forward Voltage	V _{DF}	$I_F=4A, V_{GS}=0$	-	0.9	-	v
Body-Drain Diode Reverse Recovery Time	<i>t</i> "	$I_{\mu}=4A, V_{cs}=0$ $di_{F}/dt=50A/\mu s$		300	-	ns

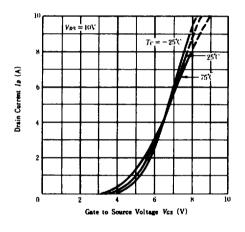
*Pulse Test

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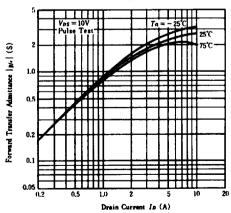
MAXIMUM SAFE OPERATION AREA



TYPICAL TRANSFER CHARACTERISTICS

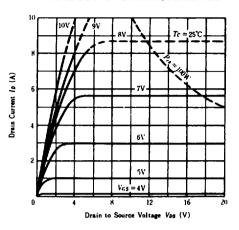


FORWARD TRANSFER ADMITTANCE VS. DRAIN CURRENT

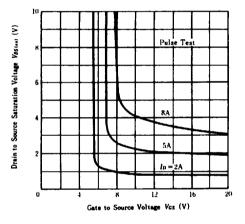


HITACHI/(OPTOELECTRONICS)

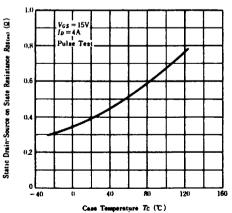
TYPICAL OUTPUT CHARACTERISTICS



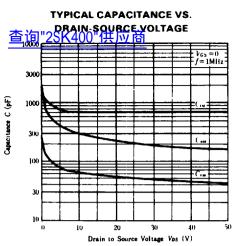
DRAIN-SOURCE SATURATION VOLTAGE VS. GATE-SOURCE VOLTAGE



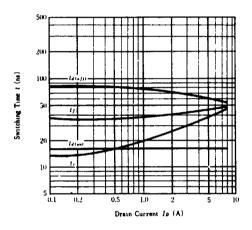
STATIC DRAIN-SOURCE ON STATE RESISTANCE VS. TEMPERATURE



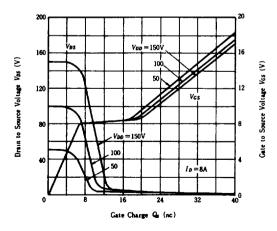
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SWITCHING CHARACTERISTICS

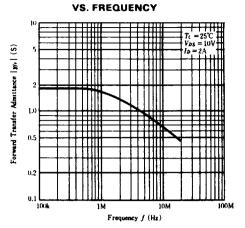


DYNAMIC INPUT CHARACTERISTICS

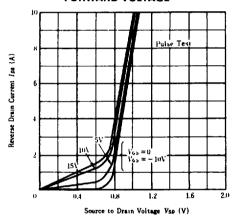


FORWARD TRANSFER ADMITTANCE

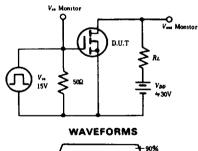
2SK400

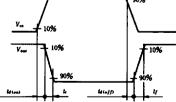


MAXIMUM BODY-DRAIN DIODE FORWARD VOLTAGE



SWITCHING TIME TEST CIRCUIT





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