## Feature

- Operating voltage: $4.0 \mathrm{~V} \sim 5.5 \mathrm{~V}$
- Microsoft 3D Intelli mouse and IBM PS/2 mouse compatible
- Microsoft Windows 2000 and 5-button Wheel mouse compatible
- Z-axis can support two kinds of scroller input divided by 2 or 4 (package option)
- Supports 500 or 1000 DPI for ADNS-5020 (package option)


## General Description

The HT82M35X are designed as 3/5-key 3D PS/2 optical mouse controller. These have serial interface to access the sensor ADNS-5020 or the same compatible

- Serial interface with ADNS-5020
- Auto detect as to which photo sensor is used
- 2 MHz RC oscillator for system frequency with external pull-high resistor ( $121 \mathrm{k} \Omega$ )
- Interface compliant with ADNS-5020
- 16-pin DIP package
series sensor. Refer to the datasheets for detailed register descriptions of the sensors.


## Selection Table

| Part No. | Interface | X/Y-axis Option | Z-axis Option | Package |
| :---: | :---: | :---: | :---: | :---: |
| HT82M35A/HT82M35A-1 | 3/5-key 3D PS/2 optical mouse controller (for Avago ADNS-5020) | Avago Sensor Opto 500 DPI | Divided by 2 | 16 DIP |
| HT82M35B/HT82M35B-1 |  | Avago Sensor Opto 1000 DPI | Divided by 2 |  |
| HT82M35C/HT82M35C-1 |  | Avago Sensor Opto 500 DPI | Divided by 4 |  |
| HT82M35D/HT82M35D-1 |  | Avago Sensor Opto 1000 DPI | Divided by 4 |  |

## Pin Assignment



HT82M35A/HT82M35A-1
HT82M35B/HT82M35B-1
HT82M35C/HT82M35C-1
HT82M35D/HT82M35D-1
-16 DIP-A

Pin Description

| Pin Name | I/O | $\quad$ Description |
| :--- | :---: | :--- |
| RB, RO, LB | I | Right Button: Normal pull-high resistor $(30 \mathrm{k} \Omega)$ <br> Rolling Button: Normal pull-high resistor $(30 \mathrm{k} \Omega)$ <br> Left Button: Normal pull-high resistor $(30 \mathrm{k} \Omega)$ |
| Z2/A, Z1/B | I | "Z" axis input supports three kinds of scroller input |
| Normal pull-high resistor (30k $\Omega)$ |  |  |

## Absolute Maximum Ratings

| Supply Voltage . | . $\mathrm{V}_{\text {SS }}-0.3 \mathrm{~V}$ to $\mathrm{V}_{\mathrm{SS}}+6.0 \mathrm{~V}$ | Storage Temperature ......................... $-50^{\circ} \mathrm{C}$ to $125^{\circ} \mathrm{C}$ |
| :---: | :---: | :---: |
| Input Voltage | . $\mathrm{V}_{\text {SS }}-0.3 \mathrm{~V}$ to $\mathrm{V}_{\text {SS }}+6.0 \mathrm{~V}$ | Operating Temperature........................ $-40^{\circ} \mathrm{C}$ to $85^{\circ} \mathrm{C}$ |

Note: These are stress ratings only. Stresses exceeding the range specified under "Absolute Maximum Ratings" may cause substantial damage to the device. Functional operation of this device at other conditions beyond those listed in the specification is not implied and prolonged exposure to extreme conditions may affect device reliability.

## D.C. Characteristics <br> $\mathrm{Ta}=25^{\circ} \mathrm{C}$

| Symbol | Parameter | Test Conditions |  | Min. | Typ. | Max. | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | V ${ }_{\text {D }}$ | Conditions |  |  |  |  |
| $V_{D D}$ | Operating Voltage | - | $\mathrm{f}_{\mathrm{SYS}}=2 \mathrm{MHz}$ | 4.0 | 5.0 | 5.5 | V |
| $\mathrm{I}_{\mathrm{DD}}$ | Operating Current | 5 V | No load, $\mathrm{f}_{\mathrm{SYS}}=2 \mathrm{MHz}$ | - | 2.5 | 4 | mA |
| $\mathrm{V}_{\text {IL1 }}$ | Input Low Voltage for RB, LB, RO, Z1, Z2, RB1, RB0, SDIO, PS2CK and PS2D | - | - | 0 | - | $0.3 \mathrm{~V}_{\mathrm{DD}}$ | V |
| $\mathrm{V}_{\mathrm{IH} 1}$ | Input High Voltage for RB, LB, RO, Z1, Z2, RB1, RB0, SDIO, PS2CK and PS2D | - | - | $0.7 \mathrm{~V}_{\mathrm{DD}}$ | - | $V_{D D}$ | V |
| $\mathrm{V}_{\text {IL2 }}$ | Input Low Voltage for $\overline{\mathrm{RES}}$ | - | - | 0 | - | $0.4 \mathrm{~V}_{\mathrm{DD}}$ | V |
| $\mathrm{V}_{\mathrm{IH} 2}$ | Input High Voltage for $\overline{\mathrm{RES}}$ | - | - | $0.9 \mathrm{~V}_{\mathrm{DD}}$ | - | $V_{D D}$ | V |
| lOL | I/O Port Sink Current | 5 V | $\mathrm{V}_{\mathrm{OL}}=0.1 \mathrm{~V}_{\mathrm{DD}}$ | 10 | 20 | - | mA |
| IOH | I/O Port Source Current | 5 V | $\mathrm{V}_{\mathrm{OL}}=0.9 \mathrm{~V}_{\mathrm{DD}}$ | -2 | -4 | - | mA |
| $\mathrm{R}_{\mathrm{PH}}$ | Pull-high Resistance for RB, LB, RO, Z1, Z2, RB1, RB0, SDIO, PS2CK and PS2D | 5 V | - | 10 | 30 | 50 | $\mathrm{k} \Omega$ |

## A.C. Characteristics

$\mathrm{Ta}=25^{\circ} \mathrm{C}$

| Symbol | Parameter | Test Conditions |  | Min. | Typ. | Max. | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | VDD | Conditions |  |  |  |  |
| twdTosc | Watchdog Oscillator Period | 5 V | - | 32 | 65 | 130 | $\mu \mathrm{S}$ |
| $\mathrm{t}_{\text {WDT1 }}$ | Watchdog Time-out Period | 5 V | Without WDT prescaler | 8 | 17 | 33 | ms |
| $t_{\text {RES }}$ | External Reset Low Pulse Width | - | - | 1 | - | - | $\mu \mathrm{S}$ |

## Functional Description

## PS/2 Mouse

- PS/2 status byte

Byte 1
bit
7: Reserved
6: 0=Stream Mode, 1=Remote Mode
5: 0=Disabled, 1=Enabled
4: $0=$ Scaling $1: 1,1=$ Scaling $2: 1$
3: $1=$ Wrap Mode, $0=$ Stream or Remote
(different from IBM specs.)
2: 1=Left Button Pressed
1: 1=Middle Button Pressed
0: 1=Right Button Pressed
Byte 2
Bit 0~7 current resolution setting
(Bit 0=LSB)
Byte 3
Bit 0~7 current sampling rate (Bit 0=LSB)

- Standard PS/2 data format

Variable rps, $0,8,1$, bidirectional, synchronous

| Bit No. | $\mathbf{7}$ | $\mathbf{6}$ | $\mathbf{5}$ | $\mathbf{4}$ | $\mathbf{3}$ | $\mathbf{2}$ | $\mathbf{1}$ | $\mathbf{0}$ |
| :--- | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 1st word | YV | XV | YS | XS | $\mathbf{1}$ | MB | RO | LB |
| 2nd word | X 7 | X 6 | X 5 | X 4 | X 3 | X 2 | X 1 | X 0 |
| 3rd word | Y 7 | Y 6 | Y 5 | Y 4 | Y 3 | Y 2 | Y 1 | Y 0 |

- Data format for 3D PS/2

Variable rps, 0, 8, 1, bidirectional, synchronous

| Bit No. | $\mathbf{7}$ | $\mathbf{6}$ | $\mathbf{5}$ | $\mathbf{4}$ | $\mathbf{3}$ | $\mathbf{2}$ | $\mathbf{1}$ | $\mathbf{0}$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 1st word | YV | XV | YS | XS | 1 | MB | RO | LB |
| 2nd word | X 7 | X 6 | X 5 | X 4 | X 3 | X 2 | X 1 | X 0 |
| 3rd word | Y 7 | Y 6 | Y 5 | Y 4 | Y 3 | Y 2 | Y 1 | Y 0 |
| 4th word | Z 7 | Z 6 | Z 5 | Z 4 | Z 3 | Z 2 | Z 1 | Z 0 |

The $\mathrm{x} / \mathrm{y}$ data report is 9 -bit 2's complement
The $z$ data report is 8 -bit 2's complement

- Data format for 5-button Wheel Mouse

| Bit No. | $\mathbf{7}$ | $\mathbf{6}$ | $\mathbf{5}$ | $\mathbf{4}$ | $\mathbf{3}$ | $\mathbf{2}$ | $\mathbf{1}$ | $\mathbf{0}$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 1st word | 0 | 0 | YS | XS | 1 | MB | RO | LB |
| 2nd word | X 7 | X 6 | X 5 | X 4 | X 3 | X 2 | X 1 | X 0 |
| 3rd word | Y 7 | Y 6 | Y 5 | Y 4 | Y 3 | Y 2 | Y 1 | Y 0 |
| 4th word | 0 | 0 | RB 1 | RB 0 | Z 3 | Z 2 | Z 1 | Z 0 |

X- movement towards the right is positive, moving towards the left is negative

Y - upward movement is positive, moving down is negative

Z- rolling towards the user is positive, else negative
Button status: 1=pressed, 0=released

- Mouse mode changes between Standard and 3D PS/2 mode

Sending the commands in the following sequence will set the mouse to 3D PS/2 mode.

| Command | Response From Mouse |
| :--- | :---: |
| F3h | FAh |
| C8h | FAh |
| F3h | FAh |
| $64 h$ | FAh |
| F3h | FAh |
| $50 h$ | FAh |
| F2h | FAh, 03h |

- Mouse mode changes between Standard and Win2K PS/2 mode.

Sending the commands in the following sequence will set the mouse to Win2K PS/2 mode.

| Command | Response From Mouse |
| :--- | :--- |
| F3h | FAh |
| C8h | FAh |
| F3h | FAh |
| C8h | FAh |
| F3h | FAh |
| $50 h$ | FAh |
| F2h | FAh, 04h |

- Any time the PC sends a reset "FFh" command to the mouse, it will reset the mouse to Standard PS/2 mode.
- After power-on reset is initiated, the mouse is set to Standard PS/2 mode.


## Timing Diagrams

Z-Axis Photo-coupler Cross Width


Note: For Z-axis tr, tp, tf $>1 \mathrm{~ms}$

## Z-Axis Counting



## PS/2 Mouse

- Data output

(6)

|  | Timing Parameter | Min./Max. |
| :--- | :--- | :--- |
| T1 | DATA transition to the falling edge of CLK | $5 / 25 \mu \mathrm{sec}$ |
| T2 | Rising edge of CLK to DATA transition | $5 / \mathrm{T} 4-5 \mu \mathrm{sec}$ |
| T3 | Inactive CLK Duration | $30 / 50 \mu \mathrm{sec}$ |
| T4 | Active CLK Duration | $30 / 50 \mu \mathrm{sec}$ |
| T5 | Minimum time to inhibit MOUSE after clock 11 | $>0 \mu \mathrm{sec}$ |
| T6 | Maximum time to inhibit MOUSE after clock 11 to ensure <br> that the MOUSE does not start another transmission | $<50 \mu \mathrm{sec}$ |

- Data input


|  | Timing Parameter | Min./Max. |
| :--- | :--- | :--- |
| T7 | CLK Duration, low | $30 / 50 \mu \mathrm{sec}$ |
| T8 | CLK Duration, high | $30 / 50 \mu \mathrm{sec}$ |
| T9 | Time from low to high CLK transition to time when | $5 / 25 \mu \mathrm{sec}$ |

## Application Circuits

## 2D PS/2 Optical Mouse for ADNS-5020



3D PS/2 Optical Mouse for ADNS-5020


Win2K PS/2 Optical Mouse for ADNS-5020


HT82M35X Z-Axis Optomechanical (This Application Circuit is for Reference Only)


Note: * For resistor value selection, refer to high or low input level of $Z 1$ and $Z 2$ in the D.C. Characteristics table. The recommended value is $6 \mathrm{k} \Omega$.

## Package Information

16-pin DIP ( 300 mil ) Outline Dimensions


Fig1. Full Lead Packages


Fig2. 1/2 Lead Packages

- MS-001d (see fig1)

| Symbol | Dimensions in mil |  |  |
| :---: | :---: | :---: | :---: |
|  | Min. | Nom. | Max. |
| A | 780 | - | 880 |
| B | 240 | - | 280 |
| C | 115 | - | 195 |
| D | 115 | - | 150 |
| E | 14 | - | 22 |
| F | 45 | - | 70 |
| G | - | 100 | - |
| H | 300 | - | 325 |
| I | - | - | 430 |

- MS-001d (see fig2)

| Symbol | Dimensions in mil |  |  |
| :---: | :---: | :---: | :---: |
|  | Min. | Nom. | Max. |
| A | 735 | - | 775 |
| B | 240 | - | 280 |
| C | 115 | - | 195 |
| D | 115 | - | 150 |
| E | 14 | - | 22 |
| F | 45 | - | 70 |
| G | - | 100 | - |
| H | - | - | 325 |
| I | - |  | 430 |

- MO-095a (see fig2)

| Symbol | Dimensions in mil |  |  |
| :---: | :---: | :---: | :---: |
|  | Min. | Nom. | Max. |
| A | 745 | - | 785 |
| B | 275 | - | 295 |
| C | 120 | - | 150 |
| D | 110 | - | 150 |
| E | 14 | - | 22 |
| F | 45 | - | 60 |
| G | - | 100 | - |
| H | 300 | - | 325 |
| I | - | - | 430 |

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