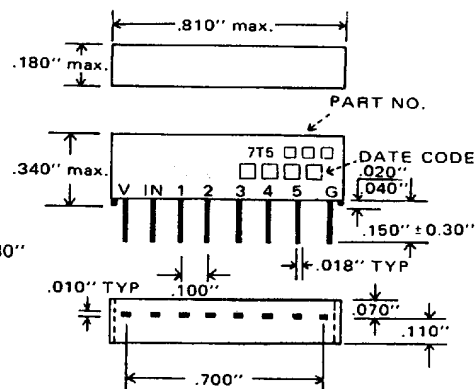


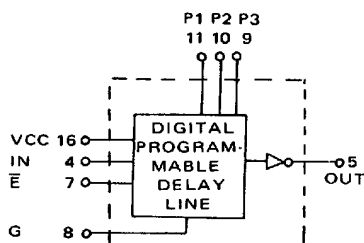
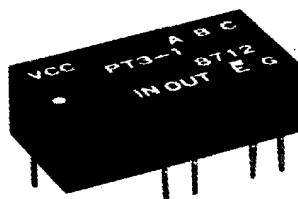
PT	DIL PROGRAMMABLE DELAY LINE
7T	SIP 8 PIN ACTIVE DELAY LINE

7T

-
- The drawing consists of three views of the 74181 ALU:
- Top View:** A rectangle with a width of .800" and a height of .400" max. It features a VCC pin on the left, a PT pin at the top center, and a G pin at the bottom right. There are four square pins along the top edge and four square pins along the bottom edge. Labels include "PART NO." and "DATE CODE".
 - Side View:** A rectangle with a width of .280" max. and a height of .020" and .040". It shows six pins protruding from the bottom. The distance from the bottom edge to the top of the pins is .150 ± .030".
 - Bottom View:** A rectangle with a width of .700" and a height of .150" TYP and .300". It shows eight pins: pins 4, 5, 7, and 8 on the top edge, and pins 16, 11, 10, and 9 on the bottom edge. The pin pitch is .018" TYP, and the distance between the top and bottom rows of pins is .100".



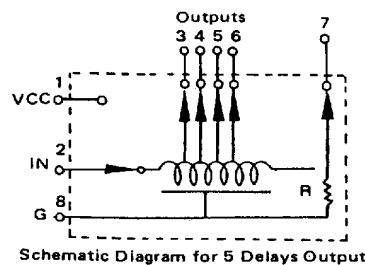
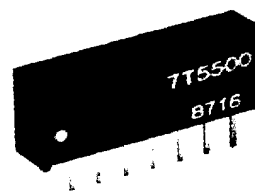
PT



PART NO.	ZERO STEP (ns)	TOTAL (ns)	PER STEP (ns)
PT3-1	7±1	14±2	1±0.5
PT3-2		21±2	2±0.6
PT3-3		28±2	3±0.7
PT3-4		35±2	4±0.8
PT3-5		42±2.1	5±1.0
PT3-6		49±2.4	6±1.2

PART NO.	ZERO STEP (ns)	TOTAL (ns)	PER STEP (ns)
PT3-7	7±2	56±2.8	7±1.4
PT3-8		63±3.1	8±1.6
PT3-9		70±3.5	9±1.8
PT3-10		77±3.8	10±2.0
PT3-11		84±4.2	11±2.0
PT3-12		91±4.5	12±2.0

7T



PART NO.	TOTAL DELAY (ns)	TAP TO TAP (ns)
7T5200	20±2	4±2
7T5250	25±2	5±2
7T5300	30±2	6±2
7T5400	40±2	8±2
7T5500	50±2.5	10±2
7T5600	60±3	12±2
7T5750	75±3.7	15±2

PART NO.	TOTAL DELAY (ns)	TAP TO TAP (ns)
7T5101	100±5	20±2
7T5125	125±6.2	25±2
7T5151	150±7.5	30±2
7T5175	175±8.7	35±2
7T5201	200±10	40±2
7T5251	250±12.5	50±2.5