

EMIF06-VID01C1

6 LINES LOW CAPACITANCE EMI FILTER AND ESD PROTECTION

IPAD™

MAIN APPLICATION

Where EMI filtering in ESD sensitive equipment is required:

- LCD and camera for mobile phones
- Computers and printers
- Communication systems
- MCU board

DESCRIPTION

The EMIF06-VID01C1 is a 6 lines highly integrated array designed to suppress EMI / RFI noise in all systems subjected to electromagnetic interferences.

The EMIF06-VID01C1 Flip-Chip packaging means the package size is equal to the die size. Additionally, this filter includes an ESD protection circuitry which prevents the protected device from destruction when subjected to ESD surges up to 15 kV.

BENEFITS

- High efficiency EMI filtering (-40db @ 900MHz)
- Low line capacitance suitable for high speed data bus
- Low serial resistance for camera impedance adaptation
- Optimized PCB space consuming: 2.92mm x 1.29mm
- Very thin package: 0.69 mm
- High efficiency in ESD suppression on inputs pins (IEC61000-4-2 level 4)
- High reliability offered by monolithic integration
- High reducing of parasitic elements through integration & wafer level packaging

COMPLIES WITH THE FOLLOWING STANDARDS: IEC61000-4-2

Level 4 on input pins 15kV (air discharge) 8kV (contact discharge)

MIL STD 883E - Method 3015-6 Class 3

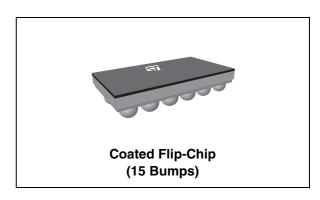


Table 1: Order Code

Part Number	Marking	
EMIF06-VID01C1	GR	

Figure 1: Pin Configuration (ball side)

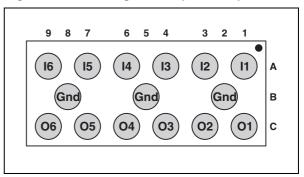
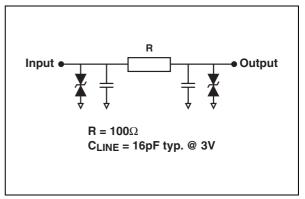


Figure 2: Configuration



TM: IPAD is a trademark of STMicroelectronics.

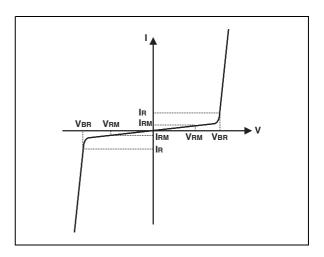
EMIF06-VID01C1

Table 2: Absolute Ratings (limiting values)

Symbol	Parameter and test conditions	Value	Unit
T _j	Maximum junction temperature	125	°C
T _{op}	Operating temperature range	- 40 to + 85	°C
T _{stg}	Storage temperature range	- 55 to + 150	°C

Table 3: Electrical Characteristics $(T_{amb} = 25^{\circ}C)$

\ and			
Symbol	Parameter		
V _{BR}	Breakdown voltage		
I _{RM}	Leakage current @ V _{RM}		
V _{RM}	Stand-off voltage		
R	Series resistance between Input & Output		
C _{line}	Input capacitance per line		



Symbol	Test conditions	Min.	Тур.	Max.	Unit
V _{BR}	I _R = 1mA	6	8	10	V
I _{RM}	V _{RM} = 3V per line			500	nA
R	I = 10mA	80	100	120	Ω
C _{line}	V _R = 3V DC 1MHz V _{OSC} = 30mV		16	19	pF

2/6

Figure 3: S21 (dB) attenuation measurement

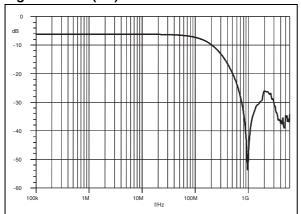


Figure 5: ESD response to IEC61000-4-2 (+15kV air discharge) on one input V(in) and on one output (Vout)

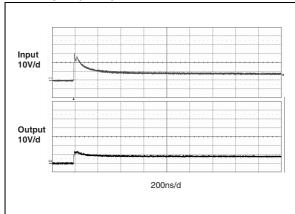


Figure 7: Junction capacitance versus reverse voltage applied (typical values)

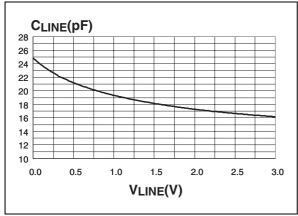


Figure 4: Analog crosstalk measurement

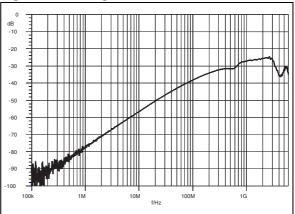


Figure 6: ESD response to IEC61000-4-2 (-15kV air discharge) on one input V(in) and on one output (Vout)

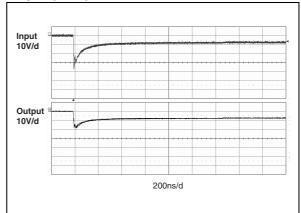


Figure 8: Ordering Information Scheme

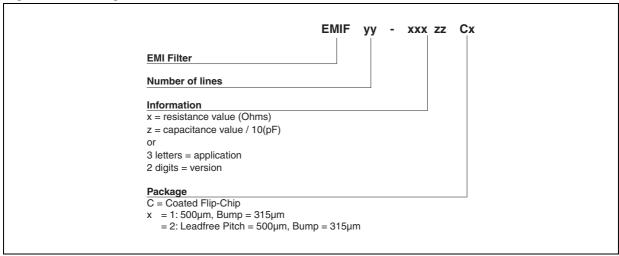


Figure 9: FLIP-CHIP Package Mechanical Data

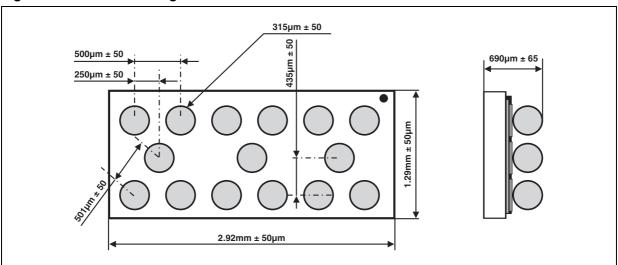


Figure 10: Foot Print Recommendations

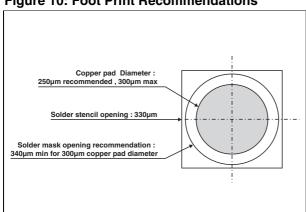
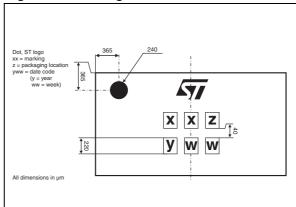


Figure 11: Marking



4/6

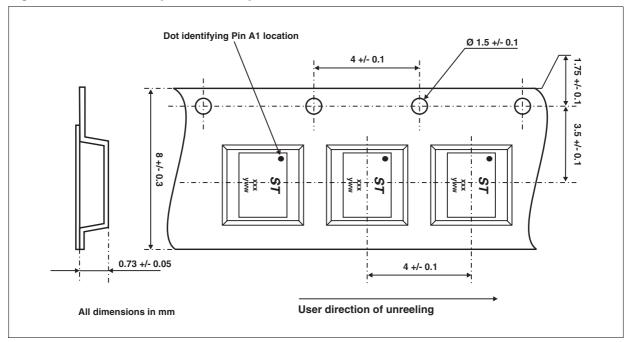


Figure 12: FLIP-CHIP Tape and Reel Specification

Table 4: Ordering Information

Ordering code	Marking	Package	Weight	Base qty	Delivery mode
EMIF06-VID01C1	GR	Flip-Chip	5.9 mg	5000	Tape & reel 7"

Note: More packing informations are available in the application note AN1235: "Flip-Chip: Package description and recommendations for use" AN1751: "EMI Filters: Recommendations and measurements"

Table 5: Revision History

Date	Revision	Description of Changes
15-Feb-2005	1	First issue.

Information furnished is believed to be accurate and reliable. However, STMicroelectronics assumes no responsibility for the consequences of use of such information nor for any infringement of patents or other rights of third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of STMicroelectronics. Specifications mentioned in this publication are subject to change without notice. This publication supersedes and replaces all information previously supplied. STMicroelectronics products are not authorized for use as critical components in life support devices or systems without express written approval of STMicroelectronics.

The ST logo is a registered trademark of STMicroelectronics. All other names are the property of their respective owners

© 2005 STMicroelectronics - All rights reserved

STMicroelectronics group of companies

Australia - Belgium - Brazil - Canada - China - Czech Republic - France - Germany - Hong Kong - India - Israel - Italy - Japan - Malaysia - Malta - Morocco - Singapore - Spain - Sweden - Switzerland - United Kingdom - United States of America www.st.com

