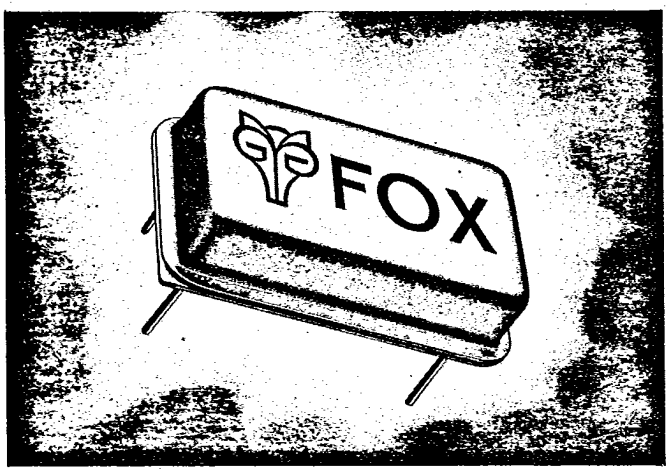
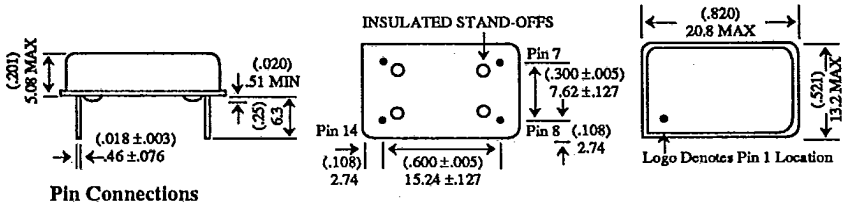
F5C-3 / HCMOS TRI-STATE ENABLE/DISABLE OSCILLATOR

T-50-23



The FOX F5C-3 Clock Oscillator employs a Tri-State buffer for control of the output. Applying a logic '0' voltage level to pin 1 enables the oscillator output and a logic '1' applied to pin 1 disables the output. When disabled, the output goes to the high impedance state. In the high impedance state, the oscillator appears to have been removed from the circuit. The high Z state allows the oscillator to be left in the board when testing the board with Automatic Test Equipment (ATE).

The package is all metal with pin 7 as case ground which provides shielding to help minimize EMI radiation.



Pin Connections

#1 E/D	#8 Output
#7 GND	#14 +5 VDC

Metric dimensions shall govern
All dimensions are in millimeters and parenthetically in inches

FEATURES

- Rugged Resistance Weld
- Low Profile
- Superior Quality
- Stainless Steel Cover
- Tri-State Output allows testing by ATE

F5C-3 SPECIFICATIONS

Frequency Range	.5 MHz - 50 MHz
Frequency Stability *	±0.01%
Operating Temperature Range	-10°C to +70°C
Input Voltage	5 VDC ±10%
Input Current	12 mA (TYP) @ 25 MHz, 50 mA (MAX)
Symmetry @ 2.5 VDC	40/60% (MAX)
Rise/Fall Time	5 nS (MAX) (0.5 - 2.4 VDC) 10 nS (MAX) (0.5 - 4.5 VDC)
Logic '0' - VOL	0.5 V (MAX)
Logic '1' - VOH	4.5 V (MIN)
Enable Input	Enable = Logic '0' 0.8 V (MAX) Disable = Logic '1' 2.0 V (MIN)
Output Current	IOL = 10 mA (MIN) IOH = - .4 mA (MAX)
Output Load	TTL Load: 6 TTL Gates MOS Load: 15 pF (TYP)
Shock	1000 G's, 0.35 mS, 1/2 Sine Wave, 3 Shocks each plane
Vibration	10-55 Hz, 0.060" D.A., 55-2000 Hz, 35 G's, Duration Time 12 Hrs
Humidity	85% Relative Humidity, 85°C, 250 Hrs
Hermetic Seal	Leak Rate less than 2 x 10^{-8} Atmos. CC/sec of Helium

See page 70 for Test Circuits.

* Inclusive of 25°C tolerance, operating temperature range, input voltage change, load change, aging, shock, and vibration.
All specifications subject to change without notice.

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