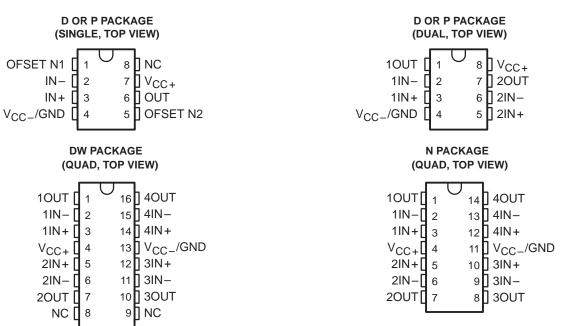
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- Wide Gain-Bandwidth Product . . . 4.5 MHz
- High Slew Rate . . . 13 V/μs
- Fast Settling Time . . . 1.1 μs to 0.1%
- Wide-Range Single-Supply Operation 4 V to 44 V
- Wide Input Common-Mode Range Includes Ground (V_{CC})
- Low Total Harmonic Distortion . . . 0.02%



- Large Output Voltage Swing
 -14.7 V to 14 V (With ±15-V Supplies)
- Large Capacitance Drive Capability 10,000 pF
- Excellent Phase Margin . . . 60°
- Excellent Gain Margin . . . 12 dB
- Output Short-Circuit Protection



NC - No internal connection

AVAILABLE OPTIONS

		PACKAGE							
TA	COMPLEXITY	PLAS [*]	TIC DIP	SMALL OUTLINE					
'A	COMITECATI	STANDARD GRADE	PRIME GRADE	STANDARD GRADE	PRIME GRADE				
0°C	Single	TL34071P	TL34071AP	TL34071D	TL34071AD				
to	Dual	TL34072P	TL34072AP	TL34072D	TL34072AD				
70°C	Quad	TL34074N	TL34074AN	TL34074DW	TL34074ADW				
-40°C	Single	TL33071P	TL33071AP	TL33071D	TL33071AD				
to	Dual	TL33072P	TL33072AP	TL33072D	TL33072AD				
105°C	Quad	TL33074N	TL33074AN	TL33074DW	TL33074ADW				
–55°C	Single	TL35071P	TL35071AP	TL35071D	TL35071AD				
to	Dual	TL35072P	TL35072AP	TL35072D	TL35072AD				
125°C	Quad	TL35074N	TL35074AN	TL35074DW	TL35074ADW				

D and DW packages are available taped and reeled. Add R suffix to device type (e.g., TL34071ADR).



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.



TL3x071, TL3x071A, TL3x072, TL3x072A, TL3x074, TL3x074A HIGH-SLEW-RATE, SINGLE-SUPPLY OPERATIONAL AMPLIFIERS

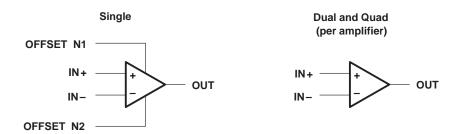
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description

Quality, low cost, bipolar fabrication with innovative design concepts are employed for the TL33071/2/4, TL34071/2/4, and TL35071/2/4 series of monolithic operational amplifiers. This series of operational amplifiers offers 4.5 MHz of gain bandwidth product, 13 V/ μ s slew rate, and fast settling time without the use of JFET device technology. Although this series can be operated from split supplies, it is particularly suited for single-supply operation since the common-mode input voltage range includes ground potential (V_{CC}). With a Darlington transistor input stage, this series exhibits high input resistance, low input offset voltage, and high gain. The all-npn output stage, characterized by no dead-band crossover distortion and large output voltage swing, provides high-capacitance drive capability, excellent phase and gain margins, low open-loop high-frequency output impedance, and symmetrical source/sink ac frequency response.

The TL34071/2/4 devices are avaliable in standard or prime performance (A-suffix) grades and are specified over the commercial (0°C to 70°C) temperature range. The TL33071/2/4 devices are available in standard or prime performance (A-suffix) grades and are specified over industrial/vehicular (–40°C to 105°C) temperature range. The TL35071/2/4 devices are available in standard or prime performance (A-suffix) grades and are specified over the military (–55°C to 125°C) temperature range. These low-cost amplifiers are available in single, dual, and quad configurations and are pin compatible with the MC33071/2/4, MC34071/2/4, and MC35071/2/4 series of amplifiers. Packaging options include standard plastic DIP and SO packages.

symbol



TL3x071, TL3x071A, TL3x072, TL3x072A, TL3x074, TL3x074A HIGH-SLEW-RATE, SINGLE-SUPPLY OPERATIONAL AMPLIFIERS

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absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

Supply voltage, V _{CC+} (see Note 1)	
Supply voltage, V _{CC}	22 V
Differential input voltage, V _{ID} (see Note 2)	±44 V
Input voltage, V _I (any input)	V _{CC±}
Input current, I _I (each input)	±1 mA
Output current, I _O	±80 mA
Total current into V _{CC+}	
Total current out of V _{CC}	80 mA
Duration of short-circuit current at (or below) 25°C (see Note 3)	
Continuous total power dissipation	See Dissipation Rating Table
Operating free-air temperature range, T _A : TL3307x	–40°C to 105°C
TL3407x	0°C to 70°C
TL3507x	–55°C to 125°C
Storage temperature range, T _{stq}	–65°C to 150°C
Lead temperature 1.6 mm (1/16 inch) from case for 10 seconds: D, DW, N, or	

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

- NOTES: 1. All voltage values, except differential voltages, are with respect to the midpoint between V_{CC+} and V_{CC-}.
 - 2. Differential voltages are at the noninverting input with respect to the inverting input. Excessive current flows if input is brought below V_{CC}_-0.3 V.
 - 3. The output can be shorted to either supply. Temperature and/or supply voltages must be limited to ensure that the maximum dissipation rating is not exceeded.

DISSIPATION RATING TABLE

PACKAGE	$\begin{aligned} & \textbf{T}_{\pmb{A}} \leq \textbf{25}^{\circ}\textbf{C} \\ & \textbf{POWER RATING} \end{aligned}$	DERATING FACTOR ABOVE T _A = 25°C	T _A = 70°C POWER RATING	T _A = 105°C POWER RATING	T _A = 125°C POWER RATING
D	725 mW	5.8 mW/°C	464 mW	261 mW	145 mW
DW	1025 mW	8.2 mW/°C	656 mW	369 mW	205 mW
N	1150 mW	9.2 mW/°C	736 mW	414 mW	230 mW
Р	1000 mW	8.0 mW/°C	640 mW	360 mW	200 mW

recommended operating conditions

		TL3307x		TL3407x		TL3507x		UNIT	
		MIN	MAX	MIN	MAX	MIN	MAX	UNIT	
Supply voltage, V _{CC±}			±22	±2	±22	±2	±22	V	
Common-mode input voltage, V _{IC}	V _{CC} = 5 V	0	2.7	0	2.9	0	2.7	V	
	V _{CC±} = ±15 V	-15	12.7	-15	12.9	-15	12.7	V	
Operating free-air temperature, T _A			105	0	70	-55	125	°C	



TL3x071, TL3x071A, TL3x072, TL3x072A, TL3x074, TL3x074A HIGH-SLEW-RATE, SINGLE-SUPPLY OPERATIONAL AMPLIFIERS

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electrical characteristics at specified free-air temperature, $V_{CC\pm}$ = ± 15 V (unless otherwise noted)

DADAMETED		TEGT GOLIDITIONS			Т	TL3x07xA			TL3x07x			
	PARAMETER	TEST CONDITIONS		T _A †	MIN	TYP‡	MAX	MIN	TYP‡	MAX	UNIT	
			V _{CC} = 5 V	25°C		0.5	3		1.5	5		
۷ıO	V _{IO} Input offset voltage		V 145 V	25°C		0.5	3		1.0	5	mV	
			V _{CC} = ±15 V	Full range			5			7		
αVIO	Temperature coefficient of input offset voltage	V _{IC} = 0,	V _{CC} = ±15 V	Full range		10			10		μV/°C	
li o	Input offset current	$V_O = 0$, $R_S = 50 \Omega$	V00 - +15 V	25°C		7	100		7	100	nA	
110	input onset current		$V_{CC} = \pm 15 \text{ V}$	Full range			250			250	IIA	
			V _{CC} = 5 V	25°C		-0.8	-2		-0.8	-2		
	Input bias current		VCC = 3 V	Full range			-2.3			-2.3	μΑ	
ΙΙΒ	input bias current		V _{CC} = ±15 V	25°C		-0.7	-1.5		-0.7	-1.5	μΛ	
			VCC = ± 13 V	Full range			-1.8			-1.8		
V	VICR Common-mode input voltage range		R _S = 50 Ω		-15 to 13.2			-15 to 13.2			V	
VICR					-15 to 12.8			-15 to 12.8				
		V _{CC+} = 5 \ R _L = 2 kΩ	$V_{CC} = 0$	25°C	3.7	4		3.7	4			
VOH	V _{OH} High-level output voltage		$R_L = 10 \text{ k}\Omega$		13.6	14		13.6	14		V	
		$R_L = 2 k\Omega$		Full range	13.4			13.4				
		$V_{CC+} = 5 \text{ V}, V_{CC-} = 0,$ $R_L = 2 \text{ k}\Omega$ $R_L = 10 \text{ k}\Omega$		25°C		0.1	0.3		0.1	0.3	,,	
VOL	Low-level output voltage			25°C		-14.7	-14.3		-14.7	-14.3	V	
		$R_L = 2 k\Omega$	R _L = 2 kΩ				-13.5			-13.5		
AVD	Large-signal differential	Vo = +10 V	$^{\prime}$, R _L = 2 k Ω	25°C	50	100		25	100		V/mV	
/\VD	voltage amplification	VO = ±10 V	, 11 - 2 132	Full range	25			20			V/111 V	
los	Short-circuit output current	Source: V _{ID} = 1 V, V _O = 0	25°C	-10	-30		-10	-30		mA		
105	Chort should sulput surrent	Sink: V _{ID} =	$-1 \text{ V}, \text{ V}_{O} = 0$	20 0	20	30		20	30		1117 (
CMRR	Common-mode rejection ratio	$V_{IC} = V_{ICR}$ min, RS = 50 Ω		25°C	80	97		70	97		dB	
ksvr	Supply-voltage rejection ratio ($\Delta V_{CC\pm}/\Delta V_{IO}$)	$V_{CC\pm} = \pm 1$ to $\pm 16.5 \text{ V}$,	$R_S = 100 Ω$	25°C	80	97		70	97		dB	
		\/o = 0	No Lood	25°C		3.5	4.5		3.5	4.5		
loc	Supply current	$V_O = 0$,	No Load	Full range			4.7			4.7		
ICC	(per channel)	V _{CC+} = 5 \	/, V _{CC} = 0,	25°C		3.4	4.4		3.4	4.4	mA	
		$V_{O} = 0$,	No Load	Full range			4.6			4.6		

[†] Full range is 0°C to 70°C for the TL3407x devices, and -40°C to 105°C for the TL3307x devices, and -55°C to 125°C for the TL3507x devices. ‡ All typical values are at $T_A = 25$ °C.



TL3x071, TL3x071A, TL3x072, TL3x072A, TL3x074, TL3x074A HIGH-SLEW-RATE, SINGLE-SUPPLY OPERATIONAL AMPLIFIERS

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operating characteristics, $V_{CC\pm}$ = ± 15 V, T_A = $25^{\circ}C$

PARAMETER		TEST CONDITI	TL3x07xA			TL3x07x			UNIT	
		TEST CONDITI	TEST CONDITIONS			MAX	MIN	TYP	MAX	UNII
SR+	Positive slew rate	$V_{I} = -10 \text{ V to } 10 \text{ V},$	$A_V = 1$	8	10		8	10		V/μs
SR-	Negative slew rate	$R_L = 2 k\Omega$	$A_V = -1$		13			13		ν/μ5
	Settling time	A _{VD} = -1, 10-V step	To 0.1%		1.1			1.1		
t _S	Setting time	AVD = -1, 10-v step	To 0.01%		2.2			2.2		μs
٧n	Equivalent input noise voltage	$f = 1 \text{ kHz}, R_S$	= 100 Ω	32			32		nV/√Hz	
In	Equivalent input noise current	f = 1 kHz			0.22			0.22		pA/√Hz
THD	Total harmonic distortion	$V_O = 2 V \text{ to } 20 \text{ V}, R_L = 2 \text{ k}\Omega,$ $A_{VD} = 10, f = 10 \text{ kHz}$		0.02		0.02			%	
טחו	Total Harmonic distortion			0.02					/0	
GBW	Gain-bandwidth product	f=100 kHz		3.5	4.5		3.5	4.5		MHz
BW	Power bandwidth		(PP) = 20 V, 0 = 5.0%		200			200		kHz
,	Dhace mergin	$R_L = 2 k\Omega$, $C_L =$	= 0		60°			60°		
φm	Phase margin	$R_L = 2 k\Omega$, $C_L =$	= 300 pF	40°		40°				
	Cain marain	$R_L = 2 k\Omega$, $C_L =$	= 0		12			12		dB
	Gain margin	$R_L = 2 k\Omega$, $C_L = 300 pF$		4		4			иь	
rį	Differential input resistance	VIC = 0			150			150		MΩ
Ci	Input capacitance	VIC = 0			2.5			2.5		pF
	Channel separation	f = 10 kHZ			120			120		dB
z _O	Open-loop output impedance	f = 1 MHZ	·		30			30		Ω



PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	Eco Plan ⁽²⁾	Lead/Ball Finish	MSL Peak Temp ⁽³⁾
TL33071AP	NRND	PDIP	Р	8		Pb-Free (RoHS)	CU NIPDAU	Level-NC-NC-NC
TL33071D	NRND	SOIC	D	8		Pb-Free (RoHS)	CU NIPDAU	Level-2-260C-1YEAR/ Level-1-220C-UNLIM
TL33071P	NRND	PDIP	Р	8		Pb-Free (RoHS)	CU NIPDAU	Level-NC-NC-NC
TL33072AD	OBSOLETE	SOIC	D	8		None	Call TI	Call TI
TL33072ADR	OBSOLETE	SOIC	D	8		None	Call TI	Call TI
TL33072AP	OBSOLETE	PDIP	Р	8		None	Call TI	Call TI
TL33072DR	OBSOLETE	SOIC	D	8		None	Call TI	Call TI
TL33072P	OBSOLETE	PDIP	Р	8		None	Call TI	Call TI
TL33074ADW	OBSOLETE	SOIC	DW	16		None	Call TI	Call TI
TL33074ADWR	OBSOLETE	SOIC	DW	16		None	Call TI	Call TI
TL33074AN	NRND	PDIP	N	14		None	Call TI	Call TI
TL33074DW	NRND	SOIC	DW	16		None	Call TI	Call TI
TL33074DWR	OBSOLETE	SOIC	DW	16		None	Call TI	Call TI
TL33074N	NRND	PDIP	N	14		None	Call TI	Call TI
TL34071AP	OBSOLETE	PDIP	Р	8		Pb-Free (RoHS)	CU NIPDAU	Level-NC-NC-NC
TL34071D	OBSOLETE	SOIC	D	8		None	Call TI	Call TI
TL34071DR	OBSOLETE	SOIC	D	8		None	Call TI	Call TI
TL34071P	OBSOLETE	PDIP	Р	8		Pb-Free (RoHS)	CU NIPDAU	Level-NC-NC-NC
TL34072AD	OBSOLETE	SOIC	D	8		None	Call TI	Call TI
TL34072ADR	OBSOLETE	SOIC	D	8		None	Call TI	Call TI
TL34072AP	OBSOLETE	PDIP	Р	8		None	Call TI	Call TI
TL34072D	OBSOLETE	SOIC	D	8		None	Call TI	Call TI
TL34072DR	OBSOLETE	SOIC	D	8		None	Call TI	Call TI
TL34072P	OBSOLETE	PDIP	Р	8		None	Call TI	Call TI
TL34074ADW	OBSOLETE	SOIC	DW	16		None	Call TI	Call TI
TL34074ADWR	OBSOLETE	SOIC	DW	16		None	Call TI	Call TI
TL34074AN	OBSOLETE	PDIP	N	14		Pb-Free (RoHS)	CU NIPDAU	Level-NC-NC-NC
TL34074DW	OBSOLETE	SOIC	DW	16		None	Call TI	Call TI
TL34074DWR	OBSOLETE	SOIC	DW	16		None	Call TI	Call TI
TL34074N	OBSOLETE	PDIP	N	14		Pb-Free (RoHS)	CU NIPDAU	Level-NC-NC-NC
TL35072P	ACTIVE	PDIP	Р	8	50	Pb-Free (RoHS)	CU NIPDAU	Level-NC-NC-NC
TL35072PE4	ACTIVE	PDIP	Р	8	50	None	Call TI	Call TI
TL35074ADW	OBSOLETE	SOIC	DW	16		None	Call TI	Call TI
TL35074ADWR	OBSOLETE	SOIC	DW	16		None	Call TI	Call TI
TL35074AN	OBSOLETE	PDIP	N	14		Pb-Free (RoHS)	CU NIPDAU	Level-NC-NC-NC
TL35074DW	OBSOLETE	SOIC	DW	16		None	Call TI	Call TI



PACKAGE OPTION ADDENDUM

11-Mar-2005

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins Package Qty	Eco Plan ⁽²⁾	Lead/Ball Finish	MSL Peak Temp ⁽³⁾
TL35074DWR	OBSOLETE	SOIC	DW	16	None	Call TI	Call TI
TL35074N	OBSOLETE	PDIP	N	14	Pb-Free (RoHS)	CU NIPDAU	Level-NC-NC-NC

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - May not be currently available - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

None: Not yet available Lead (Pb-Free).

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(3) MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDECindustry standard classifications, and peak solder temperature.

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Mailing Address: Texas Instruments

Post Office Box 655303 Dallas, Texas 75265

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