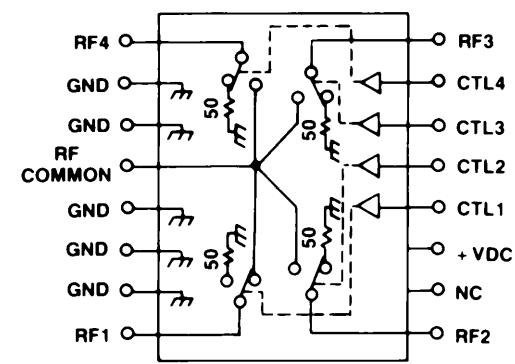


Truth Table

Control Input				Condition of Switch			
"1" = Logic High				RF Common To Each RF PORT			
CTL1	CTL2	CTL3	CTL4	RF1	RF2	RF3	RF4
1	0	0	0	ON	OFF	OFF	OFF
0	1	0	0	OFF	ON	OFF	OFF
0	0	1	0	OFF	OFF	ON	OFF
0	0	0	1	OFF	OFF	OFF	ON

Schematic



Typical Performance

