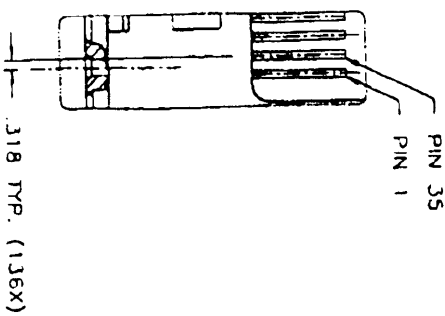


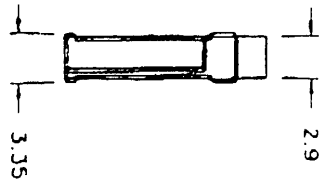
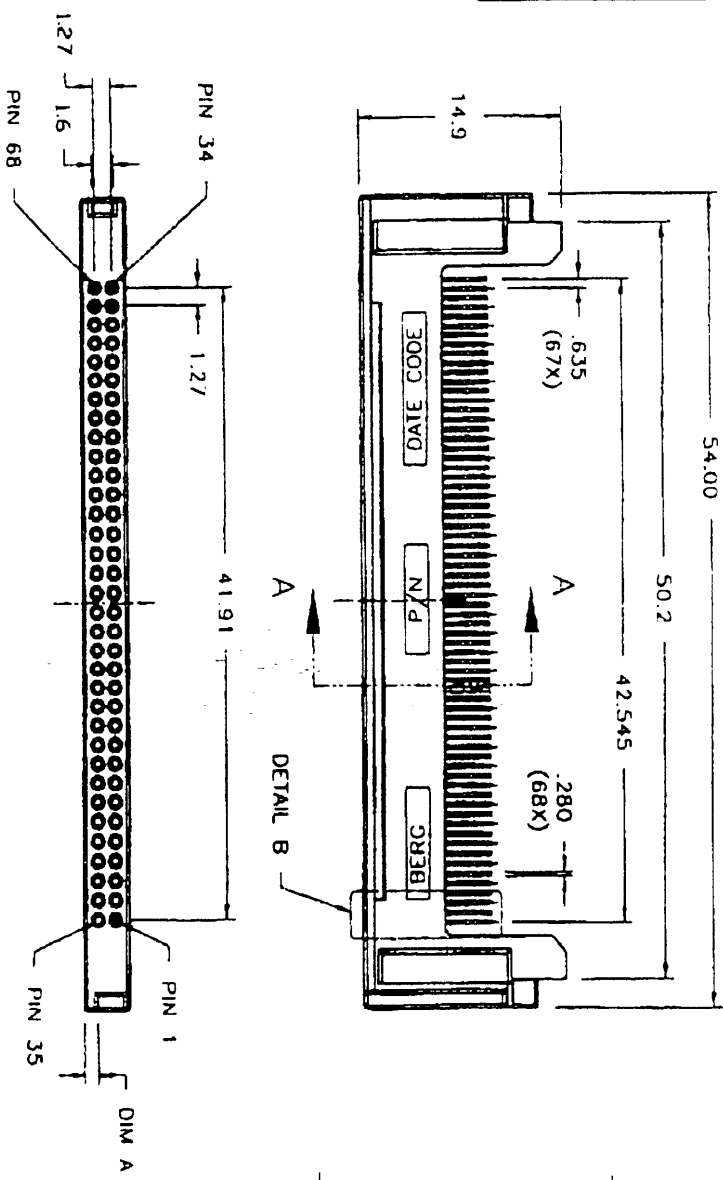
PRODUCT NUMBER	VOLTAGE KEY	DIM A	BOARD OFFSET DIM C	PACKAGING
72267-001	5.0V	1.0	0.3	TRAY-STANDARD PACKAGE 1000
72267-011	3.3V	2.1	0.3	
72267-002	5.0V	1.0	0.6	
72267-012	3.3V	2.1	0.6	
72267-003	5.0V	1.0	0.0	TAPE AND REEL
72267-013	3.3V	2.1	0.0	
72267-001T	5.0V	1.0	0.3	
72267-011T	3.3V	2.1	0.3	
72267-002T	5.0V	1.0	0.6	TRAY-STANDARD PACKAGE 200
72267-012T	3.3V	2.1	0.6	
72267-003S	5.0V	1.0	0.0	
72267-013S	3.3V	2.1	0.0	
72267-002TL	5.0V	1.0	0.6	TAPE AND REEL



DETAIL B
SCALE 4/1

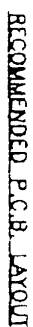
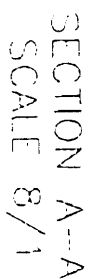
NOTE:
1. MATERIAL

CONTACT: BeCu 0.061
HOUSING: LCP UC94V-0 (COLOR: BLACK)
FINISH
CONTACT: ALL OVER--- Ni UNDER 0.5um THK.
SELECTIVE--- Au 0.1um THK.
SOLDER TAIL--- TIN LEAD 2.5um THK.
OVER Pd-Ni 0.5um THK.



MATERIAL CODE				TOLERANCES UNLESS OTHERWISE SPECIFIED				CUSTOMER COPY		BERG ELECTRONICS	
REV	ECN NO	DATE	DESCRIPTION	LINE NO	XX ± 1.3	XX ± 0.51	XX ± 0.27	PROJECTION	MIN	PRODUCT FAMILY	CODE
D	V61823	RDS	12/17/96							PCMCIA RECEPTACLE	
E	V70189	RHC	2/16/97							V2 68PIN SMTL ASSY	
F	V70461	RHC	4/25/97	angles							
G	V71007	RHC	7/25/97	dr							
H	V71695	RHC	12/18/97	eng							
J	V81211	RHC	6/16/98	chn							
K	V90056	CCC	1/18/99	opnd							
Sheet				Revision	K	K					

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part code		tolerances unless otherwise specified		CUSTOMER COPY		IBERG ELECTRONICS	
ly	ecn no	dr	date	.X ± .J .XX ± .13 .XXX ± .051 0 ± .Z		title PCMCIA RECEPTACLE V2 68PIN SWILL ASSY	
K				.XX ± .13 .XXX ± .051 0 ± .Z		product family PCMCIA	
				.XX ± .13 .XXX ± .051 0 ± .Z		size dwg no 72267	
				.XX ± .13 .XXX ± .051 0 ± .Z		code 2 0 2	
sheet	revision						
inset	sheet						