

**SERIES 631 14 PIN DUAL H-CMOS DIGITAL DELAY MODULES**

- 14 Pin DIP Auto-Insertable
- Two Independent Equal Delay Units Per Package
- H-Cmos Logic

**Specifications:**

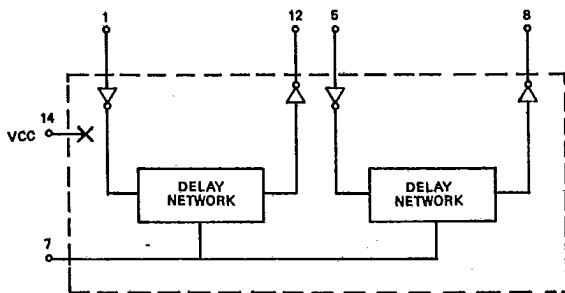
- Supply Voltage : 4.75 to 5.25VDC
- Logic 1 Input Voltage : 3.2v
- Logic 0 Input Voltage : 0.9v
- Logic 1 Output Voltage : 4.5v
- Logic 0 Output Voltage : 0.1v
- Input Current : 1μA Max
- Supply Current : ICCH = 5mA, ICCL = 20μA
- Fan-Out : 10 LSTTL Loads Min
- Rise-Time : 8NS TYP. (measured from 0.8v to 2.0v)
- Operating Temp Range : 0°C to 70°C
- Temp Coefficient : 300 PPM/°C



Electrical Specifications at 25°C (Measured with no Loads on Taps)

Part Number	Total Delay Nanosecond (1)
631-5	5±1
631-6	6±1
631-8	8±2
631-10	10±2
631-12	12±2
631-16	16±2
631-25	25±2
631-30	30±2
631-35	35±2
631-40	40±2
631-45	45±2.2
631-50	50±2.5
631-60	60±3
631-75	75±3.5
631-100	100±5.0
631-125	125±5.5
631-150	150±6.0
631-175	175±8.7
631-200	200±10
631-250	250±12.5

Note: (1) Measured at 1.5v level leading edge.



SCHEMATIC

