

Low/Ultra-Low Offset Voltage

| Type | Description | V _{OS} (μ V Max) | Δ V _{OS} / Δ T (μ V/ $^{\circ}$ C) (Max) | Δ V _{OS} / Δ t (nV/month) (Typ) | I _{BIAS} (pA Max) | GBW (MHz) | V _{Supply} (V Max) | Temperature Range ($^{\circ}$ C) | |
|---------|-------------|-----------------------------------|--|--|-------------------------------|--------------|--------------------------------|---|--------------|
| SINGLES | ICL7650C | CMOS, Chopper-stabilized | ± 8 | ± 0.02 | 100 | 20 | 2.0 | ± 9 | 0 to +70 & |
| | ICL7650I | CMOS, Chopper-stabilized | ± 10 | ± 0.02 | 100 | 50 | 2.0 | ± 9 | -25 to +85 & |
| | ICL7650M | CMOS, Chopper-stabilized | ± 20 | ± 0.03 | 100 | 500 | 2.0 | ± 9 | -55 to +125 |
| | ICL7652C | Low-noise 7650C | ± 7 | ± 0.01 | 100 | 30 | 0.5 | ± 9 | 0 to 70 & |
| | ICL7652I | Low-noise 7650I | ± 10 | ± 0.02 | 100 | 30 | 0.5 | ± 9 | -25 to +85 & |
| | ICL7652M | Low-noise 7650M | ± 50 | ± 0.1 | 100 | 500 | 0.5 | ± 9 | -55 to +125 |
| | ICL420 | High Voltage Chopper | ± 5 | ± 0.05 | 100 | 30 | 0.5 | ± 16.5 | 0 to +70 & |
| | ICL421 | High Voltage Chopper | ± 5 | ± 0.05 | 100 | 30 | 0.5 | ± 16.5 | -40 to +85 & |
| | ICL422 | High Voltage, Low Power | ± 5 | ± 0.05 | 100 | 30 | 0.4 | ± 16.5 | -55 to +125 |
| | ICL423 | High Voltage, Low Power | ± 5 | ± 0.05 | 100 | 30 | 0.4 | ± 16.5 | |

Low Input Bias Current

| Type | Description | I _{BIAS} (pA Max) | I _{OS} (pA Typ) | V _{OS} (mV Max) | GBW (MHz) | Compensation | V _{Supply} (V Max) | Temperature Range ($^{\circ}$ C) | |
|---------|-------------|---------------------------------|-----------------------------|-----------------------------|--------------|--------------|--------------------------------|---|---------------------------|
| SINGLES | ICL7611 | CMOS, Selectable I _Q | 50 | 0.5 | 2, 5, 15 | 1.4 | INT | ± 9 | 0 to +70 & |
| | ICL7612 | CMOS, Extended CMVR | 50 | 0.5 | 2, 5, 15 | 1.4 | INT | ± 9 | -55 to +125 |
| | ICL8007M | JFET Input Op-Amp | 20 | 0.5 | 20 | 1.0 | INT | ± 18 | -55 to +125 |
| | ICL8007AM | JFET Input, Low Bias | 4.0 | 0.2 | 30 | 1.0 | INT | ± 18 | -55 to +125 |
| | ICL8007C | JFET Input Op-Amp | 50 | 0.5 | 50 | 1.0 | INT | ± 18 | 0 to +70 |
| | ICL8007AC | JFET Input, Low Bias | 4.0 | 0.2 | 30 | 1.0 | INT | ± 18 | 0 to +70 |
| | ICH8500A | PMOS Input, Low Bias | 0.01 | - | 50 | 0.7 | INT | ± 18 | -25 to +85 |
| | ICH8500 | PMOS Input | 0.1 | - | 50 | 0.7 | - | ± 18 | - |
| DUALS | ICL7621 | CMOS, Fixed I _Q | 50 | 0.5 | 2, 5, 15 | 0.48 | INT | ± 9 | 0 to +70 & -55 to +125 |
| | ICL8043M | JFET Input Op-Amp | 20 | 0.5 | 20 | 1.0 | INT | ± 18 | -55 to +125 |
| | ICL8043C | JFET Input Op-Amp | 50 | 0.5 | 50 | 1.0 | INT | ± 18 | 0 to +70 |
| TRIPLES | ICL7631 | CMOS, Selectable I _Q | 50 | 0.5 | 5, 10, 20 | 1.4 | INT | ± 9 | 0 to +70 & -55 to +125 |
| QUADS | ICL7641 | CMOS, Fixed I _Q | 50 | 0.5 | 5, 10, 20 | 1.4 | INT | ± 9 | 0 to +70 & |
| | ICL7642 | CMOS, Fixed I _Q | 50 | 0.5 | 5, 10, 20 | 0.044 | INT | ± 9 | 0-55 to +125 |